makefile

```
 1  #
 2  #  USAGE:
 3  #     make          ... to build the program
 4  #     make test     ... to run the default test case
 5  #
 6
 7  include make.def
 8
 9  EXES= vadd$(EXE)
10
11  JAC_OBJS  = jac_solv.$(OBJ) mm_utils.$(OBJ)
12
13  all: $(EXES)
14
15  vadd$(EXE): vadd.$(OBJ)
16      $(CLINKER) $(OPTFLAGS) -o vadd$(EXE) vadd.$(OBJ) $(LIBS)
17
18  test: $(EXES)
19      for i in $(EXES); do \
20              $(PRE)$$i; \
21          done
22
23  clean:
24      $(RM) $(EXES) *.$(OBJ) *.ptx *.cub
25
26  .SUFFIXES:
27  .SUFFIXES: .c .cpp .$(OBJ)
28
29  .c.$(OBJ):
30      $(CC) $(CFLAGS) -c $<
31
32  .cpp.$(OBJ):
33      $(CC) $(CFLAGS) -c $<
34
```