Exercises

For the first three questions, consider a stopwatch as described here. The state machine below presents the desired behavior.

Diagram

Description automatically generated

It has the following hardware.

* Buttons for start, stop, and clear functions.
  + Pressing Start starts the stopwatch running. If pressed multiple times, stopwatch continues running without resetting elapsed time.
  + Pressing Stop stops the stopwatch from counting.
  + Pressing Clear zeroes out the elapsed time if the stopwatch is not running. If it is running, the clear button is ignored.
* A timer which triggers an interrupt every 1ms. The timer drives a counter which counts milliseconds since system start-up and can be read as elapsed\_time\_counter.
* A display to show elapsed time with 1ms resolution. The display must be updated 10 times per second.

1. Design pseudocode for the software using event-triggered scheduling with interrupts. Assume that each button can generate an interrupt.

* Use a variable called state to indicate whether the stopwatch is stopped or running
* Use a variable called elapsed\_time to track how much time has elapsed since the start button was pressed.
* Use a variable called display\_delay to track how many milliseconds remain until the display needs to be updated again.

1. Now design pseudocode for the software using a **static scheduler without using any interrupts**. Assume that the timer updates a hardware register called elapsed\_time\_register every millisecond.

* Use a variable called state to indicate whether the stopwatch is stopped or running
* Use a variable called start\_time to record when the start button was pressed.
* Use a variable called stop\_time to record when the stop button was pressed.
* Use a variable called next\_display\_update to indicate when the display needs to be updated next.

1. Create a flowchart to represent your solution to the previous question.
2. Consider a system with the following tasks. We wish to **minimize** the response time for task C. For each type of scheduler, describe the sequence of processing activities which will lead to the minimum and the maximum response times for task C. Assume that each task is ready to run and there are no further task releases.

|  |  |
| --- | --- |
| Task | Duration |
| A | 3 |
| B | 1 |
| C | 2 |

* 1. Static, non-preemptive scheduler
  2. Dynamic, non-preemptive scheduler
  3. Dynamic, preemptive scheduler

1. Design a state chart showing the operation of a calculator with digits 0 through 9, four functions (+ - \* /), a clear button, and an equals button. Assume that there are two valid sequences of operations:
2. <operand> <operator> <operand> <equals>: this performs the operation specified on the two operands in order entered and places the result in the accumulator.
3. <operand> <equals>: this performs the operation specified on the accumulator and the operand in order entered and places the result in the accumulator.

There are various possible state charts, based on one’s assumptions about calculator functionality.