Exercises

1. If an interrupt is generated at 19199 Hz and the bus clock frequency is 24 MHz, what is the max value of the counter?
2. If the max value of the counter is 0x00394391 and the bus clock frequency is 24 MHz, what is the interrupt period?
3. Specify how the interrupt timer control registers must be configured so that an interrupt is generated with a frequency of 315 Hz, a clock speed of 24 MHz. What is the actual frequency generated?