# Kelly Cache Networks

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*Abstract*—We study networks of M/M/1 queues in which nodes act as caches that store objects. Exogenous requests for objects are routed towards nodes that store them; as a result, object traffic in the network is determined not only by demand but, crucially, by where objects are cached. We determine how to place objects in caches to attain a certain design objective, such as, e.g., minimizing network congestion or retrieval delays. We show that for a broad class of objectives, including minimizing both the expected network delay and the sum of network queue lengths, this optimization problem can be cast as an NP-hard submodular maximization problem. We show that so-called *continuous greedy* algorithm [1] attains a ratio arbitrarily close to $1 - 1/e \approx 0.63$ **using a deterministic estimation via a power series; this drastically reduces execution time over prior art, which resorts to sampling. Finally, we show that our results generalize, beyond M/M/1 queues, to networks of M/M/$k$ and symmetric M/D/1 queues.**

*Index Terms*—Kelly networks, cache networks, ICN

## I. INTRODUCTION

Kelly networks [2] are multi-class networks of queues capturing a broad array of queue service disciplines, including FIFO, LIFO, and processor sharing. Both Kelly networks and their generalizations (including networks of quasi-reversible and symmetric queues) are well studied and classic topics [2]–[5]. One of their most appealing properties is that their steady-state distributions have a product-form: as a result, steady state properties such as expected queue sizes, packet delays, and server occupancy rates have closed-form formulas as functions of, e.g., routing and scheduling policies.

In this paper, we consider Kelly networks in which nodes are equipped with caches, i.e., storage devices of finite capacity, which can be used to store objects. Exogenous requests for objects are routed towards nodes that store them; upon reaching a node that stores the requested object, a response packet containing the object is routed towards the request source. As a result, object traffic in the network is determined not only by the demand but, crucially, by where objects are cached. This abstract setting is motivated by–and can be used to model–various networking applications involving the placement and transmission of content. This includes information centric networks [6]–[8], content delivery networks [9], [10], web-caches [11]–[13], wireless/femtocell networks [14]–[16], and peer-to-peer networks [17], [18], to name a few.

In many of these applications, determining the *object placement*, i.e., how to place objects in network caches, is a decision that can be made by the network designer in response to object popularity and demand. To that end, we are interested in determining how to place objects in caches so that traffic attains a design objective such as, e.g., minimizing delay.

We make the following contributions. First, we study the problem of optimizing the placement of objects in caches in Kelly cache networks of M/M/1 queues, with the objective of minimizing a cost function of the system state. We show that, for a broad class of cost functions, including packet delay, system size, and server occupancy rate, *this optimization amounts to a submodular maximization problem with matroid constraints*. This result applies to general Kelly networks with fixed service rates; in particular, it holds for FIFO, LIFO, and processor sharing disciplines at each queue.

The so-called continuous greedy algorithm [1] attains a $1 - 1/e$ approximation for this NP-hard problem. However, it does so by computing an expectation over a random variable with exponential support via randomized sampling. The number of samples required to attain the $1-1/e$ approximation guarantee can be prohibitively large in realistic settings. Our second contribution is to show that, for Kelly networks of M/M/1 queues, *this randomization can be entirely avoided*: a closed-form solution can be computed using the Taylor expansion of our problem's objective. To the best of our knowledge, we are the first to identify a submodular maximization problem that exhibits this structure, and to exploit it to eschew sampling. Finally, we extend our results to *networks of M/M/k and symmetric M/D/1 queues*, and prove a negative result: submodularity does *not* arise in networks of M/M/1/$k$ queues. We extensively evaluate our proposed algorithms over several synthetic and real-life topologies.

The remainder of our paper is organized as follows. We review related work in Sec. II. We present our mathematical model of a Kelly cache network in Sec. III, and our results on submodularity and the continuous-greedy algorithm in networks of M/M/1 queues in Sections IV and V, respectively. Our extensions are described in Sec. VI; our numerical evaluation is in Sec. VII. Finally, we conclude in Sec. VIII. For brevity, we omit detailed proofs, which can be found in [19].

## II. RELATED WORK

Our approach is closest to, and inspired by, recent work by Shanmugam et al. [20] and Ioannidis and Yeh [8]. Ioannidis and Yeh consider a setting very similar to ours but without queuing: edges are assigned a fixed weight, and the objective is a linear function of incoming traffic scaled by these weights. This can be seen as a special case of our model, namely, one where edge costs are linear (see also Sec. III-B). Shanmugam et al. [20] study a similar optimization problem, restricted to the context of femtocaching. The authors show that this is an NP-hard, submodular maximization problem with matroid

constraints. They provide a $1 - 1/e$ approximation algorithm based on a technique by Ageev and Sviridenko [21]: this involves maximizing a concave relaxation of the original objective, and rounding via pipage-rounding [21]. Ioannidis and Yeh show that the same approximation technique applies to more general cache networks with linear edge costs. They also provide a distributed, adaptive algorithm that attains an $1-1/e$ approximation. The same authors extend this framework to jointly optimize both caching and routing decisions [22].

Our work can be seen as an extension of [8], [20], in that it incorporates queuing in the cache network. In contrast to both [8] and [20] however, costs like delay or queue sizes are highly non-linear in the presence of queuing. From a technical standpoint, this departure from linearity requires us to employ significantly different optimization methods than the ones in [8], [20]. In particular, our objective *does not admit a concave relaxation* and, consequently, the technique by Ageev and Sviridenko [21] used in [8], [20] *does not apply*. Instead, we must solve a non-convex optimization problem directly (c.f. Eq. (13)) using the so-called continuous-greedy algorithm.

Several papers have studied the cache optimization problems under restricted topologies [9], [23]–[26]. These works model the network as a bipartite graph: nodes generating requests connect directly to caches in a single hop. The resulting algorithms do not readily generalize to arbitrary topologies. In general, the approximation technique of Ageev and Sviridenko [21] applies to this bipartite setting, and additional approximation algorithms have been devised for several variants [9], [23]–[25]. We differ by (a) considering a multi-hop setting, and (b) introducing queuing, which none of the above works considers.

Submodular function maximization subject to matroid constraints appears in many important problems in combinatorial optimization; for a brief review of the topic and applications, see [27] and [28], respectively. Nemhauser et al. [29] show that the greedy algorithm produces a solution within 1/2 of the optimal. Vondrák [30] and Calinescu et al. [1] show that the continuous-greedy algorithm produces a solution within $(1 - 1/e)$ of the optimal in polynomial time, which cannot be further improved [31]. In the general case, the continuous-greedy algorithm requires sampling to estimate the gradient of the so-called multilinear relaxation of the objective (see Sec. V). One of our main contributions is to show that MAXCG, the optimization problem we study here, exhibits additional structure: we use this to construct a sampling-free estimator of the gradient via a power-series or Taylor expansion. To the best of our knowledge, we are the first to use such an expansion to eschew sampling; this technique may apply to submodular maximization problems beyond MAXCG.

## III. MODEL

Motivated by applications such as ICNs [6], CDNs [9], [10], and peer-to-peer networks [17], we introduce Kelly cache networks. In contrast to classic Kelly networks, each node is associated with a cache of finite storage capacity. Exogenous traffic consisting of *requests* is routed towards nodes that store
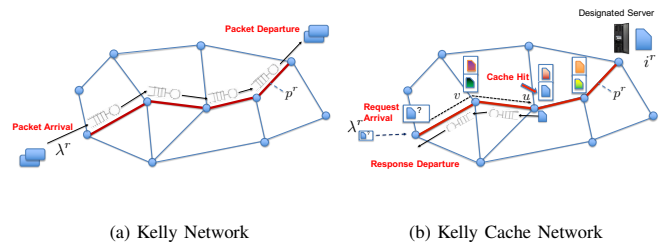


(a) Kelly Network  (b) Kelly Cache Network

Fig. 1: (a) Example of a Kelly network. Packets of class $r$ enter the network with rate $\lambda^r$, are routed through consecutive queues over path $p^r$, and subsequently exit the network. (b) Example of a Kelly cache network. Each node $v \in V$ is equipped with a cache of capacity $c_v$. Exogenous requests of type $r$ for object $i^r$ enter the network and are routed over a predetermined path $p^r$ towards the designated server storing $i^r$. Upon reaching an intermediate node $u$ storing the requested object $i^r$, a response packet containing the object is generated. The response is then forwarded towards the request's source in the reverse direction on path $p^r$. Request packets are of negligible size compared to response messages; as a result, we ignore request traffic and focus on queuing due to response traffic alone.

objects; upon reaching a node that stores the requested object, a *response* packet containing the object is routed towards the node that generated the request. As a result, content traffic in the network is determined not only by demand but, crucially, by how contents are cached. An illustration highlighting the differences between Kelly cache networks, introduced below, and classic Kelly networks, can be found in Fig. 1.

Although we describe Kelly cache networks in terms of FIFO M/M/1 queues, the product form distribution (c.f. (4)) arises for many different service principles beyond FIFO (c.f. Section 3.1 of [2]) including Last-In First-Out (LIFO) and processor sharing. All results we present extend to these service disciplines; we discuss more extensions in Sec. VI.

### A. Kelly Cache Networks

**Graphs and Paths.** We use the notation $G(V, E)$ for a directed graph $G$ with nodes $V$ and edges $E \subseteq V \times V$. A directed graph is called *symmetric* or *bidirectional* if $(u, v) \in E$ if and only if $(v, u) \in E$. A *path* $p$ is a sequence of adjacent nodes, i.e., $p = p_1, p_2, \ldots, p_K$ where $(p_k, p_{k+1}) \in E$, for all $1 \leq i < K \equiv |p|$. A path is *simple* if it contains no loops (i.e., each node appears once). We use the notation $v \in p$, where $v \in V$, to indicate that node $v$ appears in the path, and $e \in p$, where $e = (u, v) \in E$, to indicate that nodes $u, v$ are two consecutive (and, therefore, adjacent) nodes in $p$. For $v \in p$, where $p$ is simple, we denote by $k_p(v) \in \{1, \ldots, |p|\}$ the position of node $v \in V$ in $p$, i.e., $k_p(v) = k$ if $p_k = v$.

**Network Definition.** Formally, we consider a Kelly network of M/M/1 FIFO queues, represented by a symmetric directed graph $G(V, E)$. As in classic Kelly networks, each edge $e \in E$ is associated with an M/M/1 queue with service rate $\mu_e$[1]. In addition, each node has a cache that stores objects of equal size from a set $\mathcal{C}$, the *object catalog*. Each node $v \in V$ may store at most $c_v \in \mathbb{N}$ objects from $\mathcal{C}$ in its cache. Hence, if $x_{vi} \in \{0, 1\}$ is a binary variable indicating whether node

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[1]We associate queues with edges for concreteness. Alternatively, queues can be associated with nodes, or both nodes and edges; all such representations lead to product form distributions (4), and all our results extend to these cases.

$v \in V$ is storing object $i \in \mathcal{C}$, then $\sum_{i \in \mathcal{C}} x_{vi} \leq c_v$, for all $v \in V$. We refer to $\mathbf{x} = [x_{vi}]_{v \in V, i \in \mathcal{C}} \in \{0,1\}^{|V||\mathcal{C}|}$ as the *global placement* or, simply, *placement* vector. We denote by

$$\mathcal{D} = \left\{ \mathbf{x} \in \{0,1\}^{|V||\mathcal{C}|} : \sum_{i \in \mathcal{C}} x_{vi} \leq c_v, \forall v \in V \right\}, \quad (1)$$

the set of *feasible* placements that satisfy the storage capacity constraints. We assume that for every object $i \in \mathcal{C}$, there exists a set of nodes $\mathcal{S}_i \subseteq V$ that *permanently store* $i$. We refer to nodes in $\mathcal{S}_i$ as *designated servers* for $i \in \mathcal{C}$. We assume that designated servers store $i$ in permanent storage *outside* their cache. Put differently, the aggregate storage capacity of a node is $c_v' = c_v + |\{i : v \in \mathcal{S}_i\}|$, but only the non-designated slots $c_v$ are part of the system's design.

**Object Requests and Responses.** Traffic in the cache network consists of two types of packets: *requests* and *responses*, as shown in Fig. 1(b). Requests for an object are always routed towards one of its designated servers, ensuring that every request is satisfied. However, requests may terminate early: upon reaching any node that caches the requested object, the latter generates a response carrying the object. This is forwarded towards the request's source, following the same path as the request, in reverse. Consistent with prior literature [8], [22], we treat request traffic as negligible when compared to response traffic, which carries objects, and henceforth focus only on queues bearing response traffic.

Formally, a request and its corresponding response are fully characterized by (a) the object being requested, and (b) the path that the request follows. That is, for the set of requests $\mathcal{R}$, a request $r \in \mathcal{R}$ is determined by a pair $(i^r, p^r)$, where $i^r \in \mathcal{C}$ is the object being requested and $p^r$ is the path the request follows. Each request $r$ is associated with a corresponding Poisson arrival process with rate $\lambda^r \geq 0$, independent of other arrivals and service times. We denote the vector of arrival rates by $\boldsymbol{\lambda} = [\lambda^r]_{r \in \mathcal{R}} \in \mathbb{R}_+^{|\mathcal{R}|}$. For all $r \in \mathcal{R}$, we assume that the path $p^r$ is well-routed [8], that is: (a) path $p^r$ is simple, (b) the terminal node of the path is a designated server, i.e., a node in $\mathcal{S}_i$, and (c) no other intermediate node in $p^r$ is a designated server. As a result, requests are always served, and response packets (carrying objects) always follow a sub-path of $p^r$ in reverse towards the request source (namely, $p_1^r$).

**Steady State Distribution.** Given an object placement $\mathbf{x} \in \mathcal{D}$, the resulting system is a multi-class Kelly network, with packet classes determined by the request set $\mathcal{R}$. This is a Markov process over the state space determined by queue contents. In particular, let $n_e^r$ be the number of packets of class $r \in \mathcal{R}$ in queue $e \in E$, and $n_e = \sum_{r \in \mathcal{R}} n_e^r$ be the total queue size. The state of a queue $\mathbf{n}_e \in \mathcal{R}^{n_e}$, $e \in E$, is the vector of length $n_e$ representing the class of each packet in each position of the queue. The *system state* is then given by $\mathbf{n} = [\mathbf{n}_e]_{e \in E}$; we denote by $\Omega$ the state space of this Markov process.

In contrast to classic Kelly networks, network traffic and, in particular, the load on each queue, depend on placement $\mathbf{x}$. Indeed, if $(v,u) \in p^r$ for $r \in \mathcal{R}$, the arrival rate of responses

of class $r \in \mathcal{R}$ in queue $(u,v) \in E$ is:

$$\lambda_{(u,v)}^r(\mathbf{x}, \boldsymbol{\lambda}) = \lambda^r \prod_{k'=1}^{k_{p^r}(v)} (1 - x_{p_{k'}^r, i^r}), \quad \text{for } (v,u) \in p^r, \quad (2)$$

i.e., responses to requests of class $r$ pass through edge $(u,v) \in E$ if and only if no node preceding $u$ in the path $p^r$ stores object $i^r$—see also Fig. 1(b). As $\mu_{(u,v)}$ is the service rate of the queue in $(u,v) \in E$, the load on edge $(u,v) \in E$ is:

$$\rho_{(u,v)}(\mathbf{x}, \boldsymbol{\lambda}) = \frac{1}{\mu_{(u,v)}} \sum_{r \in \mathcal{R}:(v,u) \in p^r} \lambda_{(u,v)}^r(\mathbf{x}, \boldsymbol{\lambda}). \quad (3)$$

The Markov process $\{\mathbf{n}(t); t \geq 0\}_{t \geq 0}$ is positive recurrent when $\rho_{(u,v)}(\mathbf{x}, \boldsymbol{\lambda}) < 1$, for all $(u,v) \in E$ [2], [32]. Then, the steady-state distribution has a *product form*, i.e.:

$$\pi(\mathbf{n}) = \prod_{e \in E} \pi_e(\mathbf{n}_e), \quad \mathbf{n} \in \Omega, \quad (4)$$

where $\pi_e(\mathbf{n}_e) = (1 - \rho_e(\mathbf{x}, \boldsymbol{\lambda})) \prod_{r \in \mathcal{R}:e \in p^r} \left( \frac{\lambda_e^r(\mathbf{x}, \boldsymbol{\lambda})}{\mu_e} \right)^{n_e^r}$, and $\lambda_e^r(\mathbf{x}, \boldsymbol{\lambda})$, $\rho_e(\mathbf{x}, \boldsymbol{\lambda})$ are given by (2), (3), respectively.

**Stability Region.** Given a placement $\mathbf{x} \in \mathcal{D}$, a vector of arrival rates $\boldsymbol{\lambda} = [\lambda^r]_{r \in \mathcal{R}}$ yields a stable (i.e., positive recurrent) system if and only if $\boldsymbol{\lambda} \in \Lambda_{\mathbf{x}}$, where

$$\Lambda_{\mathbf{x}} := \{\boldsymbol{\lambda} : \boldsymbol{\lambda} \geq 0 : \rho_e(\mathbf{x}, \boldsymbol{\lambda}) < 1, \forall e \in E\} \subset \mathbb{R}_+^{|\mathcal{R}|}, \quad (5)$$

where loads $\rho_e$, $e \in E$, are given by (3). Conversely, given a vector $\boldsymbol{\lambda} \in \mathbb{R}_+^{|\mathcal{R}|}$,

$$\mathcal{D}_{\boldsymbol{\lambda}} = \{\mathbf{x} \in \mathcal{D} : \rho_e(\mathbf{x}, \boldsymbol{\lambda}) < 1, \forall e \in E\} \subseteq \mathcal{D} \quad (6)$$

is the set of feasible placements under which the system is stable. It is easy to confirm that, by the monotonicity of $\rho_e$ w.r.t. $\mathbf{x}$, if $\mathbf{x} \in \mathcal{D}_{\boldsymbol{\lambda}}$ and $\mathbf{x}' \geq \mathbf{x}$, then $\mathbf{x}' \in \mathcal{D}_{\boldsymbol{\lambda}}$, where the vector inequality $\mathbf{x}' \geq \mathbf{x}$ is component-wise. In particular, if $\mathbf{0} \in D_{\boldsymbol{\lambda}}$ (i.e., the system is stable without caching), then $\mathcal{D}_{\boldsymbol{\lambda}} = \mathcal{D}$.

*B. Cache Optimization*

Given a Kelly cache network represented by graph $G(V,E)$, service rates $\mu_e$, $e \in E$, storage capacities $c_v$, $v \in V$, a set of requests $\mathcal{R}$, and arrival rates $\lambda_r$, for $r \in \mathcal{R}$, we wish to determine placements $\mathbf{x} \in \mathcal{D}$ that optimize a certain design objective. In particular, we seek placements that are solutions to optimization problems of the following form:

MINCOST
$$\text{Minimize:} \quad C(\mathbf{x}) = \sum_{e \in E} C_e(\rho_e(\mathbf{x}, \boldsymbol{\lambda})), \quad (7a)$$
$$\text{subj. to:} \quad \mathbf{x} \in \mathcal{D}_{\boldsymbol{\lambda}}, \quad (7b)$$

where $C_e : [0,1) \to \mathbb{R}_+$, $e \in E$, are positive *cost* functions, $\rho_e : \mathcal{D} \times \mathbb{R}_+^{|\mathcal{R}|} \to \mathbb{R}_+$ is the load on edge $e$, given by (3), and $\mathcal{D}_{\boldsymbol{\lambda}}$ is the set of feasible placements that ensure stability, given by (6). We make the following standing assumption on the cost functions appearing in MINCOST:

**Assumption 1.** *For all $e \in E$, functions $C_e : [0,1) \to \mathbb{R}_+$ are convex and non-decreasing on $[0,1)$.*

Assumption 1 is natural; indeed it holds for many cost functions that often arise in practice. We list several examples:
**Example 1. Queue Size:** Under steady-state distribution (4), the expected number of packets in queue $e \in E$ is given by

$\mathbb{E}[n_e] = C_e(\rho_e) = \frac{\rho_e}{1-\rho_e}$, which is indeed convex and non-decreasing for $\rho_e \in [0,1)$. Hence, the expected total number of packets in the system in steady state can indeed be written as the sum of such functions.

**Example 2. Delay:** From Little's Theorem [32], the expected delay experienced by a packet in the system is $\mathbb{E}[T] = \frac{1}{\|\boldsymbol{\lambda}\|_1} \sum_{e \in E} \mathbb{E}[n_e]$, where $\|\boldsymbol{\lambda}\|_1 = \sum_{r \in \mathcal{R}} \lambda^r$ is the total arrival rate, and $\mathbb{E}[n_e]$ is the expected size of each queue. Thus, the expected delay can also be written as the sum of functions that satisfy Assumption 1. We note that the same is true for the sum of the expected delays per queue $e \in E$, as the latter are given by $\mathbb{E}[T_e] = \frac{1}{\lambda_e}\mathbb{E}[n_e] = \frac{1}{\mu_e(1-\rho_e)}$, which are also convex and non-decreasing in $\rho_e$.

**Example 3. Queuing Probability/Load per Edge:** In a FIFO queue, the *queuing probability* is the probability of arriving in a system where the server is busy; this is given by $C_e(\rho_e) = \rho_e = \lambda_e/\mu_e$, which is again non-decreasing and convex. This is also, of course, the load per edge. By treating $1/\mu_e$ as the weight of edge $e \in E$, this setting recovers the objective of [8] as a special case of our model.

**Example 4. Monotone Separable Costs:** More generally, consider a state-dependent cost function $c : \Omega \to \mathbb{R}_+$ that satisfies the following three properties: (1) it is separable across queues, (2) it depends only on queue sizes $n_e$, and (3) it is non-decreasing w.r.t. these queue sizes. Formally, $c(\mathbf{n}) = \sum_{e \in E} c_e(n_e)$, where $c_e : \mathbb{N} \to \mathbb{R}_+$, $e \in E$, are non-decreasing functions of the queue sizes. Then, the steady state cost under distribution (4) has precisely form (7a) with convex costs, i.e., $\mathbb{E}[c(\mathbf{n})] = \sum_{e \in E} C_e(\rho_e)$ where $C_e : [0,1) \to \mathbb{R}_+$ satisfy Assumption 1. This follows from the fact that:

$$C_e(\rho_e) \equiv \mathbb{E}[c_e(\mathbf{n})] = c_e(0) + \sum_{n=0}^{\infty}(c_e(n+1)-c_e(n))\rho_e^n. \quad (8)$$

In summary, MINCOST captures many natural cost objectives, while Assumption 1 holds for *any* monotonically increasing cost function that depends only on queue sizes.

## IV. SUBMODULARITY AND THE GREEDY ALGORITHM

Problem MINCOST is NP-hard; this is true even when cost functions $c_e$ are linear, and the objective is to minimize the sum of the loads per edge [8], [20]. In what follows, we outline our methodology for solving this problem; it relies on the fact that the objective of MINCOST is a *supermodular set function*; our first main contribution is to show that this property is a direct consequence of Assumption 1.

**Cost Supermodularity and Caching Gain.** First, observe that the cost function $C$ in MINCOST can be naturally expressed as a set function. Indeed, for $S \subset V \times \mathcal{C}$, let $\mathbf{x}_S \in \{0,1\}^{|V||\mathcal{C}|}$ be the binary vector whose support is $S$ (i.e., its non-zero elements are indexed by $S$). As there is a 1-1 correspondence between a binary vector $\mathbf{x}$ and its support $\text{supp}(\mathbf{x})$, we can interpret $C : \{0,1\}^{|V||\mathcal{C}|} \to \mathbb{R}_+$ as set function $C : V \times \mathcal{C} :\to \mathbb{R}_+$ via $C(S) \triangleq C(\mathbf{x}_S)$. Then, the following theorem holds:

**Theorem 1.** *Under Assumption 1, $C(S) \triangleq C(\mathbf{x}_S)$ is non-increasing and supermodular over $\{\text{supp}(\mathbf{x}) : \mathbf{x} \in \mathcal{D}_{\boldsymbol{\lambda}}\}$.*

TABLE I: Notation Summary

| Kelly Cache Networks | |
|---|---|
| $G(V,E)$ | Network graph, with nodes $V$ and edges $E$ |
| $k_p(v)$ | position of node $v$ in path $p$ |
| $\mu_{(u,v)}$ | Service rate of edge $(u,v) \in E$ |
| $\mathcal{R}$ | Set of classes/types of requests |
| $\lambda^r$ | Arrival rate of class $r \in \mathcal{R}$ |
| $p^r$ | Path followed by class $r \in \mathcal{R}$ |
| $i^r$ | Object requested by class $r \in \mathcal{R}$ |
| $\mathcal{C}$ | Item catalog |
| $\mathcal{S}_i$ | Set of designated servers of $i \in \mathcal{C}$ |
| $c_v$ | Cache capacity at node $v \in V$ |
| $x_{vi}$ | Variable indicating whether $v \in V$ stores $i \in \mathcal{C}$ |
| $\mathbf{x}$ | Placement vector of $x_{vi}$s, in $\{0,1\}^{|V||\mathcal{C}|}$ |
| $\boldsymbol{\lambda}$ | Vector of arrival rates $\lambda^r$, $r \in \mathcal{R}$ |
| $\lambda_e^r$ | Arrival rate of class $r$ responses over edge $e \in E$ |
| $\rho_e$ | Load on edge $e \in E$ |
| $\Omega$ | State space |
| $\mathbf{n}$ | Global state vector in $\Omega$ |
| $\pi(\mathbf{n})$ | Steady-state distribution of $\mathbf{n} \in \Omega$ |
| $\mathbf{n}_e$ | State vector of queue at edge $e \in E$ |
| $\pi_e(\mathbf{n}_e)$ | Marginal of steady-state distribution of queue $\mathbf{n}_e$ |
| $n_e$ | Size of queue at edge $e \in E$ |
| **Cache Optimization** | |
| $C$ | Global Cost function |
| $C_e$ | Cost function of edge $e \in E$ |
| $\mathcal{D}$ | Set of placements $\mathbf{x}$ satisfying capacity constraints |
| $\mathbf{x}_0$ | A feasible placement in $\mathcal{D}$ |
| $F(\mathbf{x})$ | Caching gain of placement $\mathbf{x}$ over $\mathbf{x}_0$ |
| $y_{vi}$ | Probability that $v \in V$ stores $i \in \mathcal{C}$ |
| $\mathbf{y}$ | Vector of marginal probabilities $y_{vi}$, in $\{0,1\}^{|V||\mathcal{C}|}$ |
| $G(\mathbf{y})$ | Multilinear extension under marginals $\mathbf{y}$ |
| $\mathcal{D}_{\boldsymbol{\lambda}}$ | Set of placements under which system is stable under arrivals $\boldsymbol{\lambda}$ |
| $\hat{\mathcal{D}}$ | Convex hull of constraints of MAXCG |
| **Conventions** | |
| $\text{supp}(\cdot)$ | Support of a vector |
| $\text{conv}(\cdot)$ | Convex hull of a set |
| $[\mathbf{x}]_{+i}$ | Vector equal to $\mathbf{x}$ with $i$-th coordinate set to 1 |
| $[\mathbf{x}]_{-i}$ | Vector equal to $\mathbf{x}$ with $i$-th coordinate set to 0 |
| $\mathbf{0}$ | Vector of zeros |

*Proof (Sketch).* It is easy to verify that $\rho_e(S), \forall e \in E$, is supermodular and non-increasing in $S$. By Assumption 1 $C_e$ is a non-decreasing function, so $C_e(S) \triangleq C_e(\rho_e(S))$ is non-increasing. The theorem then follows from Assumption 1 and the fact that if $f : \mathbb{R} \to \mathbb{R}$ is convex and non-decreasing and $g : \mathcal{X} \to \mathbb{R}$ is non-increasing supermodular then $h(\mathbf{x}) \triangleq f(g(\mathbf{x}))$ is also supermodular. $\square$

In light of the observations in Sec. III-B regarding Assumption 1, Thm. 1 implies that supermodularity arises *for a broad array of natural cost objectives*, including expected delay and system size; it also applies under the *full generality of Kelly networks*, including FIFO, LIFO, and round robin service disciplines. Armed with this theorem, we turn our attention to converting MINCOST to a *submodular maximization* problem. In doing so, we face the problem that domain $\mathcal{D}_{\boldsymbol{\lambda}}$, determined not only by storage capacity constraints, but also by stability, may be difficult to characterize. Nevertheless, we show that a problem that is amenable to approximation can be constructed, provided that a placement $\mathbf{x}_0 \in \mathcal{D}_{\boldsymbol{\lambda}}$ is known.

In particular, suppose that we have access to a single $\mathbf{x}_0 \in \mathcal{D}_{\boldsymbol{\lambda}}$. We define the *caching gain* $F : \mathcal{D}_{\boldsymbol{\lambda}} \to \mathbb{R}_+$ as $F(\mathbf{x}) = C(\mathbf{x}_0) - C(\mathbf{x})$. Note that, for $\mathbf{x} \geq \mathbf{x}_0$, $F(\mathbf{x})$ is the relative decrease in the cost compared to the cost under $\mathbf{x}_0$. We consider the following optimization problem:

$$\text{MAXCG}$$
$$\text{Maximize:} \quad F(\mathbf{x}) = C(\mathbf{x}_0) - C(\mathbf{x}) \quad (9a)$$
$$\text{subj. to:} \quad \mathbf{x} \in \mathcal{D}, \mathbf{x} \geq \mathbf{x}_0 \quad (9b)$$
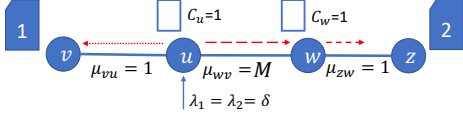
Fig. 2: A path graph, illustrating that the $1/2$-approximation ratio of greedy is tight. Greedy caches item 2 in node $u$, while the optimal decision is to cache item 1 in $u$ and item 2 in node $w$. For $M$ large enough, the approximation ratio can be made arbitrarily close to 1/2. In our experiments in Sec. VII, we set $\delta = 0.5$ and $M = 200$.

---

**Algorithm 1** Greedy

**Input:** $F : \mathcal{D} \to \mathbb{R}_+, \mathbf{x}_0$
1: $\mathbf{x} \leftarrow \mathbf{x}_0$
2: **while** $A(\mathbf{x}) := \{(v,i) \in V \times \mathcal{C} : \mathbf{x} + \mathbf{e}_{vi} \in \mathcal{D}\}$ is not empty **do**
3: $\quad (v^*, i^*) \leftarrow \arg\max_{(v,i) \in A(\mathbf{x})} (F(\mathbf{x} + \mathbf{e}_{vi}) - F(\mathbf{x}))$
4: $\quad \mathbf{x} \leftarrow \mathbf{x} + \mathbf{e}_{v^* i^*}$
5: **end while**
6: **return** $\mathbf{x}$

---

Observe that, if $\mathbf{0} \in \mathcal{D}_{\boldsymbol{\lambda}}$, then $\mathcal{D}_{\boldsymbol{\lambda}} = \mathcal{D}$; in this case, taking $\mathbf{x}_0 = \mathbf{0}$ ensures that problems MINCOST and MAXCG are equivalent. If $\mathbf{x}_0 \neq \mathbf{0}$, the above formulation attempts to maximize the gain restricted to placements $\mathbf{x} \in \mathcal{D}$ that dominate $\mathbf{x}_0$: such placements necessarily satisfy $\mathbf{x} \in \mathcal{D}_{\boldsymbol{\lambda}}$. Thm. 1 has the following immediate implication:

**Corollary 1.** *The caching gain* $F(S) \triangleq F(\mathbf{x}_S)$ *is non-decreasing and submodular over* $\{\text{supp}(\mathbf{x}) : \mathbf{x} \in \mathcal{D}_{\boldsymbol{\lambda}}\}$.

**Greedy Algorithm.** Constraints (9b) define a (partition) matroid [1], [20]. This, along with the submodularity and monotonicity of $F$ imply that we can produce a solution within $\frac{1}{2}$-approximation from the optimal via the *greedy* algorithm [33]. The algorithm, summarized in Alg. 1, iteratively allocates items to caches that yield the largest marginal gain. The solution produced by Algorithm 1 is guaranteed to be within a $\frac{1}{2}$-approximation ratio of the optimal solution of MAXCG [29]. The approximation guarantee of $\frac{1}{2}$ is tight:

**Lemma 1.** *For any* $\varepsilon > 0$, *there exists a cache network the greedy algorithm solution is within* $\frac{1}{2} + \varepsilon$ *from the optimal, when the objective is the sum of expected delays per edge.*

The instance under which the bound is tight is given in Fig. 2 (see also [19]). As we discuss in Sec. VII, the greedy algorithm performs well in practice for some topologies; however, Lemma 1 motivates us to seek alternative algorithms, that attain improved approximation guarantees.

## V. CONTINUOUS-GREEDY ALGORITHM

The *continuous-greedy* algorithm by Calinescu et al. [1] attains a tighter guarantee than the greedy algorithm, raising the approximation ratio from 0.5 to $1 - 1/e \approx 0.63$. The algorithm maximizes the so-called *multilinear extension* of objective $F$, thereby obtaining a fractional solution $Y$ in the convex hull of the constraint space. The resulting solution is then rounded to produce an integral solution.

### A. Algorithm Overview

Formally, the multilinear extension of the caching gain $F$ is defined as follows. Define the convex hull of the set defined

---

**Algorithm 2** Continuous-Greedy

**Input:** $G : \tilde{\mathcal{D}} \to \mathbb{R}_+, \mathbf{x}_0$, stepsize $0 < \gamma \leq 1$
1: $t \leftarrow 0, k \leftarrow 0 \; \mathbf{y}_0 \leftarrow \mathbf{x}_0$
2: **while** $t < 1$ **do**
3: $\quad \mathbf{m}_k \leftarrow \arg\max_{\mathbf{m} \in \tilde{\mathcal{D}}} \langle \mathbf{m}, \nabla G(\mathbf{y}_k) \rangle$
4: $\quad \gamma_k \leftarrow \min\{\gamma, 1 - t\}$
5: $\quad \mathbf{y}_{k+1} \leftarrow \mathbf{y}_k + \gamma_k \mathbf{m}_k, t \leftarrow t + \gamma_k, k \leftarrow k + 1$
6: **end while**
7: **return** $\mathbf{y}_k$

---

by the constraints (9b) in MAXCG as:

$$\tilde{\mathcal{D}} = \text{conv}(\{\mathbf{x} : \mathbf{x} \in \mathcal{D}, \mathbf{x} \geq \mathbf{x}_0\}) \subseteq [0,1]^{|V||\mathcal{C}|} \quad (10)$$

Intuitively, $\mathbf{y} \in \tilde{\mathcal{D}}$ is a *fractional* vector in $\mathbb{R}^{|V||\mathcal{D}|}$ satisfying the capacity constraints, and the bound $\mathbf{y} \geq \mathbf{x}_0$.

Given a $\mathbf{y} \in \tilde{\mathcal{D}}$, consider a random vector $\mathbf{x}$ in $\{0,1\}^{|V||\mathcal{C}|}$ generated as follows: for all $v \in V$ and $i \in \mathcal{C}$, the coordinates $x_{vi} \in \{0,1\}$ are independent Bernoulli variables such that $\mathbf{P}(x_{vi} = 1) = y_{vi}$. The multilinear extension $G : \tilde{\mathcal{D}} \to \mathbb{R}_+$ of $F : \mathcal{D}_{\boldsymbol{\lambda}} \to \mathbb{R}_+$ is defined via following expectation $G(\mathbf{y}) = \mathbb{E}_{\mathbf{y}}[F(\mathbf{x})]$, parameterized by $\mathbf{y} \in \tilde{\mathcal{D}}$, i.e.,

$$G(\mathbf{y}) = \sum_{\mathbf{x} \in \{0,1\}^{|V||\mathcal{C}|}} F(\mathbf{x}) \times \prod_{(v,i) \in V \times \mathcal{C}} y_{vi}^{x_{vi}} (1 - y_{vi})^{1-x_{vi}}, \quad (11)$$

The continuous-greedy algorithm, summarized in Alg. 2, proceeds by first producing a fractional vector $\mathbf{y} \in \tilde{\mathcal{D}}$. Starting from $\mathbf{y}_0 = \mathbf{x}_0$, the algorithm iterates over:

$$\mathbf{m}_k \in \arg\max_{\mathbf{m} \in \tilde{\mathcal{D}}} \langle \mathbf{m}, \nabla G(\mathbf{y}_k) \rangle, \quad (12a)$$
$$\mathbf{y}_{k+1} = \mathbf{y}_k + \gamma_k \mathbf{m}_k, \quad (12b)$$

for an appropriately selected step size $\gamma_k \in [0, 1]$. Intuitively, this yields an approximate solution to the non-convex problem:

$$\text{Maximize:} \quad G(\mathbf{y}) \quad (13a)$$
$$\text{subj. to:} \quad \mathbf{y} \in \tilde{\mathcal{D}}. \quad (13b)$$

Even though (13) is not convex, the output of Alg. 2 is within a $1 - 1/e$ factor from the optimal solution $\mathbf{y}^* \in \tilde{\mathcal{D}}$ of (13). This fractional solution can be rounded to produce a solution to MAXCG with the same approximation guarantee using either the pipage rounding [21] or the swap rounding [1], [34] schemes: we refer the reader to [19] for details.

**A Sampling-Based Estimator.** Function $G$, given by (11), involves a summation over $2^{|V||\mathcal{C}|}$ terms, and cannot be easily computed in polynomial time. Typically, a sampling-based estimator (see, e.g., [1]) is used instead. Function $G$ is linear when restricted to each coordinate $y_{vi}$, for some $v \in V, i \in \mathcal{C}$ (i.e., when all inputs except $y_{vi}$ are fixed). As a result, the partial derivative of $G$ w.r.t. $y_{vi}$ can be written as:

$$\frac{\partial G(\mathbf{y})}{\partial y_{vi}} = \mathbb{E}_{\mathbf{y}}[F(\mathbf{x})|x_{vi} = 1] - \mathbb{E}_{\mathbf{y}}[F(\mathbf{x})|x_{vi} = 0] \geq 0, \quad (14)$$

where the last inequality is due to monotonicity of $F$. One can thus estimate the gradient by (a) producing $T$ random samples $\mathbf{x}^{(\ell)}, \ell = 1, \ldots, T$ of the random vector $\mathbf{x}$, consisting of independent Bernoulli coordinates, and (b) computing, for each pair $(v,i) \in V \times \mathcal{C}$, the average

$$\widehat{\frac{\partial G(\mathbf{y})}{\partial y_{vi}}} = \frac{1}{T} \sum_{\ell=1}^{T} \left( F([\mathbf{x}^\ell]_{+(v,i)}) - F([\mathbf{x}^\ell]_{-(v,i)}) \right), \quad (15)$$

where $[\mathbf{x}]_{+(v,i)}, [\mathbf{x}]_{-(v,i)}$ are equal to vector $\mathbf{x}$ with the $(v,i)$-th coordinate set to 1 and 0, respectively. Using this estimate, Alg. 2 is poly-time and attains an approximation ratio arbitrarily close to $1 - 1/e$ for appropriately chosen $T$ (see [19] for a formal guarantee and characterization of Alg. 2's complexity).

### B. A Novel Estimator via Taylor Expansion

The classic approach to estimate the gradient via sampling has certain drawbacks. The number of samples $T$ required to attain the $1 - 1/e$ ratio is quadratic in $|V||\mathcal{C}|$. In practice, even for networks and catalogs of moderate size (say, $|V| = |\mathcal{C}| = 100$), the number of samples becomes prohibitive (of the order of $10^8$). Producing an estimate for $\nabla G$ via a closed form computation that eschews sampling thus has significant computational advantages. In this section, we show that the multilinear relaxation of the caching gain $F$ admits such a closed-form characterization.

We say that a polynomial $f : \mathbb{R}^d \to \mathbb{R}$ is in *Weighted Disjunctive Normal Form* (*W-DNF*) if it can be written as

$$f(\mathbf{x}) = \sum_{s \in \mathcal{S}} \beta_s \cdot \prod_{j \in \mathcal{I}(s)} (1 - x_j), \quad (16)$$

for some index set $\mathcal{S}$, positive coefficients $\beta_s > 0$, and index sets $I(s) \subseteq \{1, \ldots, d\}$. Intuitively, treating binary variables $x_j \in \{0, 1\}$ as boolean values, each W-DNF polynomial can be seen as a weighted sum (disjunction) among products (conjunctions) of negative literals. These polynomials arise naturally in the context of our problem; in particular:

**Lemma 2.** *For all $k \geq 1$, $\mathbf{x} \in \mathcal{D}$, and $e \in E$, $\rho_e^k(\mathbf{x}, \boldsymbol{\lambda})$ is a W-DNF polynomial whose coefficients depend on $\boldsymbol{\lambda}$.*

*Proof (Sketch).* The lemma holds for $k = 1$ by (2) and (3). The lemma follows by induction, as W-DNF polynomials over binary $\mathbf{x} \in \mathcal{D}$ are closed under multiplication; this is because $(1 - x)^\ell = (1 - x)$ for all $\ell \geq 1$ when $x \in \{0, 1\}$. $\square$

Hence, *all load powers are W-DNF polynomials*. Expectations of W-DNF polynomials have a remarkable property:

**Lemma 3.** *Let $f : \mathcal{D}_{\boldsymbol{\lambda}} \to \mathbb{R}$ be a W-DNF polynomial, and let $\mathbf{x} \in \mathcal{D}$ be a random vector of independent Bernoulli coordinates parameterized by $\mathbf{y} \in \tilde{\mathcal{D}}$. Then $\mathbb{E}_\mathbf{y}[f(\mathbf{x})] = f(\mathbf{y})$, where $f(\mathbf{y})$ is the evaluation of the W-DNF polynomial representing $f$ over the real vector $\mathbf{y}$.*

*Proof.* By (16) and independence, we have $\mathbb{E}_\mathbf{y}[f(\mathbf{x})] = \sum_{s \in \mathcal{S}} \beta_s \mathbb{E}_\mathbf{y}\left[\prod_{t \in \mathcal{I}(s)} (1 - x_t)\right] = \sum_{s \in \mathcal{S}} \beta_s \prod_{t \in \mathcal{I}(s)} (1 - \mathbb{E}_\mathbf{y}[x_t]) = \sum_{s \in \mathcal{S}} \beta_s \prod_{t \in \mathcal{I}(s)} (1 - y_t)$. $\square$

Lemma 3 states that, to compute the expectation of a W-DNF polynomial $f$ over i.i.d. Bernoulli variables with expectations $\mathbf{y}$, it suffices to *evaluate $f$ over input $\mathbf{y}$*. Expectations computed this way therefore do not require sampling.

We leverage this property to approximate $\nabla G(\boldsymbol{y})$ by taking the Taylor expansion of the cost functions $C_e$ at each edge $e \in E$. This allows us to write $C_e$ as a power series w.r.t. $\rho_e^k$, $k \geq 1$; from Lemmas 2 and 3, we can compute the expectation of this series in a closed form. In particular, by expanding the series and rearranging terms it is easy to show the following:

**Lemma 4.** *Consider a cost function $C_e : [0, 1) \to \mathbb{R}_+$ which satisfies Assumption 1 and for which the Taylor expansion exists at some $\rho^* \in [0, 1)$. Then, for $\mathbf{x} \in \mathcal{D}$ a random Bernoulli vector parameterized by $\mathbf{y} \in \tilde{\mathcal{D}}$,*

$$\frac{\partial G(\mathbf{y})}{\partial y_{vi}} \approx \sum_{e \in E} \sum_{k=1}^{L} \alpha_e^{(k)} \left[\rho_e^k\left([\mathbf{y}]_{-(v,i)}, \boldsymbol{\lambda}\right) - \rho_e^k\left([\mathbf{y}]_{+(v,i)}, \boldsymbol{\lambda}\right)\right] \quad (17)$$

*where, $\alpha_e^{(k)} = \sum_{i=k}^{L} \frac{(-1)^{i-k}\binom{i}{k}}{i!} C_e^{(i)}(\rho^*)(\rho^*)^{i-k}$ for $k = 0, 1, \cdots, L$, and the error of the approximation is: $\frac{1}{(L+1)!} \sum_{e \in E} C_e^{(L+1)}(\rho') \left[\mathbb{E}_{[\mathbf{y}]_{-v,i}}[(\rho_e(\mathbf{x}, \boldsymbol{\lambda}) - \rho^*)^{L+1}] - \mathbb{E}_{[\mathbf{y}]_{+v,i}}[(\rho_e(\mathbf{x}, \boldsymbol{\lambda}) - \rho^*)^{L+1}]\right]$.*

Estimator (17) is *deterministic*: no random sampling is required. Moreover, Taylor's theorem allows us to characterize the error (i.e., the *bias*) of this estimate. We use this to characterize the final fractional solution $\mathbf{y}$ produced by Alg. 2:

**Theorem 2.** *Assume that all $C_e$, $e \in E$, satisfy Assumption 1, are $L + 1$-differentiable, and that all their $L + 1$ derivatives are bounded by $W \geq 0$. Then, consider Alg. 2, in which $\nabla G(\mathbf{y}_k)$ is estimated via the Taylor estimator (17), where each edge cost function is approximated at $\rho_e^* = \mathbb{E}_{\mathbf{y}_k}[\rho_e(\mathbf{x}, \boldsymbol{\lambda})] = \rho_e(\mathbf{y}_k, \boldsymbol{\lambda})$. Then,*

$$G(\mathbf{y}_K) \geq (1 - \tfrac{1}{e})G(\mathbf{y}^*) - 2DB - \frac{P}{2K}, \quad (18)$$

*where $K = \frac{1}{\gamma}$ is the number of iterations, $\mathbf{y}^*$ is an optimal solution to (13), $D = \max_{\mathbf{y} \in \tilde{\mathcal{D}}} \|\mathbf{y}\|_2 \leq |V| \cdot \max_{v \in V} c_v$, is the diameter of $\tilde{\mathcal{D}}$, $B \leq \frac{W|E|}{(L+1)!}$ is the bias of estimator (17), and $P = 2C(\mathbf{x}_0)$, is a Lipschitz constant of $\nabla G$.*

The theorem immediately implies that we can replace (17) as an estimator in Alg. 2, and attain an approximation arbitrarily close to $1 - 1/e$.

**Estimation via Power Series.** For arbitrary $L + 1$-differentiable cost functions $C_e$, the estimator (17) can be leveraged by replacing $C_e$ with its Taylor expansion. In the case of queue-dependent cost functions, as described in Example 4 of Section III-B, the power-series (8) can be used instead. For example, the expected queue size (Example 1, Sec. III-B), is given by $C_e(\rho_e) = \frac{\rho_e}{1 - \rho_e} = \sum_{k=1}^{\infty} \rho_e^k$. In contrast to the Taylor expansion, this power series does not depend on a point $\rho_e^*$ around which the function $C_e$ is approximated.

## VI. BEYOND M/M/1 QUEUES

As discussed in Section III, the classes of M/M/1 queues for which the supermodularity of the cost functions arises is quite broad, and includes FIFO, LIFO, and processor sharing queues. In this section, we discuss how our results extend to even broader families of queuing networks. We first extend our results to M/M/$k$ queues for two cost functions $C_e$:

**Lemma 5.** *For a network of M/M/$k$ queues, both the queuing probability[2] and the expected queue size are non-increasing and supermodular over sets $\{\text{supp}(\mathbf{x}) : \mathbf{x} \in \mathcal{D}_{\boldsymbol{\lambda}}\}$.*

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[2]This is given by the so-called Erlang C formula [32].

TABLE II: Graph Topologies and Experiment Parameters.

| Graph | $|V|$ | $|E|$ | $|\mathcal{C}|$ | $|\mathcal{R}|$ | $|Q|$ | $c_v$ | $F_{\text{PL}}(\mathbf{x}_{\text{RND}})$ | $F_{\text{UNI}}(\mathbf{x}_{\text{RND}})$ |
|---|---|---|---|---|---|---|---|---|
| ER | 100 | 1042 | 300 | 1K | 4 | 3 | 2.75 | 2.98 |
| ER-20Q | 100 | 1042 | 300 | 1K | 20 | 3 | 3.1 | 2.88 |
| HC | 128 | 896 | 300 | 1K | 4 | 3 | 2.25 | 5.23 |
| HC-20Q | 128 | 896 | 300 | 1K | 20 | 3 | 2.52 | 5.99 |
| star | 100 | 198 | 300 | 1K | 4 | 3 | 6.08 | 8.3 |
| path | 4 | 3 | 2 | 2 | 1 | 1 | 1.2 | 1.2 |
| dtelekom | 68 | 546 | 300 | 1K | 4 | 3 | 2.57 | 3.66 |
| abilene | 11 | 28 | 4 | 2 | 2 | 1/2 | 4.39 | 4.39 |
| geant | 22 | 66 | 10 | 100 | 4 | 2 | 19.68 | 17.22 |
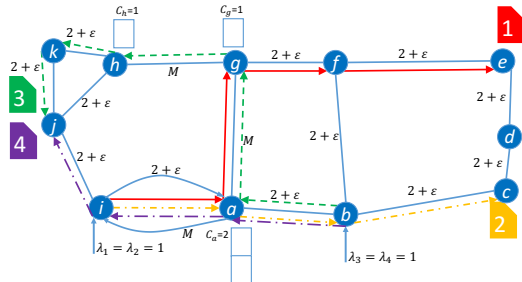
Fig. 3: The abilene topology. We consider a catalog size of $|\mathcal{C}| = 4$ and 4 requests ($|\mathcal{R}| = 4$). Requests originate from $|Q| = 2$ nodes, $b$ and $i$. Three edges have a high service rate $M \gg 1$, and the rest have a low service rate $2 + \varepsilon$. Only nodes $a$, $g$, and $h$ can cache items, and have capacities 2, 1, and 1, respectively. We set $M = 200$ and $\varepsilon = 0.05$ in our experiments. Greedy is 0.5-approximate in this instance.

We note that, as an immediate consequence of Lemma 5 and Little's theorem, both the sum of the expected delays per queue, but also the expected delay of an arriving packet, are also supermodular and non-decreasing.

Product-form steady-state distributions arise also in settings where service times are not exponentially distributed. A large class of quasi-reversible queues, named *symmetric queues* exhibit this property (c.f. Section 3.3 of [2] and Chapter 10 of [4]). In the following lemma we leverage the product form of symmetric queues to extend our results to M/D/1 symmetric queues [32].

**Lemma 6.** *For a network of M/D/1 symmetric queues, the expected queue size is non-increasing and supermodular over sets* $\{\text{supp}(\mathbf{x}) : \mathbf{x} \in \mathcal{D}_{\boldsymbol{\lambda}}\}$.

Again, Lemma 6 and Little's theorem imply that this property also extends to network delays. It is worth noting that conclusions similar to these in Lemmas 5 and 6 are not possible for all general queues with product form distributions. In particular, also we prove the following negative result:

**Lemma 7.** *There exists a network of M/M/1/k queues, containing a queue $e$, for which no strictly monotone function $C_e$ of the load $\rho_e$ at a queue $e$ is non-increasing and supermodular over sets* $\{\text{supp}(\mathbf{x}) : \mathbf{x} \in \mathcal{D}_{\boldsymbol{\lambda}}\}$. *In particular, the expected size of queue $e$ is neither monotone nor supermodular.*

## VII. NUMERICAL EVALUATION

**Networks.** We execute Algorithms 1 and 2 over 9 network topologies, summarized in Table II. Graphs ER and ER-20Q are the same 100-node Erdős-Rényi graph with parameter $p = 0.1$. Graphs HC and HC-20Q are the same hypercube graph with 128 nodes, and graph star is a star graph with 100 nodes. The graph path is the topology shown in
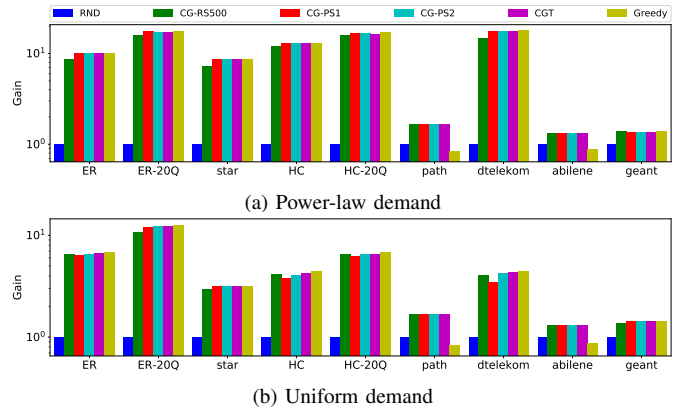
Fig. 4: Caching gains for different topologies and different arrival distributions, normalized by the gains corresponding to RND, reported in Table. II. Greedy performs comparatively well. However, it attains sub-optimal solutions for path and abilene; these solutions are worse than RND. CG-RS500 has a poor performance compared to other variations of the continuous-greedy algorithm.
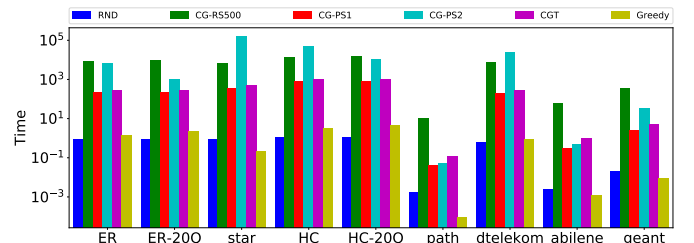
Fig. 5: Running time for different topologies and power-law arrival distribution, in seconds. CG-RS500 is slower than power series estimation CG-PS1 and CGT, sometimes exceeding CG-PS2 as well.
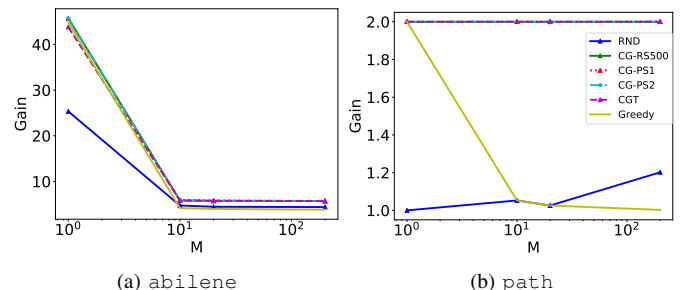
Fig. 6: Caching gain vs. $M$. As the discrepancy between the service rate of low-bandwidth and high-bandwidth links increases, the performance of Greedy deteriorates.

Fig. 2. The last 3 topologies, namely, dtelekom, geant, and abilene represent the Deutsche Telekom, GEANT, and Abilene backbone networks, respectively. The latter is also shown in Fig. 3.

**Experimental Setup.** For path and abilene, we set demands, storage capacities, and service rates as illustrated in Figures 2 and 3, respectively. Both of these settings induce an approximation ratio close to $1/2$ for greedy. For all remaining topologies, we consider a catalog of size $|\mathcal{C}|$ objects; for each object, we select 1 node uniformly at random (u.a.r.) from $V$ to serve as the designated server for this object. To induce traffic overlaps, we also select $|Q|$ nodes u.a.r. that serve as sources for requests; all requests originate from these sources. All caches are set to the same storage capacity, i.e., $c_v = c$ for all $v \in V$. We generate a set of $|\mathcal{R}|$ possible types of requests. For each request type $r \in \mathcal{R}$, $\lambda^r = 1$ request per second,
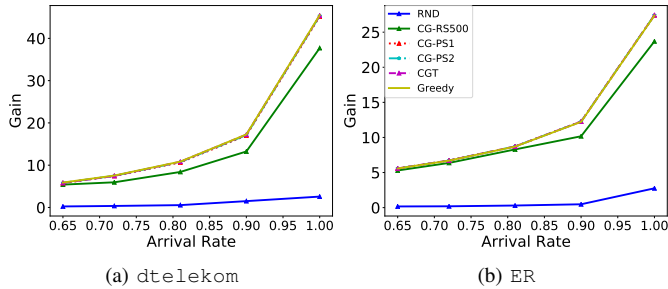
(a) dtelekom      (b) ER

Fig. 7: Caching gain vs. arrival rate. As the arrival rate increases caching gains get larger.
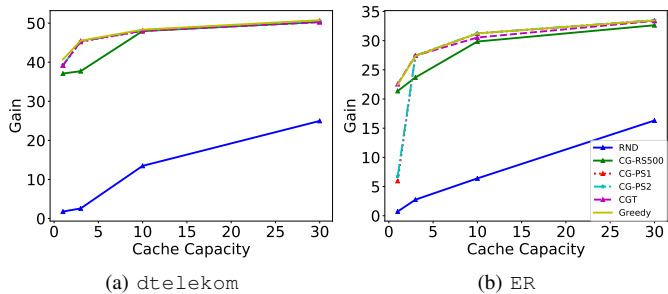


(a) dtelekom      (b) ER

Fig. 8: Caching gain vs. cache capacity. As caching capacities increase, caching gains rise.

and path $p^r$ is generated by selecting a source among the $|Q|$ sources u.a.r., and routing towards the designated server of object $i^r$ using a shortest path algorithm. We consider two ways of selecting objects $i^r \in \mathcal{C}$: in the *uniform* regime, $i^r$ is selected u.a.r. from the catalog $\mathcal{C}$; in the *power-law* regime, $i^r$ is selected from the catalog $\mathcal{C}$ via a power law distribution with exponent 1.2. All the parameter values, e.g., catalog size $|\mathcal{C}|$, number of requests $|\mathcal{R}|$, number of query sources $|Q|$, and caching capacities $c_v$ are presented in Table II.

We construct heterogeneous service rates as follows. Every queue service rate is either set to a low value $\mu_e = \mu_{\text{low}}$ or a high value $\mu_e = \mu_{\text{high}}$, for all $e \in E$. We select $\mu_{\text{low}}$ and $\mu_{\text{high}}$ as follows. Given the demands $r \in \mathcal{R}$ and the corresponding arrival rates $\lambda^r$, we compute the highest load under no caching ($\mathbf{x} = \mathbf{0}$), i.e., we find $\lambda_{\max} = \max_{e \in E} \sum_{r : e \in p^r} \lambda^r$. We then set $\mu_{\text{low}} = \lambda_{\max} \times 1.05$ and $\mu_{\text{high}} = \lambda_{\max} \times 200$. We set the service rate to $\mu_{\text{low}}$ for all congested edges, i.e., edges $e$ s.t. $\lambda_e = \lambda_{\max}$. We set the service rate for each remaining edge $e \in E$ to $\mu_{\text{low}}$ independently with probability 0.7, and to $\mu_{\text{high}}$ otherwise. Note that, as a result $\mathbf{0} \in \mathcal{D}_{\boldsymbol{\lambda}} = \mathcal{D}$, i.e., the system is stable even in the absence of caching and, on average, 30 percent of the edges have a high service rate.

**Placement Algorithms.** We implement several placement algorithms: (a) *Greedy*, i.e., the greedy algorithm (Alg. 1), (b) *Continuous-Greedy with Random Sampling* (CG-RS), i.e., Algorithm 2 with a gradient estimator based on sampling, as described in Sec. V-A, (c) *Continuous-Greedy with Taylor approximation* (CGT), i.e., Algorithm 2 with a gradient estimator based on the Taylor expansion, as described in Sec. V-B, and (d) *Continuous-Greedy with Power Series approximation* (CG-

PS), i.e., Algorithm 2 with a gradient estimator based on the power series expansion, described also in Sec. V-B. In the case of CG-RS, we collect 500 samples, i.e., $T = 500$. In the case of CG-PS we tried the first and second order expansions of the power series as CG-PS1 and CG-PS2, respectively. In the case of CGT, we tried the first-order expansion ($L = 1$). In both cases, subsequent to the execution of Alg. 2 we produce an integral solution in $\mathcal{D}$ by rounding via the swap rounding method [34]. All continuous-greedy algorithms use $\gamma = 0.001$. We also implement a random selection algorithm (RND), which caches $c_v$ items at each node $v \in V$, selected uniformly at random. We repeat RND 10 times, and report the average running time and caching gain.

**Caching Gain Across Different Topologies.** The caching gain $F(\mathbf{x})$ for $\mathbf{x}$ generated by different placement algorithms, is shown for power-law arrival distribution and uniform arrival distribution in Figures 4a and 4b, respectively. The values are normalized by the gains obtained by RND, reported in Table II. Also, the running times of the algorithms for power-law arrival distribution are reported in Fig. 5. As we see in Fig. 4, Greedy is comparable to other algorithms in most topologies. However, for topologies `path` and `abilene` Greedy obtains a sub-optimal solution, in comparison to the continuous-greedy algorithm. In fact, for `path` and `abilene` Greedy performs even worse than RND. In Fig. 4, we see that the continuous-greedy algorithms with gradient estimators based on Taylor and Power series expansion, i.e., CG-PS1, CG-PS2, and CGT outperform CG-RS500 in most topologies. Also, from Fig. 5, we see that CG-RS500 runs 100 times slower than the continuous-greedy algorithms with first-order gradient estimators, i.e., CG-PS1 and CGT. Note that 500 samples are significantly below the value needed to attain the theoretical guarantees of the continuous-greedy algorithm, which is quadratic in $|V||\mathcal{C}|$.

**Varying Service Rates.** For topologies `path` and `abilene`, the approximation ratio of Greedy is $\approx 0.5$. This ratio is a function of service rate of the high-bandwidth link $M$. In this experiment, we explore the effect of varying $M$ on the performance of the algorithms in more detail. We plot the caching gain obtained by different algorithms for `path` and `abilene` topologies, using different values of $M \in \{M_{\min}, 10, 20, 200\}$, where $M_{\min}$ is the value that puts the system on the brink of instability, i.e., 1 and $2 + \epsilon$ for `path` and `abilene`, respectively. Thus, we gradually increase the discrepancy between the service rate of low-bandwidth and high-bandwidth links. The corresponding caching gains are plotted in Fig. 6, as a function of $M$. We see that as $M$ increases the gain attained by Greedy worsens in both topologies: when $M = M_{\min}$ Greedy matches the performance of the continuous-greedy algorithms, in both cases. However, for higher values of $M$ it is beaten not only by all variations of the continuous-greedy algorithm, but by RND as well.

**Effect of Congestion on Caching Gain.** In this experiment, we study the effect of varying arrival rates $\lambda^r$ on caching gain $F$. We report results only for the `dtelekom` and `ER` topologies and power-law arrival distribution. We obtain the

cache placements $\mathbf{x}$ using the parameters presented in Table II and different arrival rates: $\lambda^r \in \{0.65, 0.72, 0.81, 0.9, 1.0\}$, for $r \in \mathcal{R}$. Fig. 7 shows the caching gain attained by the placement algorithms as a function of arrival rates. We observe that as we increase the arrival rates, the caching gain attained by almost all algorithms, except RND, increases significantly. Moreover, CG-PS1, CG-PS2, CGT, and Greedy have a similar performance, while CG-RS500 achieves lower caching gains. **Varying Caching Capacity.** In this experiment, we study the effect of increasing cache capacity $c_v$ on the acquired caching gains. Again, we report the results only for the `dtelekom` and `ER` topologies and power-law arrival distribution. We evaluate the caching gain obtained by different placement algorithms using the parameters of Table II and different caching capacities: $c_v \in \{1, 3, 10, 30\}$ for $v \in V$. The caching gain is plotted in Fig. 8. As we see, in all cases the obtained gain increases, as we increase the caching capacities. This is expected: caching more items reduces traffic and delay, increasing the gain.

## VIII. Conclusions

Our analysis suggests feasible object placements targeting many design objectives of interest, including system size and delay, can be determined using combinatorial techniques. Our work leaves the exact characterization of approximable objectives for certain classes of queues, including M/M/1/k queues, open. Our work also leaves open problems relating to stability. This includes the characterization of the stability region of arrival rates $\Lambda = \cup_{\mathbf{x} \in \mathcal{D}} \Lambda(\mathbf{x})$. It is not clear whether determining membership in this set (or, equivalently, given $\lambda$, determining whether there exists a $\mathbf{x} \in \mathcal{D}$ under which the system is stable) is NP-hard or not, and whether this region can be somehow approximated. Finally, all algorithms presented in this paper are offline: identifying how to determine placements in an online, distributed fashion, in a manner that attains a design objective (as in [8], [22]), or even stabilizes the system (as in [7]), remains an important open problem.

## IX. Acknowledgements

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