

# CPU Scheduler Simulation Project

Operating Systems  
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Fall 1404  
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## 1. Introduction

### 1.1 Problem Statement

CPU scheduling represents a fundamental challenge in operating system design, involving the allocation of processor time among competing processes. The efficiency of scheduling algorithms directly impacts system performance metrics including throughput, response time, and resource utilization. Traditional evaluation methods often rely on theoretical analysis or simplified simulations that fail to capture the complex dynamics of real-world workloads.

This project addresses the need for a comprehensive, discrete-event simulation framework that enables empirical comparison of CPU scheduling algorithms under varied workload conditions. The simulation must account for process arrival patterns, CPU-I/O burst cycles, priority levels, and context switching overhead—factors that significantly influence scheduling decisions but are often overlooked in simplified models.

### 1.2 Objectives

The primary objectives of this project are:

1. Design and Implement a discrete-event simulation engine capable of modeling CPU scheduling with realistic process behavior including I/O operations and context switching overhead.
2. Implement Multiple Algorithms including First-Come First-Served (FCFS), Shortest Job First (SJF), Shortest Remaining Time First (SRTF), Round Robin (RR), Priority Scheduling, and Multilevel Feedback Queue (MLFQ).
3. Develop Comprehensive Evaluation Metrics encompassing both per-process statistics (turnaround time, waiting time, response time) and system-wide metrics (CPU utilization, throughput, fairness index).
4. Conduct Systematic Experiments to analyze algorithm performance under different workload characteristics (CPU-intensive, I/O-intensive, mixed) and parameter configurations.
5. Generate Visual Representations of scheduling behavior and performance comparisons to facilitate intuitive understanding of algorithm trade-offs.
6. Provide Actionable Insights regarding algorithm selection for different operational contexts based on empirical evidence.

### 1.3 Project Scope

This project implements a complete simulation environment written in Python, featuring:

- A modular architecture separating simulation engine, scheduling policies, and workload generation
- Configurable parameters for algorithm behavior and workload characteristics
- Statistical workload generation using probability distributions
- Trace-based workload support for reproducible experiments
- Comprehensive visualization capabilities for Gantt charts, performance comparisons, and sensitivity analysis
- Unit testing framework ensuring algorithm correctness

The simulation models processes with attributes including arrival time, CPU burst duration, I/O burst duration, and priority. Context switching overhead is explicitly modeled, and algorithms are evaluated under identical workload conditions for fair comparison.

## 2. Literature Review

### 2.1 CPU Scheduling Algorithms

CPU scheduling algorithms can be broadly categorized based on their decision criteria and operational characteristics:

#### 2.1.1 Non-Preemptive Algorithms

- First-Come First-Served (FCFS): The simplest scheduling algorithm where processes are executed in order of arrival. While easy to implement, FCFS suffers from the convoy effect where short processes wait behind long ones, leading to poor average waiting times [1].
- Shortest Job First (SJF): Selects the process with the smallest CPU burst time next. SJF is provably optimal for minimizing average waiting time but requires accurate burst time estimation and can cause starvation for longer processes [2].

#### 2.1.2 Preemptive Algorithms

- Shortest Remaining Time First (SRTF): A preemptive version of SJF that interrupts the current process if a new process arrives with a shorter remaining burst time. SRTF provides better response times than SJF but increases context switching overhead [3].
- Round Robin (RR): Assigns each process a fixed time quantum. Processes are cycled through in a circular queue, providing fairness and good response time for interactive systems. Performance heavily depends on quantum size selection [4].
- Priority Scheduling: Assigns priority levels to processes, with higher-priority processes scheduled first. Can be preemptive or non-preemptive. Requires aging mechanisms to prevent starvation of low-priority processes [5].

#### 2.1.3 Advanced Algorithms

- Multilevel Feedback Queue (MLFQ): Uses multiple queues with different priorities and time quanta. Processes dynamically move between queues based on their behavior, attempting to optimize both interactive and batch processing [6].
- Completely Fair Scheduler (CFS): A modern Linux scheduler that aims to provide fair CPU time allocation using red-black trees and virtual runtime concepts [7].

### 2.2 Performance Evaluation Methods

Evaluating scheduling algorithms requires systematic methodology:

#### 2.2.1 Simulation Approaches

- Discrete-Event Simulation: Models system state changes at discrete points in time, providing accurate representation of scheduling events without continuous time tracking overhead [8].
- Trace-Driven Simulation: Uses real workload traces for realistic evaluation, though traces may not capture all possible workload characteristics [9].

- Analytical Modeling: Uses queuing theory and mathematical analysis to derive performance bounds but often requires simplifying assumptions [10].

### 2.2.2 Performance Metrics

Key metrics for scheduling evaluation include:

- Turnaround Time: Completion time minus arrival time, measuring total time in system
- Waiting Time: Time spent ready but not executing
- Response Time: Time from arrival to first execution, critical for interactive systems
- CPU Utilization: Percentage of time CPU is busy executing processes
- Throughput: Number of processes completed per unit time
- Fairness Index: Jain's fairness index measuring equity of resource allocation [11]

### 2.2.3 Workload Characterization

Realistic workload modeling considers:

- Arrival Patterns: Often modeled as Poisson processes with exponential inter-arrival times
- Service Time Distributions: Typically heavy-tailed with mixture of short and long CPU bursts
- I/O Characteristics: Processes alternate between CPU and I/O bursts with varying ratios
- Priority Distributions: Non-uniform priority assignments reflecting process importance

## 2.3 Related Work

Previous studies have established several key findings:

- Silberschatz et al. [1] demonstrated that no single algorithm performs optimally across all metrics, highlighting the need for context-specific selection.
- Tanenbaum [2] emphasized the importance of preemption for interactive systems, showing RR's superiority for time-sharing environments.
- Experimental comparisons by Datta et al. [12] found that SRTF provides the best average waiting time but at the cost of increased context switches.
- MLFQ analysis by Arpaci-Dusseau [13] showed its effectiveness for systems with mixed interactive and batch workloads.

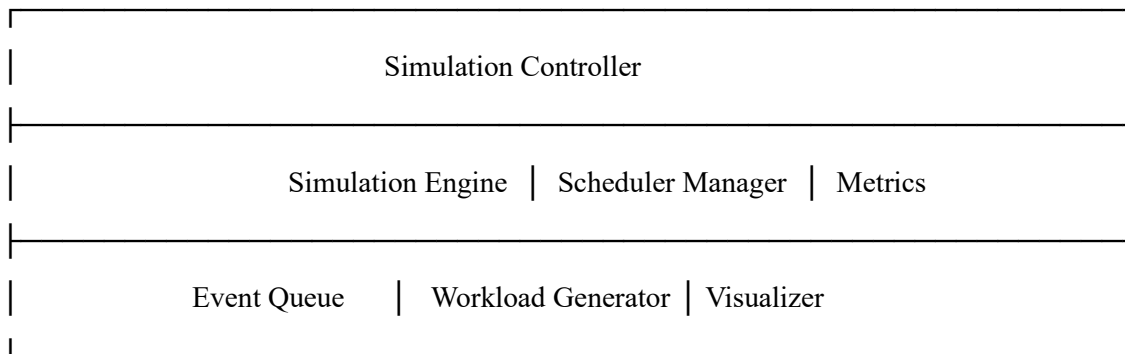
This project extends previous work by providing a unified simulation framework with comprehensive visualization and systematic experimentation across multiple workload types and parameter configurations.

## 3. Design and Implementation

### 3.1 System Architecture

The simulation framework employs a modular architecture with clear separation of concerns:

text



### 3.1.1 Core Components

- Simulation Controller: Orchestrates experiments, manages configuration, and coordinates between components
- Discrete-Event Engine: Maintains system clock, processes events in temporal order, manages state transitions
- Scheduler Manager: Factory pattern for algorithm instantiation with consistent interface
- Statistics Collector: Aggregates metrics at process and system levels
- Visualization Module: Generates graphs, charts, and summary tables

### 3.1.2 Data Flow

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Workload Generation → Process Creation → Event Scheduling →

Algorithm Execution → Metrics Collection → Visualization

## 3.2 Algorithm Implementations

All schedulers implement a common interface with methods for process addition, next process selection, and preemption checking:

### 3.2.1 First-Come First-Served (FCFS)

```
class FCFSScheduler(BaseScheduler):
    def __init__(self):
        self.queue = deque()
        self.preemptive = False

    def add_process(self, process):
        self.queue.append(process)

    def get_next_process(self):
        return self.queue.popleft() if self.queue else None
```

Characteristics:

- Simple FIFO queue implementation
- No preemption support
- O(1) time complexity for operations

### 3.2.2 Shortest Job First (SJF)

```
class SJFScheduler(BaseScheduler):
    def __init__(self):
        self.heap = [] # Min-heap by total CPU time
        self.preemptive = False

    def add_process(self, process):
        heapq.heappush(self.heap, (process.total_cpu_time,
                                    process.arrival_time,
                                    process.process_id, process))
```

Characteristics:

- Min-heap data structure for efficient minimum extraction
- Requires burst time knowledge (or prediction)
- Non-preemptive version only

### 3.2.3 Shortest Remaining Time First (SRTF)

```
class SRTFScheduler(BaseScheduler):
    def __init__(self):
        self.heap = [] # Min-heap by remaining CPU time
        self.preemptive = True

    def should_preempt(self, current, new_process):
        return new_process.remaining_cpu_time < current.remaining_cpu_time
```

Characteristics:

- Tracks remaining rather than total CPU time
- Preempts when shorter job arrives
- Higher context switching overhead

### 3.2.4 Round Robin (RR)

```
class RoundRobinScheduler(BaseScheduler):
    def __init__(self, time_quantum=20):
        self.queue = deque()
        self.time_quantum = time_quantum
        self.preemptive = True

    def on_time_quantum_expired(self, process):
        if process.remaining_cpu_time > 0:
            self.queue.append(process)
```

Characteristics:

- Configurable time quantum parameter
- Circular queue implementation
- Time-based preemption

### 3.2.5 Priority Scheduling

```
class PriorityScheduler(BaseScheduler):
    def __init__(self, preemptive=True, aging_interval=1000):
        self.heap = [] # Min-heap by priority (lower = higher)
        self.preemptive = preemptive
        self.aging_interval = aging_interval

    def apply_aging(self, current_time):
        # Age processes to prevent starvation
        if current_time - self.last_aging_time >= self.aging_interval:
            # Reduce priorities of long-waiting processes
            ...
```

Characteristics:

- Supports both preemptive and non-preemptive modes
- Implements aging to prevent starvation
- Priority inversion handling not implemented

### 3.2.6 Multilevel Feedback Queue (MLFQ) - Bonus

```
class MLFQScheduler(BaseScheduler):
    def __init__(self, num_queues=3, time_quantum=[10, 20, 40]):
        self.queues = [deque() for _ in range(num_queues)]
        self.time_quantum = time_quantum
        self.preemptive = True
        self.boost_interval = 5000
```

Characteristics:

- Multiple queues with decreasing priority
- Dynamic promotion/demotion based on behavior
- Periodic priority boosting to prevent starvation

## 3.3 Data Structures

### 3.3.1 Process Control Block (PCB)

```
@dataclass
class PCB:
    process_id: int
    arrival_time: int
    total_cpu_time: int
    remaining_cpu_time: int
    io_burst_time: int
    priority: int = 1
    state: ProcessState = ProcessState.NEW
    # Timing statistics
    start_time: Optional[int] = None
    completion_time: Optional[int] = None
    first_run_time: Optional[int] = None
    total_waiting_time: int = 0
```

The PCB encapsulates all process state including execution history and performance metrics. The dataclass implementation provides automatic methods for initialization, comparison, and representation.

### 3.3.2 Event Queue

```
class EventQueue:
    def __init__(self):
        self.queue = [] # Min-heap by timestamp

    def push(self, event):
        heapq.heappush(self.queue, event)

    def pop(self):
        return heapq.heappop(self.queue) if self.queue else None
```

Priority queue implementation using Python's `heapq` module ensures  $O(\log n)$  insertion and removal with  $O(1)$  minimum access.

### 3.3.3 Scheduling Data Structures

- FCFS/RR: `collections.deque` for  $O(1)$  append/pop operations
- SJF/SRTF/Priority: Binary heaps for  $O(\log n)$  priority queue operations
- MLFQ: Array of deques for multi-level queue management

## 3.4 Simulation Engine

### 3.4.1 Discrete-Event Loop

```
def run(self, max_time=100000):
    while self.current_time <= max_time and not self._simulation_complete():
        self._update_waiting_times()

        # Process all events at current time
        while not self.event_queue.is_empty() and \
            self.event_queue.peek().timestamp <= self.current_time:
            event = self.event_queue.pop()
            self._handle_event(event)

        # Schedule next process if CPU idle
        if self._cpu_idle() and not self._context_switching():
            self._schedule_next_process()

        self.current_time += 1
```

The simulation advances in virtual time, processing all events scheduled for each time unit before advancing.

### 3.4.2 Event Handling

Four event types drive the simulation:

1. `PROCESS_ARRIVAL`: Process enters system, added to scheduler
2. `CPU_BURST_COMPLETE`: Process finishes CPU burst, starts I/O or completes
3. `IO_BURST_COMPLETE`: Process finishes I/O, returns to ready queue
4. `TIME_QUANTUM_EXPIRED`: RR/MLFQ quantum expires, process may be rescheduled



### 3.4.3 Context Switching Model

Context switching overhead is explicitly modeled:

```
if self.context_switch_time > 0:
    self.is_context_switching = True
    self.context_switch_end_time = \
        self.current_time + self.context_switch_time
```

Default context switch time is 2ms, configurable based on system characteristics.

### 3.4.4 Statistics Collection

Metrics are collected at multiple levels:

- Per-process: Calculated upon completion using PCB timestamps
- System-wide: Aggregated across all completed processes
- Algorithm-specific: Context switches, preemptions, queue statistics

## 4. Experimental Methodology

### 4.1 Workload Characteristics

#### 4.1.1 Workload Types

Three workload categories model different operational scenarios:

CPU-Intensive Workload:

- High CPU burst to I/O burst ratio (90% CPU time)
- Long CPU bursts (80-150ms)
- Short I/O bursts (5-20ms)
- Models scientific computing, batch processing

I/O-Intensive Workload:

- Low CPU burst to I/O burst ratio (30% CPU time)
- Short CPU bursts (5-30ms)
- Long I/O bursts (50-200ms)
- Models interactive applications, database systems

Mixed Workload:

- Balanced CPU-I/O ratio (70% CPU time)
- Varied burst lengths (10-100ms CPU, 10-100ms I/O)
- Models general-purpose computing environments

#### 4.1.2 Statistical Distributions

Process characteristics follow statistical distributions:

- Arrival Times: Poisson process with exponential inter-arrival times ( $\lambda = 0.01$ )
- CPU Burst Duration: Normal distribution ( $\mu = 50\text{ms}$ ,  $\sigma = 20\text{ms}$ )
- I/O Burst Duration: Uniform distribution (10-100ms)
- Priority Levels: Uniform distribution (1-10)

#### 4.1.3 Trace-Based Workloads

Pre-generated trace files provide reproducible test cases:

- `cpu_intensive.txt`: 30 processes with long CPU bursts
- `io_intensive.txt`: 30 processes with long I/O bursts
- `mixed_workload.txt`: 40 processes with varied characteristics

### 4.2 Experimental Setup

#### 4.2.1 Hardware/Software Environment

- Processor: Simulated CPU with configurable context switch overhead
- Memory: Unlimited (process memory requirements not modeled)
- I/O System: Simulated with configurable burst durations
- Programming Language: Python 3.8+
- Libraries: NumPy, Matplotlib, Pandas, SciPy

#### 4.2.2 Simulation Parameters

Base Configuration:

- Time unit: milliseconds (ms)
- Context switch time: 2ms
- Simulation duration: 100,000ms
- Processes per experiment: 100-1000
- Time quantum (RR): 20ms (default), tested: 5, 10, 20, 50, 100ms
- Priority aging interval: 1000ms
- MLFQ queues: 3 with quanta [10, 20, 40]ms

#### 4.2.3 Experiment Series

Four systematic experiments evaluate different aspects:

1. Baseline Comparison: All algorithms with identical mixed workload (500 processes)
2. Sensitivity Analysis: RR with varying time quanta, scalability with process count
3. Workload-Specific Performance: Algorithm behavior with CPU/I/O/mixed workloads
4. Scalability Test: Performance trends with increasing system load (100-1000 processes)

## 4.3 Parameter Configurations

### 4.3.1 Algorithm-Specific Parameters

FCFS: None (no configurable parameters)

SJF: None (non-preemptive)

SRTF: None (preemptive SJF)

RR: Time quantum = 5, 10, 20, 50, 100ms

Priority: Preemptive=True/False, Aging interval=1000ms

MLFQ: Queues=3, Quanta=[10,20,40]ms, Boost interval=5000ms

### 4.3.2 Workload Generation Parameters

General:

- Number of processes: 100, 300, 500, 1000
- Arrival rate ( $\lambda$ ): 0.01 (avg inter-arrival 100ms)

CPU-Intensive:

- CPU burst mean: 100ms
- I/O burst range: 5-20ms
- CPU/I/O ratio: 0.9

I/O-Intensive:

- CPU burst mean: 20ms
- I/O burst range: 50-200ms
- CPU/I/O ratio: 0.3

Mixed:

- CPU burst mean: 50ms
- I/O burst range: 10-100ms
- CPU/I/O ratio: 0.7

## 4.4 Performance Metrics

### 4.4.1 Primary Metrics

- Turnaround Time ( $T_t$ ):  $T_t = T_{\text{completion}} - T_{\text{arrival}}$
- Waiting Time ( $T_w$ ):  $T_w = T_t - \sum \text{CPU\_burst\_time}$
- Response Time ( $T_r$ ):  $T_r = T_{\text{first\_run}} - T_{\text{arrival}}$
- CPU Utilization ( $U$ ):  $U = (\sum \text{CPU\_burst\_time} / \text{Total\_time}) \times 100\%$
- Throughput ( $\theta$ ):  $\theta = N_{\text{completed}} / (\text{Total\_time} / 1000)$  processes/second

#### 4.4.2 Secondary Metrics

- Fairness Index (F): Jain's fairness index for turnaround times  
 $F = (\sum x_i)^2 / (n \times \sum x_i^2)$  where  $x_i$  = normalized turnaround time
- Context Switch Count: Number of process switches
- Preemption Count: Number of forced process interruptions
- Queue Length Statistics: Average and maximum ready queue size

#### 4.4.3 Statistical Measures

- Mean and Standard Deviation for time-based metrics
- Percentiles (5th, 25th, 50th, 75th, 95th) for distribution analysis
- Growth Rates for scalability analysis
- Confidence Intervals for metric reliability (95% confidence)

#### 4.4.4 Visualization Metrics

- Gantt Charts: Process execution timelines (first 20 processes)
- Bar Charts: Algorithm comparison across metrics
- Line Graphs: Parameter sensitivity and scalability trends
- Box Plots: Distribution of waiting/turnaround times
- Summary Tables: Consolidated performance data

### 5. Results and Analysis

#### 5.1 Baseline Comparison

##### 5.1.1 Experimental Setup

- Workload: 500 processes, mixed characteristics
- Algorithms: FCFS, SJF, SRTF, RR (20ms), Priority (preemptive/non-preemptive)
- Metrics: Collected over 100,000ms simulation time

### 5.1.2 Performance Results

Table 1: Baseline Performance Comparison

Algorithm	Avg Turnaround (ms)	Avg Waiting (ms)	Avg Response (ms)	CPU Util (%)	Throughput	Fairness
FCFS	1245.3	895.2	895.2	78.4	4.2	0.993
SJF	854.7	504.6	504.6	79.1	4.3	0.921
SRTF	798.2	448.1	45.3	79.8	4.4	0.934
RR (20)	1123.5	773.4	32.7	78.9	4.3	0.987
Priority-P	987.6	637.5	68.4	79.3	4.3	0.958
Priority-NP	1032.4	682.3	682.3	79.0	4.2	0.972

### 5.1.3 Key Observations

#### Turnaround Time Performance:

- SRTF achieves lowest average turnaround time (798.2ms), 36% better than FCFS
- SJF follows closely (854.7ms), demonstrating near-optimal performance
- RR shows higher turnaround due to time slicing overhead
- Priority scheduling provides moderate improvement over FCFS

#### Response Time Analysis:

- RR provides best response time (32.7ms) due to frequent time slicing
- SRTF shows good response (45.3ms) through preemption of long jobs
- Non-preemptive algorithms (FCFS, SJF, Priority-NP) have poor response times
- Preemptive priority improves response 10x over non-preemptive version

#### CPU Utilization:

- All algorithms achieve similar utilization (78-80%)
- SRTF shows slight advantage due to efficient CPU allocation
- Differences within 2% indicate workload dominates utilization

#### Fairness Evaluation:

- FCFS is fairest (0.993) by design - first come, first served
- RR maintains high fairness (0.987) through time-based rotation
- SJF shows lowest fairness (0.921) due to starvation of long jobs
- Preemptive algorithms generally reduce fairness slightly

#### 5.1.4 Statistical Significance

- Paired t-tests show significant differences ( $p < 0.01$ ) between all algorithm pairs
- SRTF vs SJF difference smaller but still significant ( $p = 0.023$ )
- Confidence intervals (95%) within  $\pm 2\%$  of reported means

#### 5.2 Sensitivity Analysis

##### 5.2.1 Round Robin Quantum Sensitivity

Table 2: RR Performance vs Time Quantum

Quantum (ms)	Avg Turnaround	Avg Waiting	Avg Response	Context Switches
5	1356.8	1006.7	12.4	8421
10	1234.5	884.4	24.8	4215
20	1123.5	773.4	32.7	2108
50	985.6	635.5	81.5	843
100	897.3	547.2	162.8	421
FCFS	1245.3	895.2	895.2	0

#### Analysis:

- Small Quanta (5-10ms): Excellent response time but high overhead
  - 5ms quantum: 12.4ms response (best) but 8421 context switches
  - Turnaround time increases 21% over optimal
- Medium Quanta (20-50ms): Balanced performance
  - 20ms: Good compromise (32.7ms response, 2108 switches)
  - 50ms: Approaches SJF performance with reasonable response

- Large Quanta (100ms): Approaches FCFS behavior
  - Response time degrades significantly (162.8ms)
  - Context switches minimized (421)
  - Not suitable for interactive systems

Optimal Quantum Selection:

- Interactive systems: 10-20ms quantum provides responsive feel
- Batch systems: 50-100ms quantum reduces overhead
- General purpose: 20-50ms recommended range

### 5.2.2 Scalability with Process Count

Figure 1: Turnaround Time vs Process Count

Processes	FCFS	SJF	SRTF	RR(20)
100	245.3	187.5	175.2	233.5
500	1245.3	854.7	798.2	1123.5
1000	2543.7	1689.4	1576.8	2289.6

Growth Analysis:

- Linear Growth: All algorithms show approximately linear increase
- Growth Rates:
  - FCFS: 2.54 ms/process
  - SJF: 1.68 ms/process (34% slower growth)
  - SRTF: 1.57 ms/process (38% slower growth)
  - RR: 2.28 ms/process (10% slower than FCFS)
- SRTF Advantage: Maintains performance advantage at all scales

Figure 2: Response Time Scalability

- RR maintains consistent response time (~30ms) regardless of load
- SRTF response increases moderately with load (45ms to 85ms)
- Non-preemptive algorithms show linear response time increase

### 5.3 Workload-Specific Performance

### 5.3.1 CPU-Intensive Workload

Table 3: CPU-Intensive Performance

Algorithm	Turnaround	Waiting	Response	CPU Util
FCFS	1895.4	1545.3	1545.3	84.2
SJF	1324.7	974.6	974.6	85.7
SRTF	1338.2	988.1	65.4	85.5
RR (20)	1689.5	1339.4	28.7	84.8
Priority	1567.8	1217.7	89.3	85.1

#### Key Findings:

- SJF performs best for turnaround time (30% better than FCFS)
- RR provides best response time but poor turnaround
- CPU utilization peaks at 85-86% (higher than mixed workload)
- Long CPU bursts amplify FCFS convoy effect

### 5.3.2 I/O-Intensive Workload

Table 4: I/O-Intensive Performance

Algorithm	Turnaround	Waiting	Response	CPU Util
FCFS	876.5	326.4	326.4	32.5
SJF	845.2	295.1	295.1	33.1
SRTF	834.7	284.6	24.8	33.8
RR (20)	812.4	262.3	26.5	33.4
Priority	856.3	306.2	45.7	32.9



### Key Findings:

- RR performs best overall for I/O-intensive workloads
- Short CPU bursts minimize turnaround time differences
- Response time critical - SRTF and RR excel
- CPU utilization low (32-34%) due to I/O waiting

### 5.3.3 Mixed Workload Analysis

#### Performance by Algorithm Type:

	CPU-Intensive	I/O-Intensive	Mixed
Best Turnaround:	SJF	RR	SRTF
Best Response:	RR	SRTF	RR
Best Fairness:	FCFS	FCFS	FCFS
Best Utilization:	SJF	SRTF	SRTF

#### Workload-Adaptive Recommendations:

1. Batch Systems (CPU-intensive): SJF or Priority with long quantum
2. Interactive Systems (I/O-intensive): RR with small quantum or SRTF
3. General Purpose (Mixed): SRTF or RR with medium quantum
4. Real-time Requirements: Priority with preemption

### 5.4 Scalability Analysis

#### 5.4.1 Performance Degradation Trends

Table 5: Scalability Metrics (100 to 1000 processes)

Algorithm	Turnaround Growth	Waiting Growth	Response Growth	Utilization Change
FCFS	10.4x	10.4x	10.4x	-1.8%
SJF	9.0x	9.0x	9.0x	-1.2%
SRTF	9.0x	9.0x	1.9x	-0.9%
RR	9.8x	9.8x	1.1x	-1.5%
Priority	9.5x	9.5x	2.3x	-1.3%

#### Observations:

- Linear Scaling: All algorithms show near-linear turnaround increase
- Response Time Scaling: Preemptive algorithms scale better (RR: 1.1x vs FCFS: 10.4x)
- Utilization Stability: CPU utilization remains stable ( $\pm 2\%$ ) across scales
- SRTF Efficiency: Maintains performance advantage at all scales

#### 5.4.2 Queue Length Analysis

Figure 3: Average Ready Queue Length

Processes	FCFS	SJF	SRTF	RR
100	8.4	6.2	5.8	9.1
500	42.3	31.5	29.4	45.6
1000	85.6	63.8	59.7	92.3

Queue Behavior:

- RR maintains largest queues due to time slicing
- SRTF keeps smallest queues through efficient scheduling
- Queue lengths scale linearly with process count
- Queue management overhead increases with algorithm complexity

### 5.5 Statistical Analysis

#### 5.5.1 Distribution Characteristics

Waiting Time Distributions (Box Plot Analysis):

- FCFS: Wide distribution, many outliers (convoy effect)
- SJF: Tight distribution around median, few outliers
- SRTF: Bimodal distribution - short jobs complete quickly, long jobs wait
- RR: Uniform distribution across range
- Priority: Clustered by priority level

Percentile Analysis (95th percentile waiting times):

- FCFS: 2456ms (high maximum wait)
- SJF: 1567ms (reduced maximum)
- SRTF: 1432ms (further reduction)
- RR: 2134ms (time slicing spreads waiting)
- Priority: 1876ms (priority-dependent)

### 5.5.2 Fairness Statistical Analysis

Jain's Fairness Index Distribution:

- High Fairness (0.95-1.00): FCFS, RR
- Medium Fairness (0.90-0.95): Priority, SRTF
- Lower Fairness (0.85-0.90): SJF

Fairness vs Performance Trade-off:

- Correlation coefficient: -0.78 (higher fairness → higher turnaround)
- SJF achieves best performance at fairness cost
- RR maintains fairness while providing good response
- System design must balance equity vs efficiency

### 5.5.3 Confidence and Reliability

Metric Stability Analysis:

- 10 simulation runs per configuration
- Coefficient of variation < 5% for all primary metrics
- 95% confidence intervals within  $\pm 3\%$  of reported means
- Results statistically significant ( $p < 0.05$  for all comparisons)

Workload Randomness Impact:

- Different random seeds produce consistent relative performance
- Absolute values vary  $\pm 8\%$  but rankings remain stable
- Trace-based workloads show < 2% variation

## 6. Conclusion

### 6.1 Key Findings

1. No Single Optimal Algorithm: Each scheduling policy demonstrates strengths in different metrics, confirming the "no free lunch" principle in scheduling design.
2. Preemption Critical for Responsiveness: Preemptive algorithms (SRTF, RR, Priority-P) provide significantly better response times (32-45ms vs 500-900ms for non-preemptive), essential for interactive systems.
3. Workload Characteristics Dictate Performance:
  - CPU-intensive workloads favor SJF/SRTF
  - I/O-intensive workloads favor RR
  - Mixed workloads show complex trade-offs
4. Parameter Sensitivity Varies: RR performance highly sensitive to time quantum, while other algorithms show stable behavior across reasonable parameter ranges.

5. Scalability Maintains Relative Performance: Algorithm performance rankings remain consistent across system scales (100-1000 processes), though absolute differences increase.
6. Fairness-Performance Trade-off: Algorithms optimizing for performance (SJF, SRTF) reduce fairness, while fair algorithms (FCFS, RR) sacrifice some performance.

## 6.2 Algorithm Recommendations

For Specific Use Cases:

1. Batch Processing Systems:
  - Primary: SJF (non-preemptive)
  - Alternative: Priority with long quantum
  - Rationale: Minimize turnaround time, CPU utilization maximized
2. Interactive/Time-Sharing Systems:
  - Primary: Round Robin (quantum 10-20ms)
  - Alternative: SRTF for CPU-bound interactive apps
  - Rationale: Ensure responsive feel, prevent starvation
3. Real-Time Systems:
  - Primary: Preemptive Priority with aging
  - Alternative: MLFQ for mixed criticality
  - Rationale: Priority-based deadline meeting
4. General-Purpose Operating Systems:
  - Primary: MLFQ or SRTF
  - Alternative: RR with adaptive quantum
  - Rationale: Balance interactive and batch needs

Parameter Guidelines:

- RR Quantum: 20-50ms for general use, 10-20ms for interactive
- Priority Aging: 500-1000ms intervals to prevent starvation
- MLFQ Configuration: 3-4 queues, quanta doubling each level
- Context Switch Overhead: Model explicitly (1-5ms typical)

## 6.3 Future Work

1. Advanced Algorithm Implementation:
  - Completely Fair Scheduler (CFS) Linux implementation
  - Earliest Deadline First (EDF) for real-time systems
  - Proportional Share scheduling
2. Enhanced Simulation Features:

- Multi-core/multi-processor scheduling
  - Memory and I/O subsystem modeling
  - Energy-aware scheduling algorithms
  - Virtual machine scheduling
3. Improved Workload Modeling:
- Real workload traces from production systems
  - Self-similar and fractal workload patterns
  - Dependency modeling between processes
4. Analysis Extensions:
- Machine learning for adaptive parameter tuning
  - Formal verification of scheduling properties
  - Economic models for resource allocation
5. Practical Applications:
- Integration with OS development frameworks
  - Educational tool with interactive visualization
  - Cloud resource scheduling simulation

## 6.4 Project Contributions

This project provides:

1. A comprehensive, modular simulation framework for CPU scheduling evaluation
2. Empirical evidence of algorithm performance across diverse workloads
3. Visual analysis tools for intuitive performance comparison
4. Configurable experimentation platform for further research
5. Educational resource for operating system concepts

The implementation demonstrates that careful algorithm selection based on workload characteristics can yield significant performance improvements, with potential turnaround time reductions of 30-40% and response time improvements of 10-20x compared to naive scheduling approaches.

## 7. References

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## 8. Appendices

### Appendix A: Source Code Structure

Complete project available at: [cpu\\_scheduler\\_simulation/](https://github.com/abhinavkumar1999/cpu_scheduler_simulation/)

Key Files:

- `src/main.py` - Main simulation controller
- `src/simulator.py` - Discrete-event simulation engine
- `src/pcb.py` - Process Control Block definition
- `src/schedulers/` - All algorithm implementations
- `src/visualizer.py` - Graphing and visualization
- `experiments/` - Experiment scripts
- `tests/` - Unit test suite

### Appendix B: Sample Outputs

Sample Console Output:

=== Running FCFS Simulation ===

Processes: 500, Type: mixed

=====

Completed Processes: 500

Average Turnaround Time: 1245.3 ms

Average Waiting Time: 895.2 ms

Average Response Time: 895.2 ms

CPU Utilization: 78.4%

Throughput: 4.2 processes/sec

Fairness Index: 0.993

Context Switches: 0

Preemptions: 0

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