CPE301 – SPRING 2019

Design Assignment 3A

Student Name: Armon Latifi Student #: 2000698173

Student Email: latifa1@unlv.nevada.edu

Primary Github address: https://github.com/armonlatifi

Directory: https://github.com/armonlatifi/sub_da/tree/master/DA3A

Submit the following for all Labs:

1. In the document, for each task submit the modified or included code (only) with highlights and justifications of the modifications. Also, include the comments.

- Use the previously create a Github repository with a random name (no CPE/301, Lastname, Firstname). Place all labs under the root folder ESD301/DA, sub-folder named LABXX, with one document and one video link file for each lab, place modified asm/c files named as LabXX-TYY.asm/c.
- 3. If multiple asm/c files or other libraries are used, create a folder LabXX-TYY and place these files inside the folder.
- 4. The folder should have a) Word document (see template), b) source code file(s) and other include files, c) text file with youtube video links (see template).

1. COMPONENTS LIST AND CONNECTION BLOCK DIAGRAM w/ PINS

List of Components used:

- Assembler
- Simulator
- Debugger
- Breadboard
- Atmega328P
- Wires
- Microusb cord
- Atmel studio 7

ATMEGA328

(PCINTO/CLKO/ICP1) PB0 [14

#include <avr/io.h>

- Arudino Multi-function shield

(PCINT14/RE2 28 ☐ PC5 (ADC5/SCL/PCINT13) (PCINT16 () PD0 1 3 26 ☐ PC3 (ADC3/PCINT11) (PCINT17 () PD1 3 26 ☐ PC3 (ADC3/PCINT11) (PCINT18/IN 25 ☐ PC2 (ADC2/PCINT10) INT19/OC2B/INT1 5 24 ☐ PC1 (ADC1/PCINT9)

25 PC2 (ADC2/PCINT10) (PCINT18/IN (PCINT19/OC2B/INT1) 24 PC1 (ADC1/PCINT9) 23 PC0 (ADC0/PCINTB) (PCINT20/XCK/) 22 GND 21 AREF (PCINT6/XTAL1/TOSC1) 20 AVCC (PCINT7/XTAL2/TOSC2) PB7 [10 19 PB5 (SCK/PCINT5) (PCINT21/OC0B/T1) PD5 □ 11 18 PB4 (MISO/PCINT4) (PCINT22/OC0A/AIN0) PD6 ☐ 12 (PCINT23/AIN1) PD7 ☐ 13 17 PB3 (MOSI/OC2A/PCINT3) 16 PB2 (SS/OC1B/PCINT2)

2. INITIAL/MODIFIED/DEVELOPED CODE OF TASK 1/A

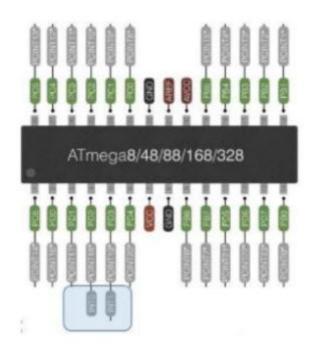
15 PB1 (OC1A/PCINT1)

#define F_CPU 16000000UL //set clock frequency #define BAUD 9600 //set baud rate

```
USART_init();
        sei();//interrupt enable
        while(1)
}
ISR(TIMER1_OVF_vect)
{
        USART_tx_string(str);
        USART_tx_string("\n"); //takes care of line feed
        USART_send('7');
        USART_tx_string("\n"); //takes care of line feed
        //printing time
        snprintf(outs,sizeof(outs),"%f\r\n", tmp);
        USART_tx_string(outs);
        USART_tx_string("\n"); //takes care of line feed
        TCNT1 = 49911; //reset
}
void USART_init( void )
        UBRR0H = 0; //take care of lower bits
        UBRR0L = F_CPU/16/BAUD - 1; //
        UCSR0B = _BV(RXEN0) | _BV(TXEN0);
        UCSROC = BV(UCSZO1) \mid BV(UCSZOO);
}
//sending data to serial port
void USART_tx_string(char *data)
{
        while((*data != '\0'))
        {
                while(!(UCSR0A & (1<<UDRE0)));
                        UDR0 = *data;
                                data++;
        }
}
void USART_send(char val)
//wait
        while(!(UCSR0A & (1<<UDRE0)));
        UDR0 = val;
}
```

3. SCHEMATICS

Use fritzing.org

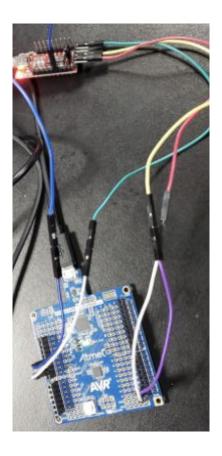


4. SCREENSHOTS OF EACH TASK OUTPUT (ATMEL STUDIO OUTPUT)

Receive

Hello world!! 24 3.14158 Hello world!! 24 3.14158

5. SCREENSHOT OF EACH DEMO (BOARD SETUP)



6. GITHUB LINK OF THIS DA https://github.com/armonlatifi/sub_da/tree/master/DA3A

Student Academic Misconduct Policy http://studentconduct.unlv.edu/misconduct/policy.html

"This assignment submission is my own, original work".

Armon Latifi