

eSim

An open source EDA tool

Circuit
Simulation
Project

oFj8W



Certificate of Participation

*This is to certify that
Arnab Mondal*

*from Vellore Institute Of Technology, Chennai
has successfully completed the circuit simulation of*

*Design Of Excess-3 Code To Bcd Code
under eSim Circuit Simulation Project*

Prof. Kannan Moudgalya

Principal Investigator - FOSSEE

(Free/libre and Open Source Software in Education)

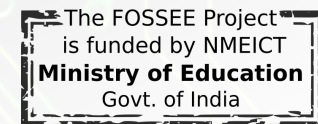
IIT Bombay

Prof. Pramod Murali

Co-Principal Investigator - FOSSEE

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IIT Bombay



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To verify, scan the QR code or visit: <https://esim.fossee.in/circuit-simulation-project/certificates/verify>