

Certificate of Participation

This is to certify that Arnab Mondal

from Vellore Institute Of Technology, Chennai has successfully completed the circuit simulation of

Half Adder Using Nor Gates

under eSim Circuit Simulation Project



Prof. Kannan Moudgalya

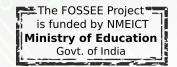
Principal Investigator - FOSSEE (Free/libre and Open Source Software in Education) IIT Bombay Prof. Pramod Murali

Co-Principal Investigator - FOSSEE
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