

eSim

An open source EDA tool

Circuit
Simulation
Project

iGya9



Certificate of Participation

*This is to certify that
Arnab Mondal*

*from Vellore Institute Of Technology, Chennai
has successfully completed the circuit simulation of*

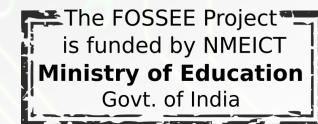
*Full Adder Using Nand Gates
under eSim Circuit Simulation Project*

Prof. Kannan Moudgalya

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