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# Assignment 4

## Question 1

### Objective

Implement a POST or power-on-self-test where each RAM location is tested for stuck-at-zero or stuck-at-one fault. In your case the function takes the start address of the RAM block and the block size in bytes. The function sets CY in case of any error (else it is set to 0); HL contains the faulty location and Acc contains 0 for stuck at zero fault and 1 for stuck at one fault.

[Note: usually there wont be any error as your RAM is not faulty -- so direct checking may not set CY flag.]

### Tool / Experimental setup considered

- Used [Jubin's 8085 Simulator](#).

### Procedure

We iterate through the block of memory and check if the value is 1. Once we get 1 we exit setting the C flag.

### Program

```
// Assuming 8004 is faulty
LXI H,8004
MVI M,01
// Checking from 805 till 5 bytes
LXI H,8000
MVI B,05
CALL CHECK
HLT

CHECK:
    MOV A,M
    ANI 01          // Doing ANDImmediate with 01
    JNZ FAULTY     // If Z is set
    INX H
    DCR B
    JNZ CHECK
    CMC
```

STC  
RET

FAULTY:

STC // set Cy 1  
RET

## Experimentation

Assuming that 8004 is faulty.

RegistersMemoryDevices

Registers :

Register	Value	7	6	5	4	3	2	1	0
Accumulator	01	0	0	0	0	0	0	0	1
Register B	01	0	0	0	0	0	0	0	1
Register C	00	0	0	0	0	0	0	0	0
Register D	00	0	0	0	0	0	0	0	0
Register E	00	0	0	0	0	0	0	0	0
Register H	80	1	0	0	0	0	0	0	0
Register L	04	0	0	0	0	0	1	0	0
Memory(M)	01	0	0	0	0	0	0	0	1

Resister	Value	S	Z	*	AC	*	P	*	CY
Flag Resister	11	0	0	0	1	0	0	0	1

Type	Value
Stack Pointer(SP)	0000
Memory Pointer (HL)	8004
Program Status Word(PSW)	0111
Program Counter(PC)	000D
Clock Cycle Counter	262
Instruction Counter	35

When nothing is faulty.

Registers	Memory	Devices							
Registers :									
Register	Value	7	6	5	4	3	2	1	0
Accumulator	00	0	0	0	0	0	0	0	0
Register B	00	0	0	0	0	0	0	0	0
Register C	00	0	0	0	0	0	0	0	0
Register D	00	0	0	0	0	0	0	0	0
Register E	00	0	0	0	0	0	0	0	0
Register H	80	1	0	0	0	0	0	0	0
Register L	05	0	0	0	0	0	1	0	1
Memory(M)	00	0	0	0	0	0	0	0	0
Resister	Value	S	Z	*	AC	*	P	*	CY
Flag Resister	54	0	1	0	1	0	1	0	0
Type	Value								
Stack Pointer(SP)	0000								
Memory Pointer (HL)	8005								
Program Status Word(PSW)	0054								
Program Counter(PC)	0008								
Clock Cycle Counter	260								
Instruction Counter	37								
SOD	SID	INTR	TRAP	R7.5	R6.5	R5.5			
0	0	0	0	0	0	0			

## Conclusion

We can see that the memory pointer is at 8004 and the C flag is 1 hence it suggests that the memory address 8004 is faulty. In the second scenario, where we had no faulty, we can see that C flag remains 0.

Hence, our code is working properly.

## Question 2

## Objective

Implement a binary search --- the function would take the start address and no. of elements in the array. If successful the function resets CY flag and the HL pair points to the element found else CY is set and the value in HL pair is irrelevant.

## Tool / Experimental setup considered

- Used [Jubin's 8085 Simulator](#).

## Procedure

We keep two pointers, one low(Register B) and another high(Register C). We will check if the mid of this two pointer is the value we are looking for. If we found the value then we set the CY flag and break, else we keep the CY flag unset.

**NOTE:** We are assuming that the array is sorted

## Program

```
# ORG 2000H
# ARR : DB 1,2,3,4,5
# ORG 0000H
# N EQU 5
# X EQU 2
```

```
MVI C, N
DCR C
MVI B, 00
MVI D, X
CALL SEARCH
LXI H, ARR
ADD L
MOV L, A
JNC NAD
INR H
```

```
NAD:
MOV A, D
CMP M
JZ END
JC END
HLT
```

```
END:
CMC
HLT
```

```
SEARCH:
MOV A, B
CMP C
RNC
ADD C
JNC NOCARRY
CMC
```

```
NOCARRY:
RAR
MOV E, A
LXI H, ARR
ADD L
MOV L, A
JNC NOTFOUND
INR H
```

```
NOTFOUND:
MOV A, D
CMP M
JC LEFT
JZ END
```

```

MOV B,E
INR B
JMP SEARCH

```

```

LEFT:
MOV C,E
DCR C
JNZ SEARCH
JZ END
RET

```

## Experimentation

When the array is {1, 2, 3, 4, 5} and we are looking for 2 .

The screenshot shows the 8085 microprocessor simulator. The **Registers** window displays the following data:

Register	Value	7	6	5	4	3	2	1	0
Accumulator	02	0	0	0	0	0	0	1	0
Register B	01	0	0	0	0	0	0	0	1
Register C	01	0	0	0	0	0	0	0	1
Register D	02	0	0	0	0	0	0	1	0
Register E	00	0	0	0	0	0	0	0	0
Register H	20	0	0	1	0	0	0	0	0
Register L	01	0	0	0	0	0	0	0	1
Memory(M)	02	0	0	0	0	0	0	1	0

Below the registers, the **Flag Register** is shown with the following status:

Resister	Value	S	Z	*	AC	*	P	*	CY
Flag Resister	55	0	1	0	1	0	1	0	1

The **Memory Editor** window shows the memory range from 0000 to FFFF. The array {1, 2, 3, 4, 5} is stored at addresses 2000 to 2004. The value 02 at address 2001 is highlighted by a red box.

When the array is {1, 2, 3, 4, 5} and we are looking for 8 .

The screenshot shows the 8085 microprocessor simulator. The **Registers** window displays the following data:

Register	Value	7	6	5	4	3	2	1	0
Accumulator	08	0	0	0	0	1	0	0	0
Register B	04	0	0	0	0	0	1	0	0
Register C	04	0	0	0	0	0	1	0	0
Register D	08	0	0	0	0	1	0	0	0
Register E	03	0	0	0	0	0	0	1	1
Register H	20	0	0	1	0	0	0	0	0
Register L	04	0	0	0	0	0	1	0	0
Memory(M)	05	0	0	0	0	0	1	0	1

Below the registers, the **Flag Register** is shown with the following status:

Resister	Value	S	Z	*	AC	*	P	*	CY
Flag Resister	14	0	0	0	1	0	1	0	0

The **Memory Editor** window shows the memory range from 0000 to FFFF. The array {1, 2, 3, 4, 5} is stored at addresses 2000 to 2004. The value 04 at address 2004 is highlighted by a red box.

## Conclusion

Clearly, when the element is found in the array the **cy** flag was set, and the address pointed by the memory pointer was the value we are looking for. On the other hand when the element is not there in the array we see that the **cy** flag remains unset. Hence, our program is working as expected.