Switching Circuit & Logic Design

Lecture 19 :
Programmable Logic
Devices

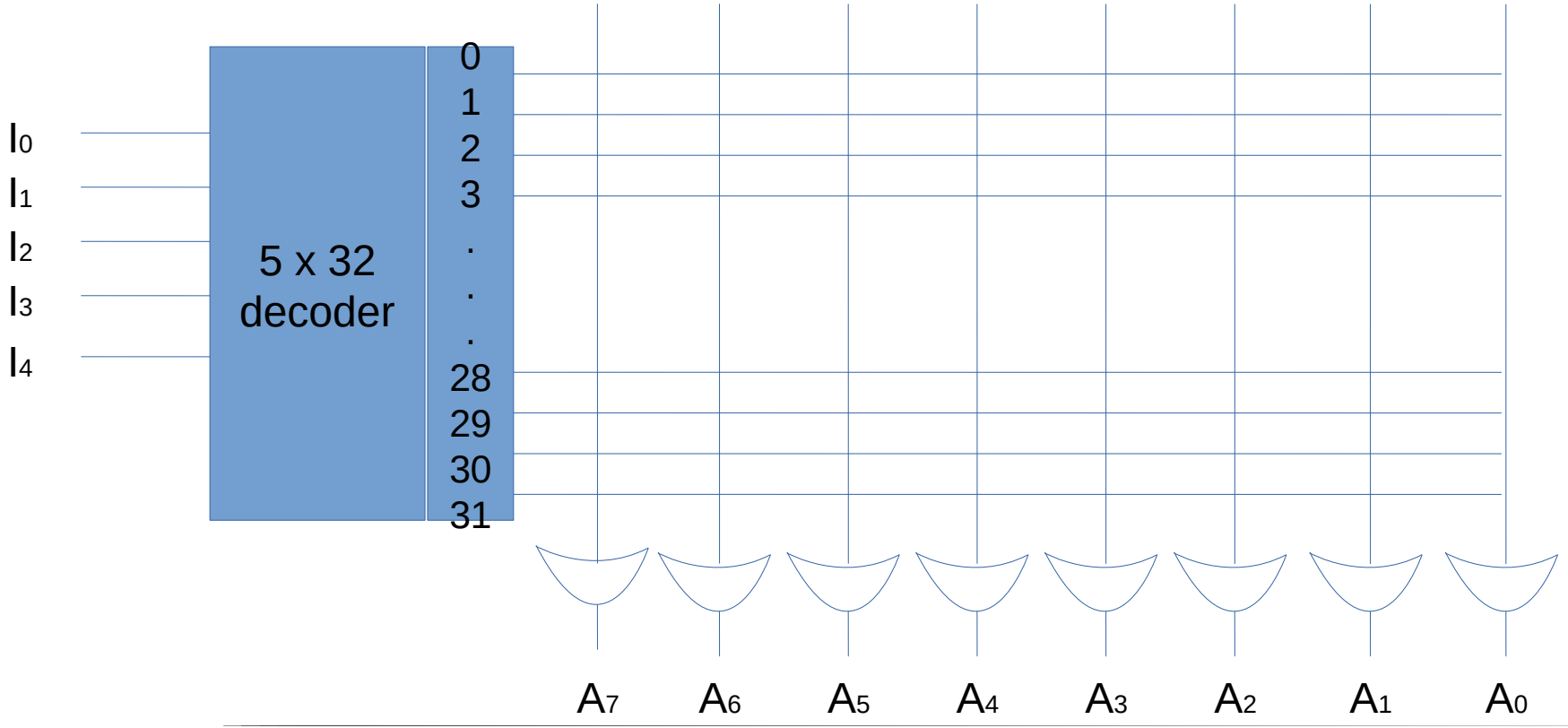
Programmable Logic Devices

- PROM – Programmable Read Only Memory
- PLA – Programmable Logic Array
- PAL – Programmable Array Logic

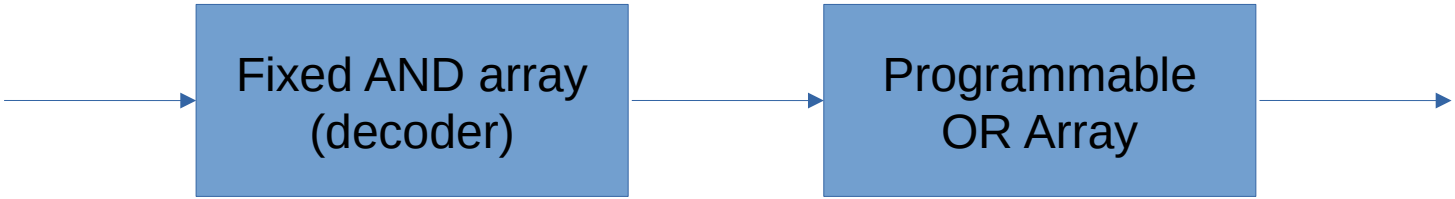
Advantages

- Low development cost
- Less space requirement
- Less power requirement
- High reliability
- Easy circuit testing
- Easy design modification
- High design security
- Less design time
- High switching speed

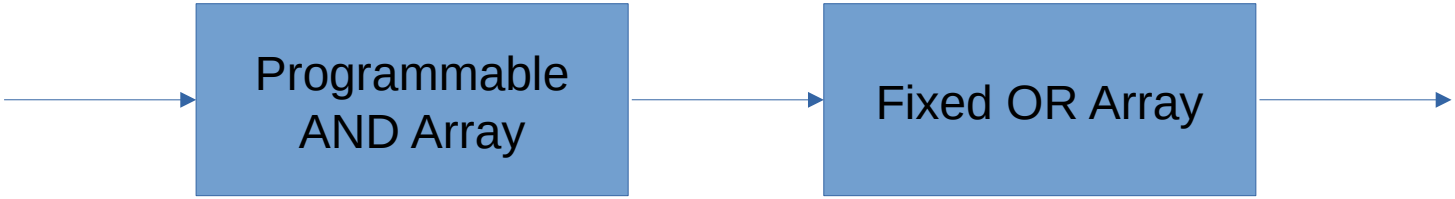
Read only memory



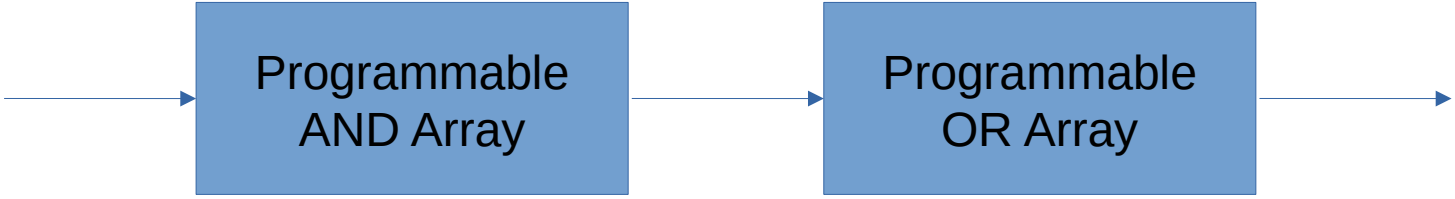
Combinational Programmable LD



Programmable read-only memory (PROM)

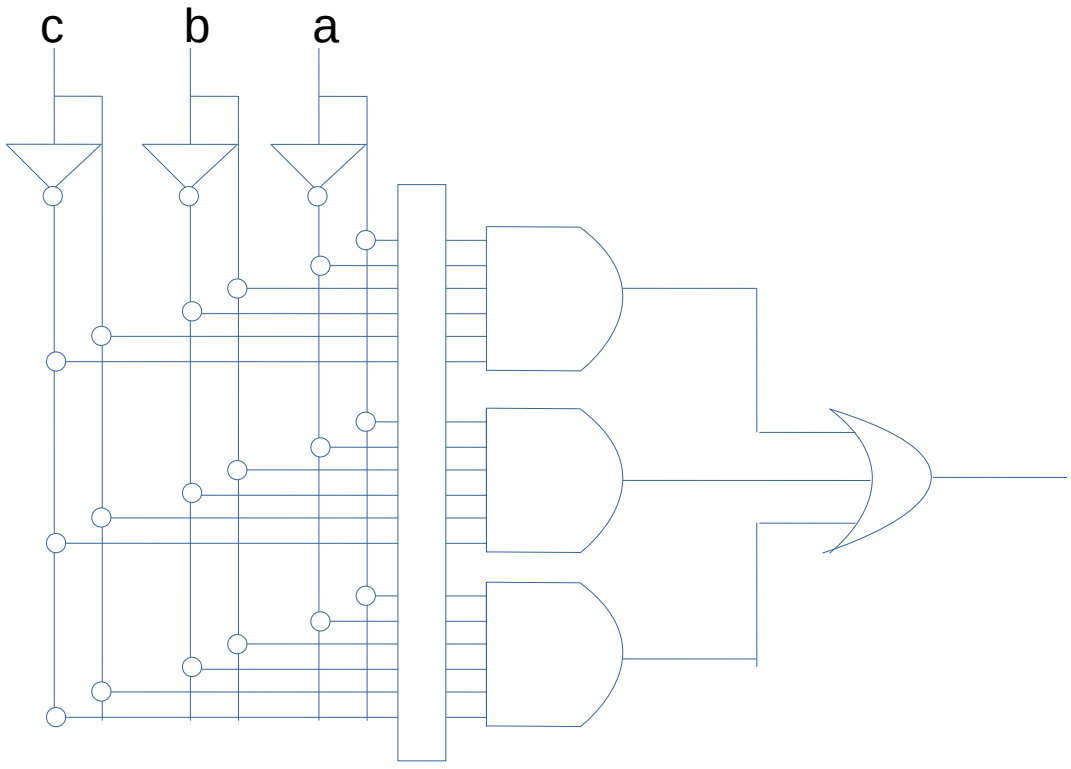


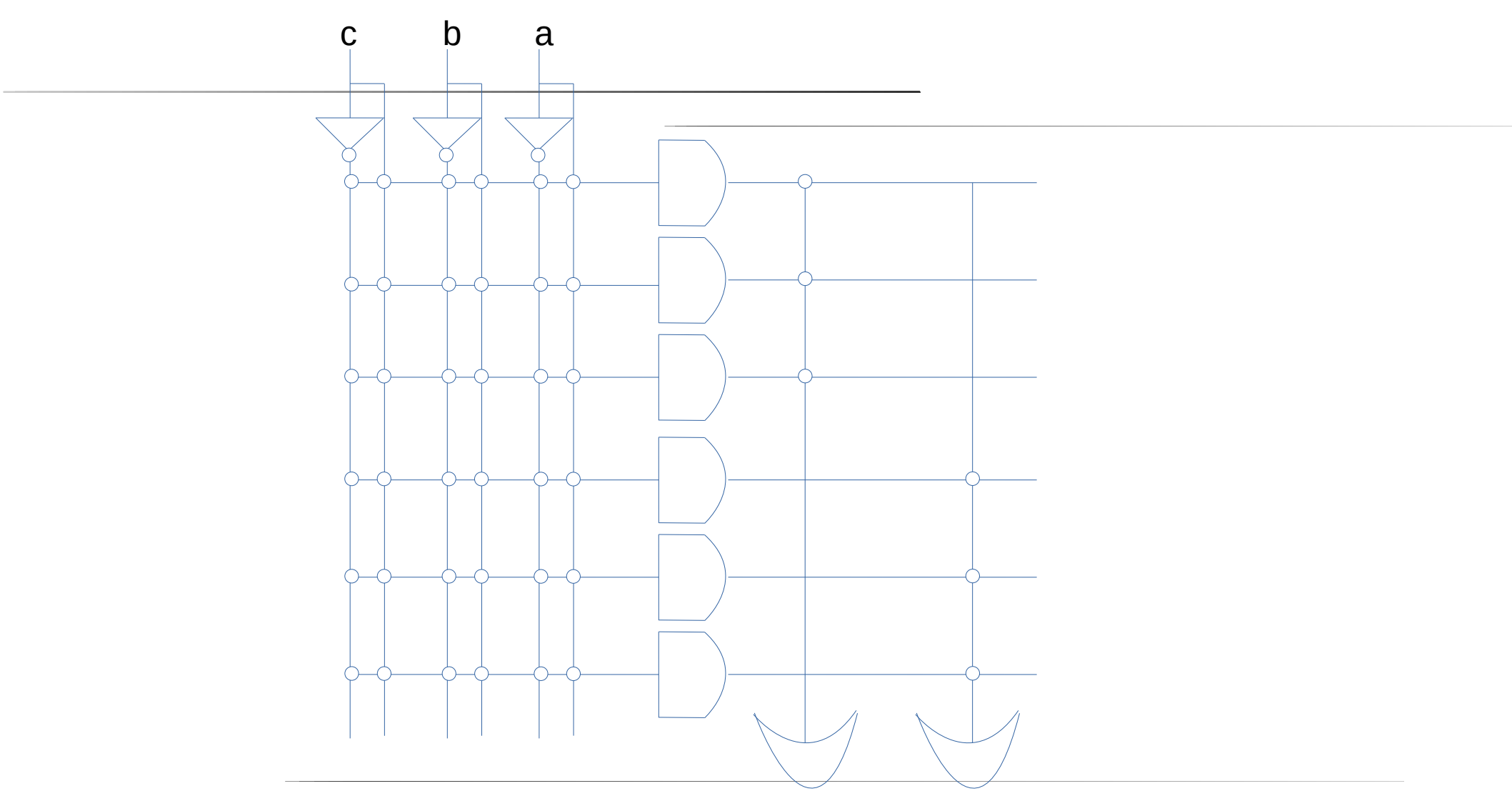
Programmable Array Logic (PAL)



Programmable Logic Array (PLA)

Programmable Array Logic (PAL)

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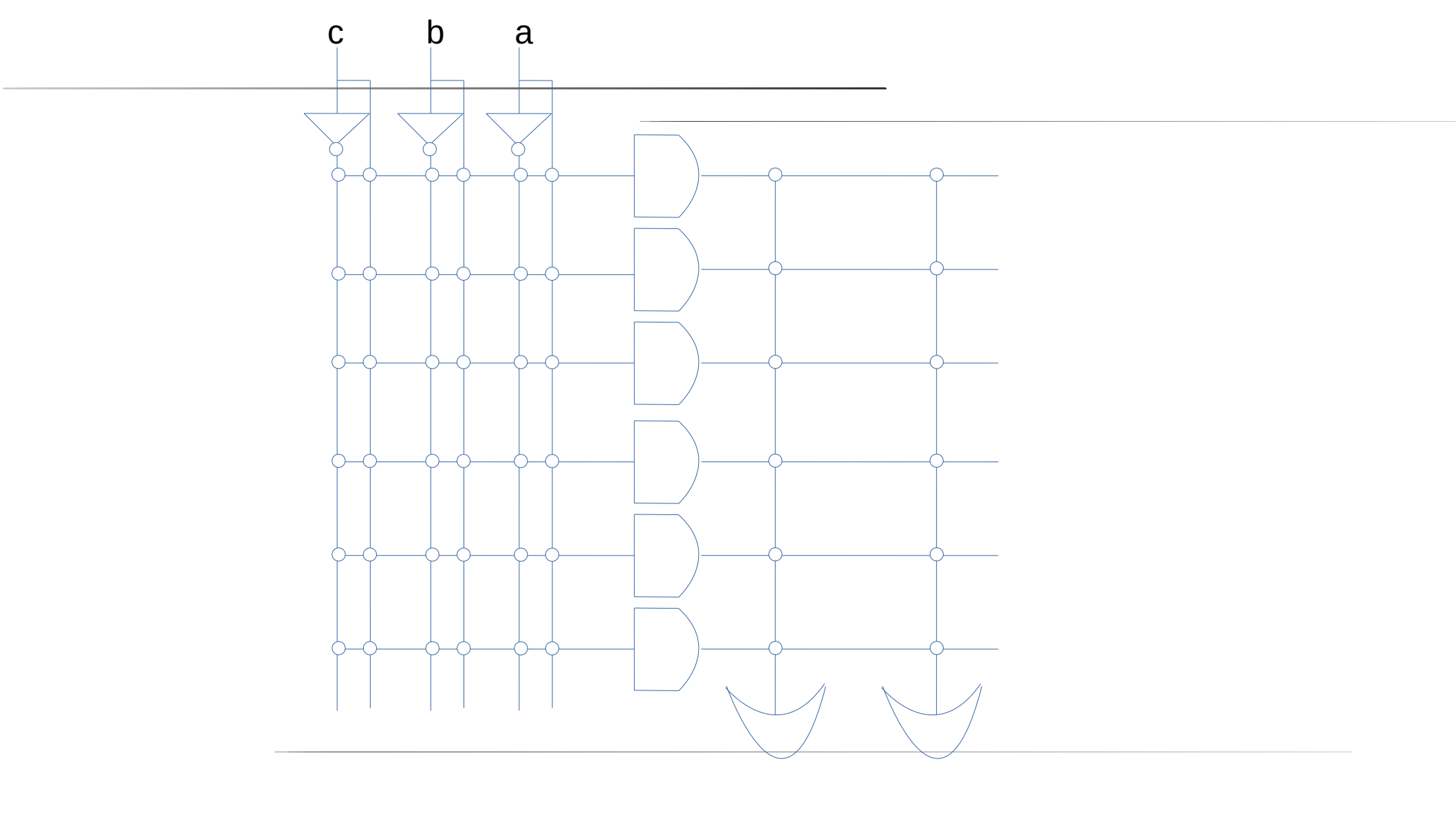
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