

Symbolic execution for security researchers

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About

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Slides are here!

Preliminaries

Expectations

- Aims to be pretty introductory
- Demystify symbolic execution for a non-specialized audience
- Focus on understanding ideas rather than specific tooling
 - Apply it to your own areas of interest
 - Make it easy to use any tools (and understand what you are doing)
 - Make it easy to contribute to/write your own (open source) tools

Calculator

Concrete calculations

$$\begin{vmatrix} 1 & 2 \\ 3 & 4 \end{vmatrix} = 4 - 2 \cdot 3 = -2$$

Calculator

Concrete calculations

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Computer Algebra System (CAS)

Symbolic calculations and expression manipulation

$$\begin{vmatrix} 1 & 2 \\ a & 4 \end{vmatrix} = 4 - 2a = 2(2 - a)$$

Intermediate representation / language (IR/IL)

Language of an abstract machine designed to aid in the analysis of computer programs:

- Compilation: common ground for architecture independent processing
- Decompilation (binary analysis): lifting from ASM to canonical higher level representation
- Transpiling: source to source compilation

What is symbolic execution?

Roughly speaking, just a computer algebra system for:

- Programming languages: C, C++, Java, Rust...
- Assembly languages: x86, x86-64, ARM64, MIPS, RISC-V...
- Intermediate languages: LLVM-IR, SMT-LIB, r2 ESIL, IDA Microcode, \$YOUR_OWN...

More specifically, symbolic execution is a **program analysis technique**:

- Represent inputs as *symbolic* variables instead of *concrete* values (normal execution or emulation)
- Derive constraints that encode control-flow and data-flow with respect to these symbolic variables

Use these constraints to reason about and extract information from the program

```
int foo(int x, int y) {
    x = y - 3*x;
    if (x < y) {
        return 2*x - x^y;
    }
    else {
        return 3*y + x | y;
    }
}</pre>
```

```
int foo(int x, int y) {
    x = y - 3*x;
    if (x < y) {
        return 2*x - x^y;
    }
    else {
        return 3*y + x | y;
    }
}</pre>
```

$$x = 1337, y = 7331$$

 $x = 7331 - 3 \times 1337 = 3320$
 $(3320 < 7331)$
 $2 \times 3320 - 1337 \oplus 7331 = 2068$
 $\Rightarrow 2068$

```
int foo(int x, int y) {
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Symbolic execution

$$x = \mathbf{x}, y = \mathbf{y}$$
$$\mathbf{x} = \mathbf{y} - 3\mathbf{x}$$

```
int foo(int x, int y) {
    x = y - 3*x;
    if (x < y) {
        return 2*x - x^y;
    }
    else {
        return 3*y + x | y;
    }
}</pre>
```

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Symbolic execution

$$x = \mathbf{x}, y = \mathbf{y}$$

 $\mathbf{x} = \mathbf{y} - 3\mathbf{x}$

$$(\mathbf{y} - 3\mathbf{x} < \mathbf{y})$$

$$\hookrightarrow 2(\mathbf{y} - 3\mathbf{x}) - (\mathbf{y} - 3\mathbf{x}) \oplus \mathbf{y}$$

```
int foo(int x, int y) {
    x = y - 3*x;
    if (x < y) {
        return 2*x - x^y;
    }
    else {
        return 3*y + x | y;
    }
}</pre>
```

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Symbolic execution

$$x = \mathbf{x}, y = \mathbf{y}$$
$$\mathbf{x} = \mathbf{y} - 3\mathbf{x}$$

$$(\mathbf{y} - 3\mathbf{x} < \mathbf{y})$$

$$(\mathbf{y} - 3\mathbf{x} \ge \mathbf{y})$$

$$(\mathbf{y} - 3\mathbf{x}) - (\mathbf{y} - 3\mathbf{x}) \oplus \mathbf{y}$$

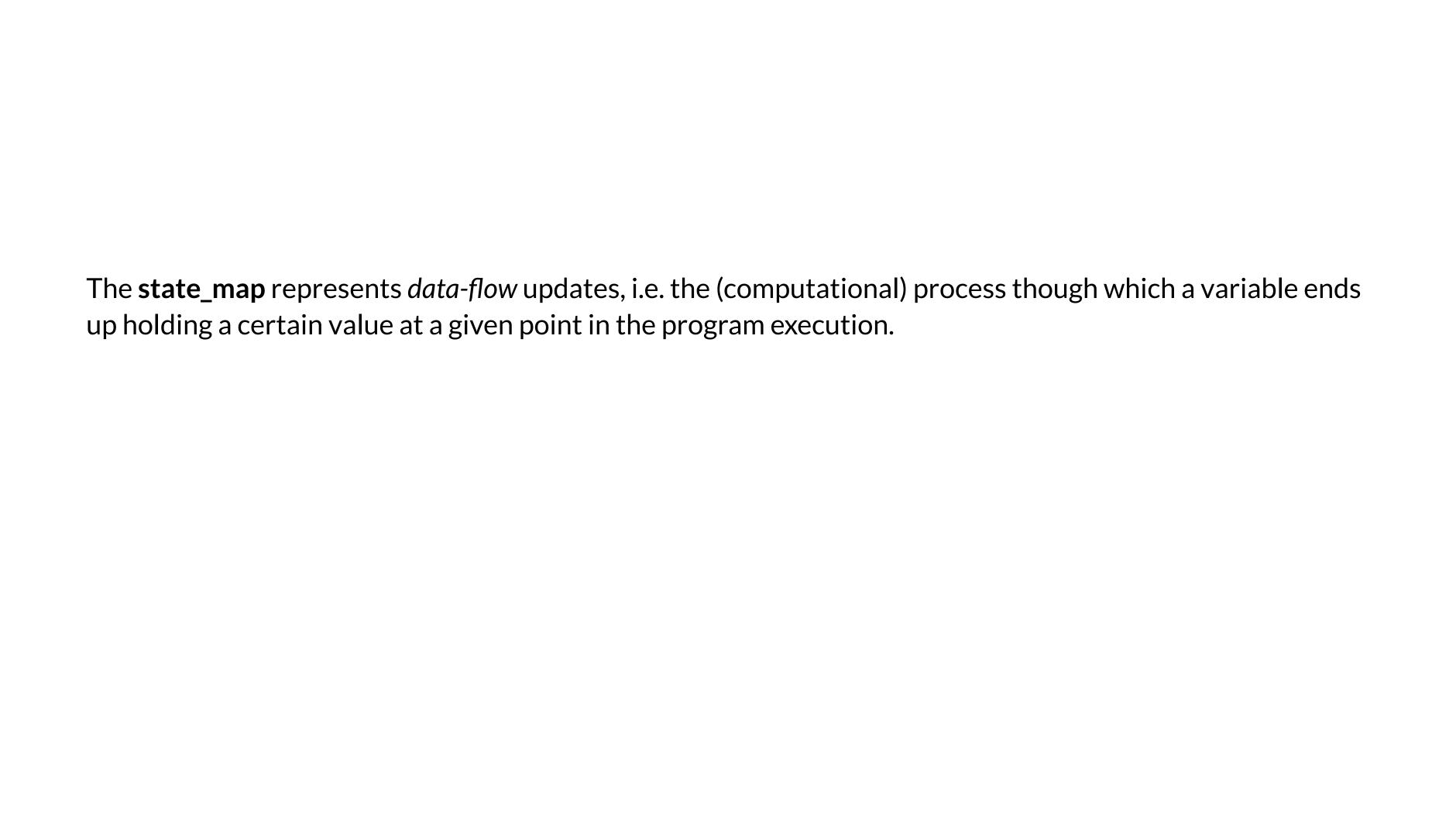
$$(\mathbf{y} - 3\mathbf{x} \ge \mathbf{y})$$

$$(\mathbf{y} - 3\mathbf{x}) - (\mathbf{y} - 3\mathbf{x}) \vee \mathbf{y}$$

But how does it *actually* work?

1. Define two data structures:

- path_constraint: conditions required to reach current instruction
- **state_map**: symbolic mapping for the variables (registers, memory locations)
- 2. Extract the semantics of each statement (instruction)
- 3. Update these two data structures to account for the effects of the executed statement (instruction)
- 4. If there is control-flow branching, *fork* these structures to keep track of different execution paths



The **state_map** represents *data-flow* updates, i.e. the (computational) process though which a variable ends up holding a certain value at a given point in the program execution. The path_constraint represents control-flow tracking, i.e. the set of constraints (conditions) on the variables that need to be satisfied for the execution to reach a given point in the program.

Visual example

```
start:
                                                   cmp rax, 1337
    mov rax, 123 <=0=
                                                   jnz bad
    add rax, rsi
    xor rax, rdi
                                              good:
                                                   xor rdi, rdi
    mov rbx, 2
    add rax, rbx
                                                   jmp exit
    mov rdi, 3
                                              bad:
    mov rsi, rax
    add rax, rbx
                                                  mov rdi, 1
    xor rax, rdi
    mov rbx, 7
                                              exit:
    and rax, rbx
                                                   mov rax, 60
    mov rdi, 1336
                                                   syscall
    add rax, rdi
path_constraint true
state_map
                 rax -> rax
                 rbx -> rbx
                 rdi -> rdi
                 rsi -> rsi
                  zf \rightarrow zf
```

```
start:
                                                 cmp rax, 1337
                                                 jnz bad
    mov rax, 123
    add rax, rsi <=0=
    xor rax, rdi
                                             good:
                                                 xor rdi, rdi
    mov rbx, 2
    add rax, rbx
                                                 jmp exit
    mov rdi, 3
                                             bad:
    mov rsi, rax
    add rax, rbx
                                                 mov rdi, 1
    xor rax, rdi
    mov rbx, 7
                                             exit:
    and rax, rbx
                                                 mov rax, 60
    mov rdi, 1336
                                                 syscall
    add rax, rdi
path_constraint true
                 rax -> 123
state_map
                 rbx -> rbx
                 rdi -> rdi
                 rsi -> rsi
                 zf -> zf
```

```
start:
                                                  cmp rax, 1337
                                                  jnz bad
    mov rax, 123
    add rax, rsi
    xor rax, rdi <=0=</pre>
                                              good:
    mov rbx, 2
                                                  xor rdi, rdi
    add rax, rbx
                                                  jmp exit
    mov rdi, 3
                                              bad:
    mov rsi, rax
    add rax, rbx
                                                  mov rdi, 1
    xor rax, rdi
    mov rbx, 7
                                              exit:
    and rax, rbx
                                                  mov rax, 60
    mov rdi, 1336
                                                  syscall
    add rax, rdi
path_constraint true
                 rax -> (123 + rsi)
state_map
                 rbx -> rbx
                 rdi -> rdi
                 rsi -> rsi
                 zf -> zf
```

```
start:
                                                 cmp rax, 1337
                                                 jnz bad
    mov rax, 123
    add rax, rsi
    xor rax, rdi
                                             good:
    mov rbx, 2 <=0=
                                                 xor rdi, rdi
    add rax, rbx
                                                 jmp exit
    mov rdi, 3
                                             bad:
    mov rsi, rax
    add rax, rbx
                                                 mov rdi, 1
    xor rax, rdi
    mov rbx, 7
                                             exit:
    and rax, rbx
                                                 mov rax, 60
    mov rdi, 1336
                                                 syscall
    add rax, rdi
path_constraint true
                 rax -> ((123 + rsi) ^ rdi)
state_map
                 rbx -> rbx
                 rdi -> rdi
                 rsi -> rsi
                 zf -> zf
```

```
start:
                                                 cmp rax, 1337
                                                 jnz bad
    mov rax, 123
    add rax, rsi
    xor rax, rdi
                                             good:
    mov rbx, 2
                                                 xor rdi, rdi
    add rax, rbx <=0=
                                                 jmp exit
    mov rdi, 3
                                             bad:
    mov rsi, rax
    add rax, rbx
                                                 mov rdi, 1
    xor rax, rdi
    mov rbx, 7
                                             exit:
    and rax, rbx
                                                 mov rax, 60
    mov rdi, 1336
                                                 syscall
    add rax, rdi
path_constraint true
                 rax -> ((123 + rsi) ^ rdi)
state_map
                 rbx -> 2
                 rdi -> rdi
                 rsi -> rsi
                 zf -> zf
```

```
start:
                                                 cmp rax, 1337
                                                 jnz bad
    mov rax, 123
    add rax, rsi
    xor rax, rdi
                                             good:
    mov rbx, 2
                                                 xor rdi, rdi
    add rax, rbx
                                                 jmp exit
    mov rdi, 3 <=0=
                                             bad:
    mov rsi, rax
    add rax, rbx
                                                 mov rdi, 1
    xor rax, rdi
    mov rbx, 7
                                             exit:
    and rax, rbx
                                                 mov rax, 60
    mov rdi, 1336
                                                 syscall
    add rax, rdi
path_constraint true
                 rax -> (((123 + rsi) ^ rdi) + 2)
state_map
                 rbx -> 2
                 rdi -> rdi
                 rsi -> rsi
                 zf -> zf
```

```
start:
                                                   cmp rax, 1337
                                                   jnz bad
    mov rax, 123
    add rax, rsi
    xor rax, rdi
                                              good:
    mov rbx, 2
                                                   xor rdi, rdi
    add rax, rbx
                                                   jmp exit
    mov rdi, 3
                                              bad:
    mov rsi, rax <=0=
    add rax, rbx
                                                  mov rdi, 1
    xor rax, rdi
    mov rbx, 7
                                              exit:
    and rax, rbx
                                                   mov rax, 60
    mov rdi, 1336
                                                   syscall
    add rax, rdi
path_constraint true
                 rax -> (((123 + rsi) ^ rdi) + 2)
state_map
                 rbx -> 2
                 rdi -> 3
                 rsi -> rsi
                  zf \rightarrow zf
```

```
start:
                                                   cmp rax, 1337
                                                   jnz bad
    mov rax, 123
    add rax, rsi
    xor rax, rdi
                                              good:
    mov rbx, 2
                                                   xor rdi, rdi
    add rax, rbx
                                                   jmp exit
    mov rdi, 3
                                              bad:
    mov rsi, rax
    add rax, rbx <=0=
                                                  mov rdi, 1
    xor rax, rdi
    mov rbx, 7
                                              exit:
    and rax, rbx
                                                  mov rax, 60
    mov rdi, 1336
                                                   syscall
    add rax, rdi
path_constraint true
                 rax -> (((123 + rsi) ^ rdi) + 2)
state_map
                 rbx -> 2
                 rdi -> 3
                 rsi -> (((123 + rsi) ^ rdi) + 2)
                 zf \rightarrow zf
```

```
start:
                                                  cmp rax, 1337
                                                  jnz bad
    mov rax, 123
    add rax, rsi
    xor rax, rdi
                                              good:
    mov rbx, 2
                                                   xor rdi, rdi
    add rax, rbx
                                                   jmp exit
    mov rdi, 3
                                              bad:
    mov rsi, rax
    add rax, rbx
                                                  mov rdi, 1
    xor rax, rdi <=0=
    mov rbx, 7
                                              exit:
    and rax, rbx
                                                  mov rax, 60
    mov rdi, 1336
                                                   syscall
    add rax, rdi
path_constraint true
                 rax -> ((((123 + rsi) ^ rdi) + 2) + 2)
state_map
                 rbx -> 2
                 rdi -> 3
                 rsi -> (((123 + rsi) ^ rdi) + 2)
                 zf \rightarrow zf
```

```
start:
                                                   cmp rax, 1337
                                                   jnz bad
    mov rax, 123
    add rax, rsi
    xor rax, rdi
                                               good:
    mov rbx, 2
                                                   xor rdi, rdi
    add rax, rbx
                                                   jmp exit
    mov rdi, 3
                                               bad:
    mov rsi, rax
    add rax, rbx
                                                   mov rdi, 1
    xor rax, rdi
    mov rbx, 7 <=0=
                                               exit:
    and rax, rbx
                                                   mov rax, 60
    mov rdi, 1336
                                                   syscall
    add rax, rdi
path_constraint true
                  rax \rightarrow (((((123 + rsi) ^ rdi) + 2) + 2) ^ 3)
state_map
                  rbx -> 2
                  rdi -> 3
                 rsi -> (((123 + rsi) ^ rdi) + 2)
                  zf \rightarrow zf
```

```
start:
                                                   cmp rax, 1337
    mov rax, 123
                                                   jnz bad
    add rax, rsi
    xor rax, rdi
                                               good:
    mov rbx, 2
                                                   xor rdi, rdi
                                                   jmp exit
    add rax, rbx
    mov rdi, 3
                                               bad:
    mov rsi, rax
    add rax, rbx
                                                   mov rdi, 1
    xor rax, rdi
    mov rbx, 7
                                               exit:
    and rax, rbx \leq 0=
                                                   mov rax, 60
    mov rdi, 1336
                                                   syscall
    add rax, rdi
path_constraint true
                 rax -> (((((123 + rsi) ^ rdi) + 2) + 2) ^ 3)
state_map
                 rbx -> 7
                 rdi -> 3
                 rsi -> (((123 + rsi) ^ rdi) + 2)
                  zf \rightarrow zf
```

```
start:
                                                   cmp rax, 1337
    mov rax, 123
                                                   jnz bad
    add rax, rsi
    xor rax, rdi
                                               good:
    mov rbx, 2
                                                   xor rdi, rdi
                                                   jmp exit
    add rax, rbx
    mov rdi, 3
                                               bad:
    mov rsi, rax
    add rax, rbx
                                                   mov rdi, 1
    xor rax, rdi
    mov rbx, 7
                                               exit:
    and rax, rbx
                                                   mov rax, 60
    mov rdi, 1336 <=0=
                                                   syscall
    add rax, rdi
path_constraint true
                 rax -> (((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) & 7)
state_map
                 rbx -> 7
                 rdi -> 3
                 rsi -> (((123 + rsi) ^ rdi) + 2)
                  zf \rightarrow zf
```

```
start:
                                                   cmp rax, 1337
    mov rax, 123
                                                   jnz bad
    add rax, rsi
    xor rax, rdi
                                              good:
    mov rbx, 2
                                                   xor rdi, rdi
                                                   jmp exit
    add rax, rbx
    mov rdi, 3
                                              bad:
    mov rsi, rax
    add rax, rbx
                                                  mov rdi, 1
    xor rax, rdi
    mov rbx, 7
                                              exit:
    and rax, rbx
                                                  mov rax, 60
    mov rdi, 1336
                                                   syscall
    add rax, rdi <=0=
path_constraint true
                 rax -> (((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) & 7)
state_map
                 rbx -> 7
                 rdi -> 1336
                 rsi -> (((123 + rsi) ^ rdi) + 2)
                  zf \rightarrow zf
```

```
start:
                                           cmp rax, 1337 <=0=
                                           jnz bad
   mov rax, 123
   add rax, rsi
   xor rax, rdi
                                        good:
   mov rbx, 2
                                           xor rdi, rdi
   add rax, rbx
                                           jmp exit
   mov rdi, 3
                                        bad:
   mov rsi, rax
   add rax, rbx
                                           mov rdi, 1
   xor rax, rdi
   mov rbx, 7
                                        exit:
   and rax, rbx
                                           mov rax, 60
   mov rdi, 1336
                                           syscall
   add rax, rdi
path_constraint true
               state_map
               rbx -> 7
               rdi -> 1336
               rsi -> (((123 + rsi) ^ rdi) + 2)
               zf \rightarrow zf
```

```
start:
                                     cmp rax, 1337
                                     jnz bad <=0=
   mov rax, 123
   add rax, rsi
   xor rax, rdi
                                  good:
   mov rbx, 2
                                     xor rdi, rdi
   add rax, rbx
                                     jmp exit
   mov rdi, 3
   mov rsi, rax
                                  bad:
   add rax, rbx
                                     mov rdi, 1
   xor rax, rdi
                                  exit:
   mov rbx, 7
   and rax, rbx
                                     mov rax, 60
   mov rdi, 1336
                                     syscall
   add rax, rdi
path_constraint true
             state_map
             rbx -> 7
             rdi -> 1336
            rsi -> (((123 + rsi) ^ rdi) + 2)
             == 1337 ? 1 : 0
```

```
start:
                                                  cmp rax, 1337
                                                  jnz bad
    mov rax, 123
    add rax, rsi
    xor rax, rdi
                                              good:
    mov rbx, 2
                                                  xor rdi, rdi <=1=</pre>
    add rax, rbx
                                                  jmp exit
    mov rdi, 3
                                             bad:
    mov rsi, rax
    add rax, rbx
                                                 mov rdi, 1 <=2=
    xor rax, rdi
    mov rbx, 7
                                             exit:
    and rax, rbx
                                                  mov rax, 60
    mov rdi, 1336
                                                  syscall
    add rax, rdi
path_constraint ((((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) & 7) + 1336) == 1337
state_map
                 zf -> 1
path_constraint ((((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) & 7) + 1336) != 1337
state map
                 zf -> 0
```

How do we *reason* about this information?

How do we *reason* about this information?

With an SMT solver

How do we *reason* about this information?

With an SMT solver

Mostly

SMT solver

SMT solver

Satisfiability Modulo Theories

- Satisfiability (SAT): determine if a (boolean) formula can be satisfied (can be true)
- Modulo: take into account (not only boolean formulas but also)...
- Theories: ...integer numbers, real numbers, floating point, bit vectors, and more

SMT solver

Satisfiability Modulo Theories

- Satisfiability (SAT): determine if a (boolean) formula can be satisfied (can be true)
- Modulo: take into account (not only boolean formulas but also)...
- Theories: ...integer numbers, real numbers, floating point, bit vectors, and more

From a very practical standpoint: a magic black-box that can only answer a very simple question.

Question

Given some variables of some type, and some constraints on these variables:

• Is there any variable assignment that makes the set of constraints satisfiable, i.e. such that (all) the constraints hold true?

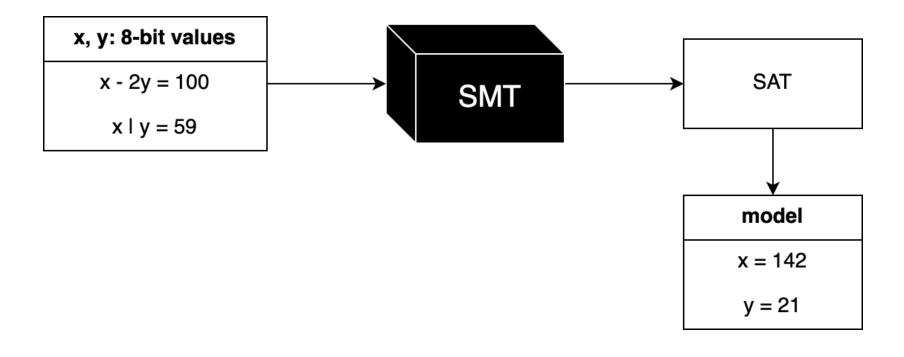
Question

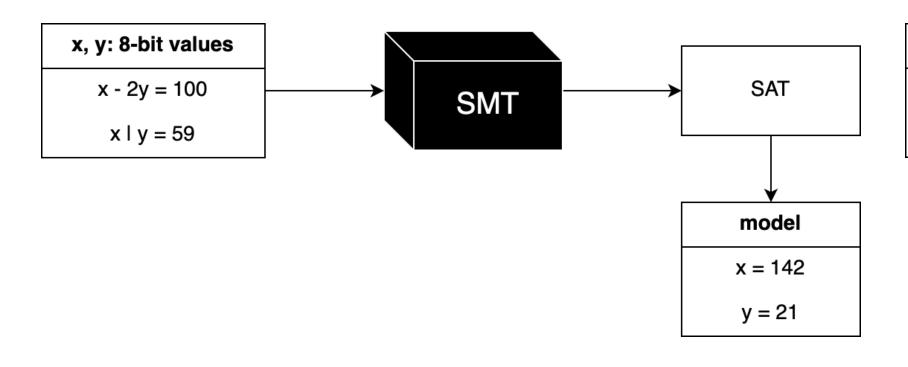
Given some variables of some type, and some constraints on these variables:

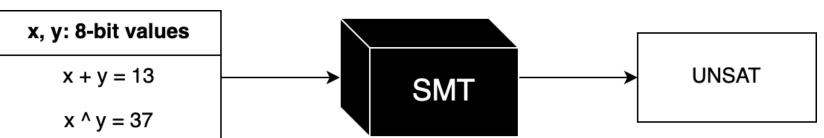
• Is there any variable assignment that makes the set of constraints satisfiable, i.e. such that (all) the constraints hold true?

Outcomes

- SAT: there is a variable assignment that makes all the constraints hold true.
 - It will actually find a model, which is a particular solution (a concrete variable assignment)
- UNSAT: there is NO variable assignment that makes all the constraints hold true.
- UNKNOWN: unable to answer the question (usually due to a time-out)







Symbolic execution + SMT solver

Symbolic execution + SMT solver

Some basic ideas

Data-flow analysis

Data-flow analysis

- Embed compiler optimization techniques into the **state_map** population process:
 - Constant propagation: by construction
 - Constant folding: evaluate intermediate expressions on constant values
 - Reaching definitions: calculate at a given point the set of definitions that reach it
 - Liveness analysis: calculate at a given point the *live* variables (may be read before updated)

1. The symbolic execution engine is used to extract the formula of the return value of a function with respect to its inputs parameters: check its value in the state_map

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2. The formula is fed into the SMT solver

- 1. The symbolic execution engine is used to extract the formula of the return value of a function with respect to its inputs parameters: check its value in the state_map
- 2. The formula is fed into the SMT solver
- 3. The SMT can:
 - Attempt to simplify the formula to get a nicer representation
 - Craft inputs value that will make the formula evaluate to a desired output (i.e. inputs that will make the function return a desired value)

1. The symbolic execution engine is used to extract the formulae (constraints) for a given path branching to happen: check its path_constraint

- 1. The symbolic execution engine is used to extract the formulae (constraints) for a given path branching to happen: check its **path_constraint**
- 2. The constraints are fed into the SMT solver

- 1. The symbolic execution engine is used to extract the formulae (constraints) for a given path branching to happen: check its path_constraint
- 2. The constraints are fed into the SMT solver
- 3. The SMT solver can prove the feasibility of the constraints, meaning the path is reachable
 - If it is, retrieve a model for it, i.e. input values that will make the program execution to reach it
 - If it is not, we have detected an obfuscating opaque predicate and can ignore/patch it away

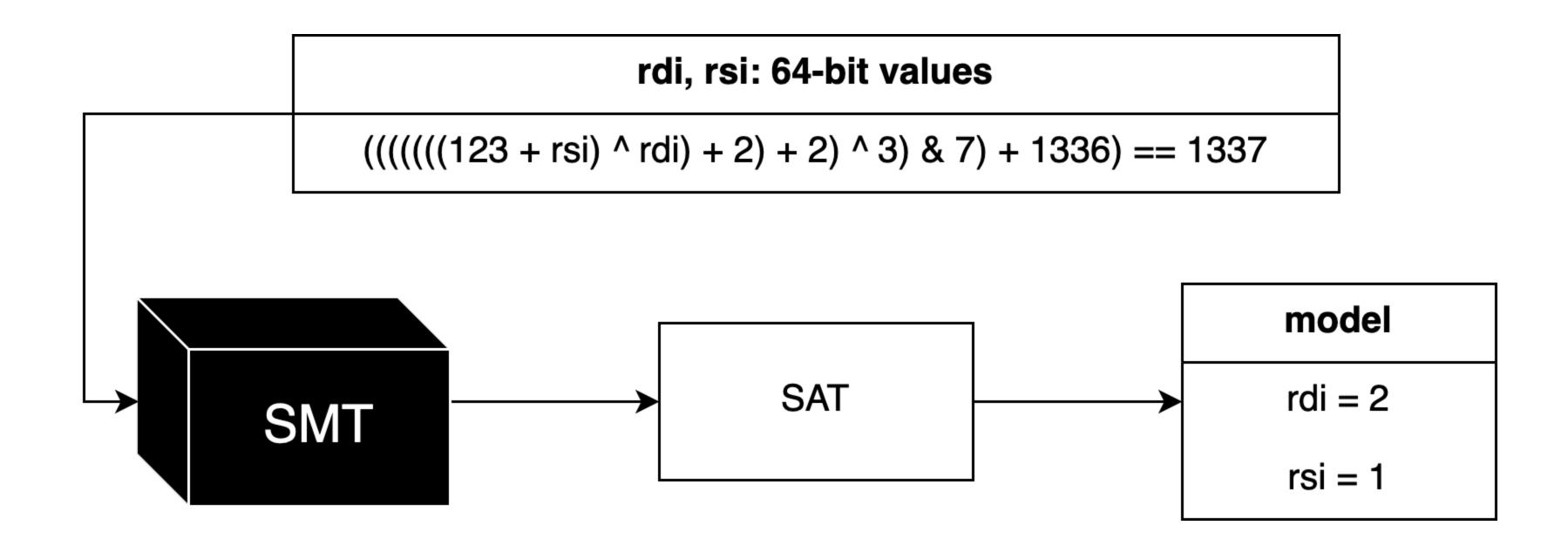
Example

```
path_constraint ((((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) & 7) + 1336) == 1337
```

```
path_constraint ((((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) & 7) + 1336) == 1337
```

Given 64-bit variables rdi and rsi:

• Is there any variable assignment (for rdi and rsi) that makes the path_constraint satisfiable?



```
import z3

rdi, rsi = z3.BitVecs('rdi rsi', 64)
path_constraint = ((((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) & 7) + 1336) == 1337

solver = z3.Solver()
solver.add(path_constraint)

if solver.check() == z3.sat:
    print(solver.model())
```

```
import z3

rdi, rsi = z3.BitVecs('rdi rsi', 64)
path_constraint = ((((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) & 7) + 1336) == 1337

solver = z3.Solver()
solver.add(path_constraint)

if solver.check() == z3.sat:
    print(solver.model())
```

[rdi = 2, rsi = 1]

Tooling

Tooling

Welcome to the jungle

Implementation technology

- Interpreter based: Miasm, Triton, Angr, Maat, radius2
- Instrumentation based: QSYM
- Compiler based: KLEE, SymCC, SymQEMU

Implementation technology

- Interpreter based: Miasm, Triton, Angr, Maat, radius2
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Target

- Binary: Miasm, Triton, Angr, Maat, radius2, QSYM, SymQEMU
- Source code: KLEE, SymCC

Implementation technology

- Interpreter based: Miasm, Triton, Angr, Maat, radius2
- Instrumentation based: QSYM
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Target

- Binary: Miasm, Triton, Angr, Maat, radius2, QSYM, SymQEMU
- Source code: KLEE, SymCC

Focus

- Analysis: Miasm, Triton, Maat
- Automagic: Angr, radius2
- Test generation: QSYM, KLEE, SymCC, SymQEMU

Practical applications

Practical applications

An appetizer

Analysis of complex code

Detect (and patch) opaque predicates

Opaque predicates

A conditional statement ${\it P}$ whose truth value is known a priori.

Opaque predicates

A conditional statement P whose truth value is known a priori.

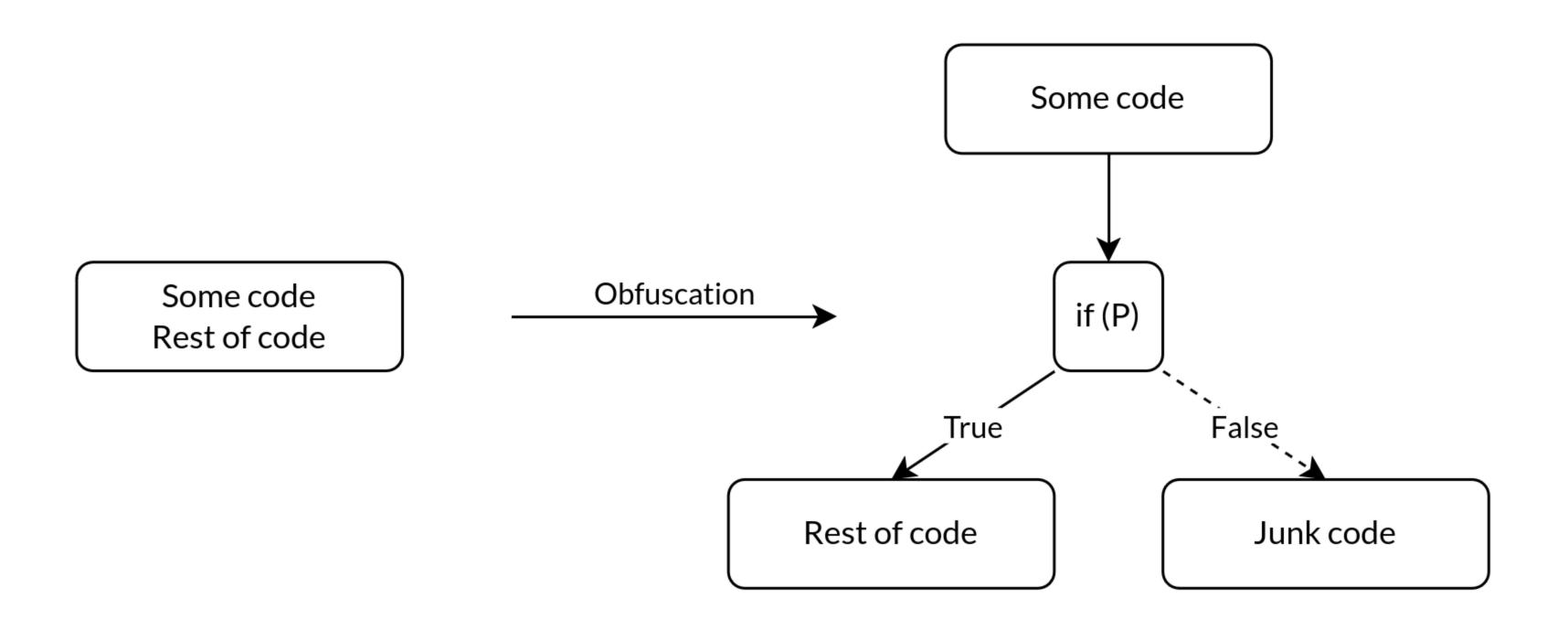
 $x^2 \ge 0$ is always true.

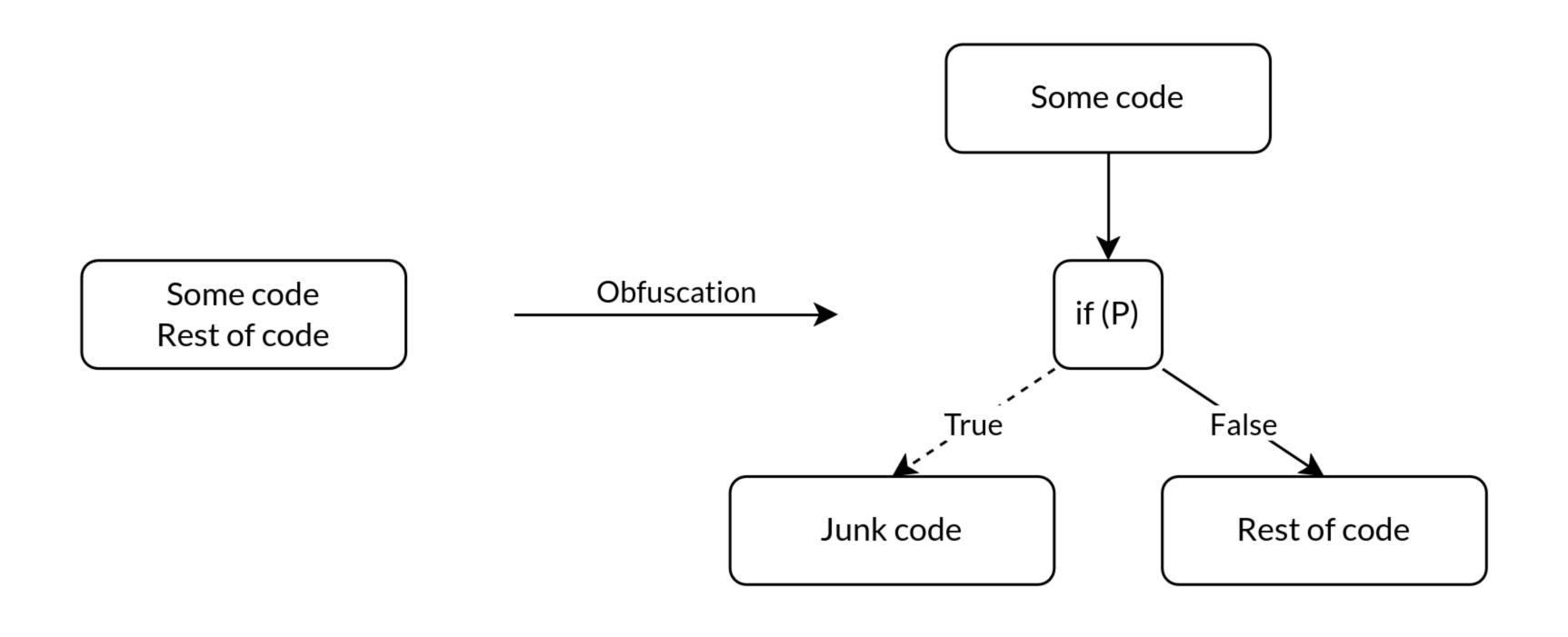
Opaque predicates

A conditional statement ${\it P}$ whose truth value is known a priori.

$$x^2 \ge 0$$
 is always true.

$$7y^2 - 1 = x^2$$
 is always false.

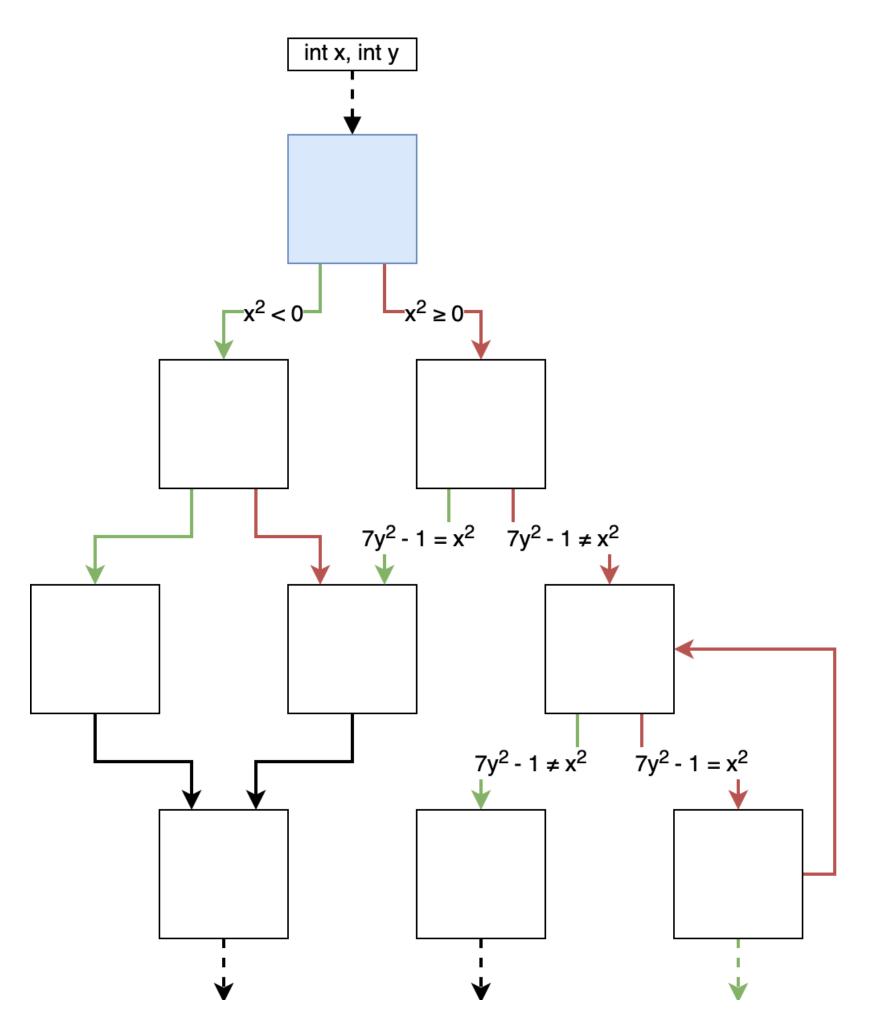




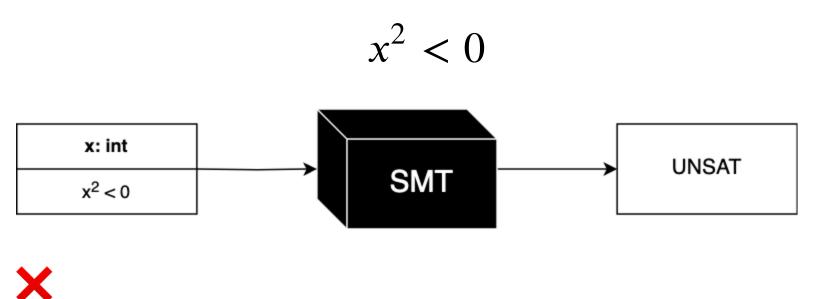
Detect (and patch) opaque predicates

- Symbolically execute a basic block
- Extract the branching constraints
- Check if the constraints are either always true (or false)
- Patch it to continue execution at the only possible branch and remove (NOP) the unreachable branch

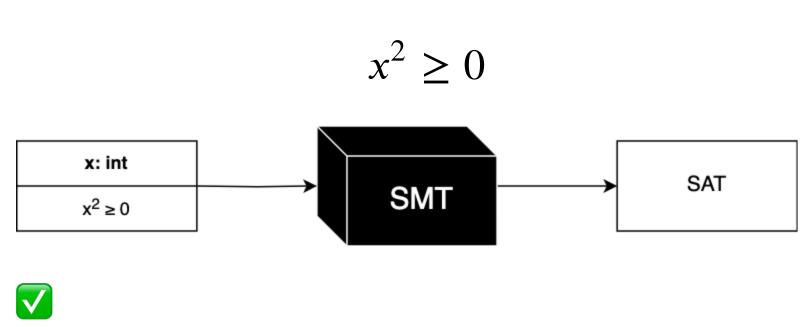
Visual example

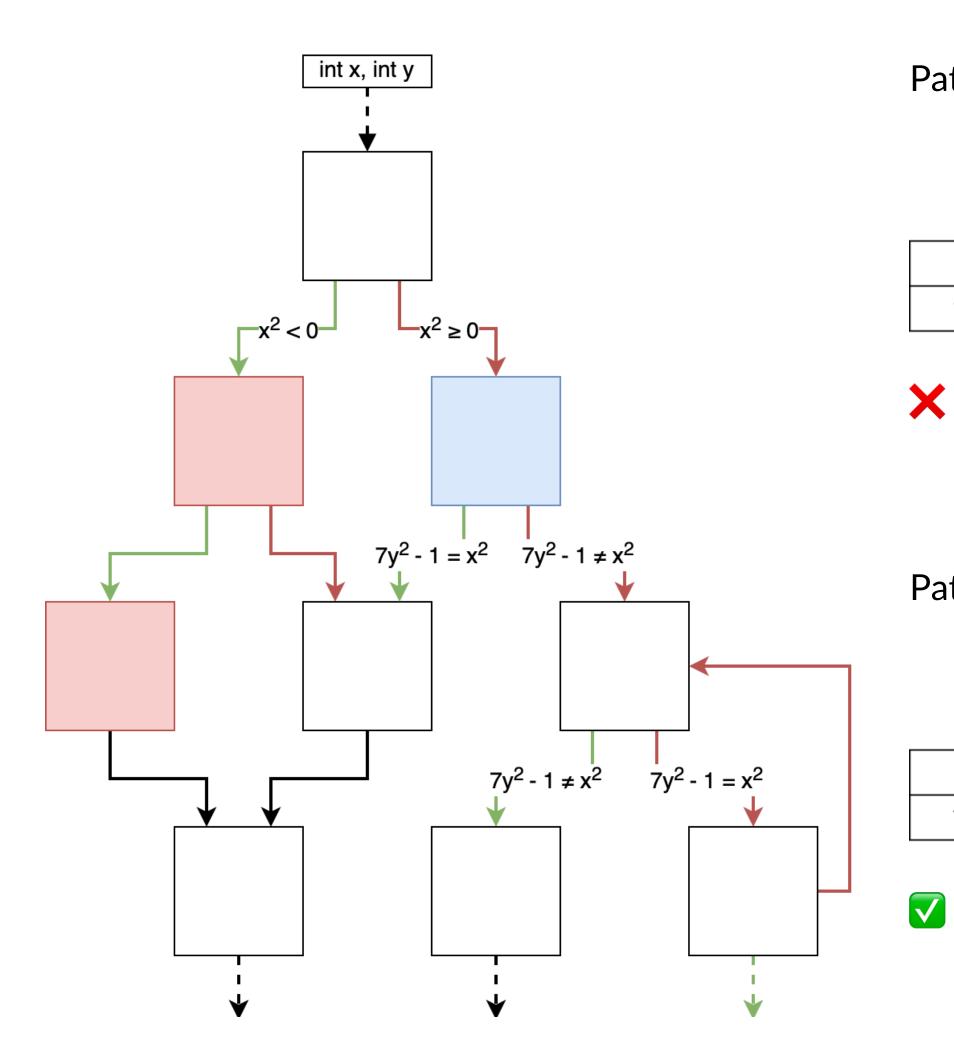


Path constraint T branch

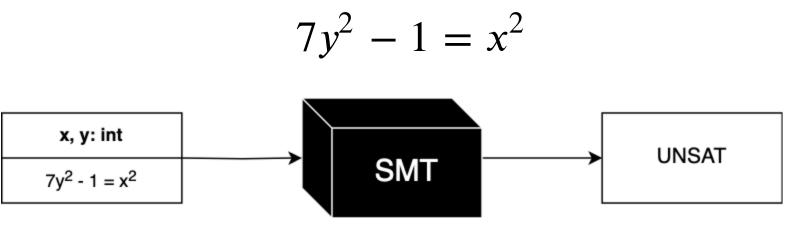


Path constraint F branch

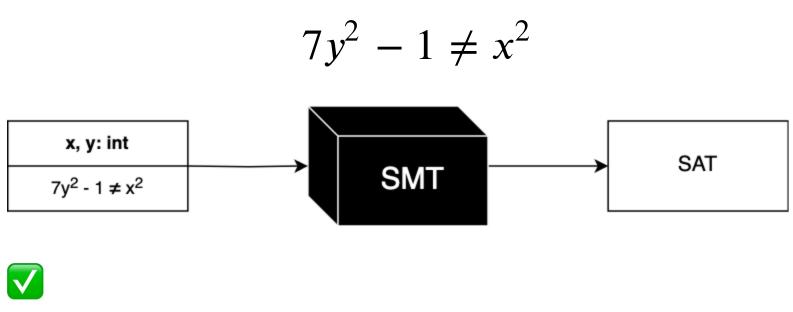


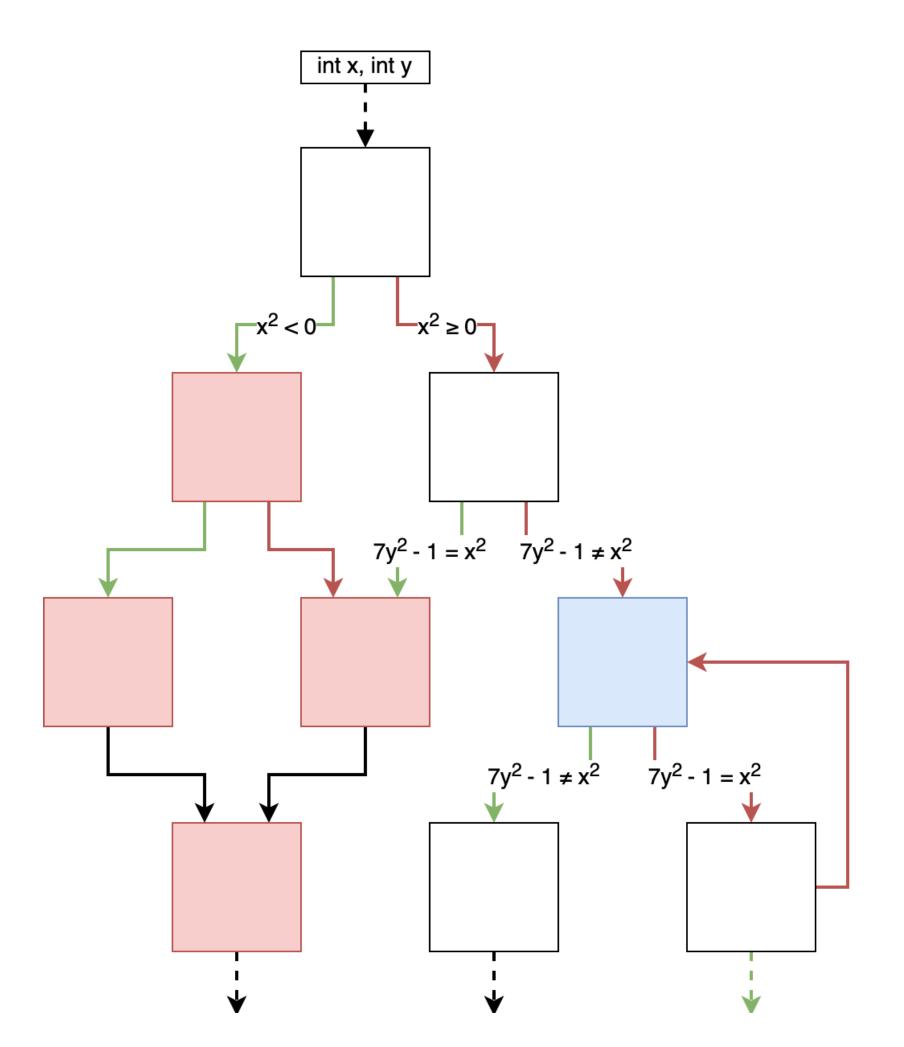


Path constraint T branch

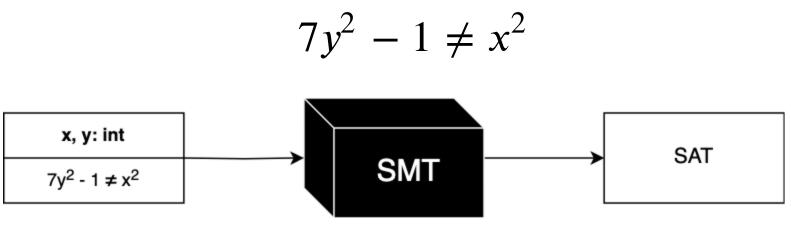


Path constraint F branch

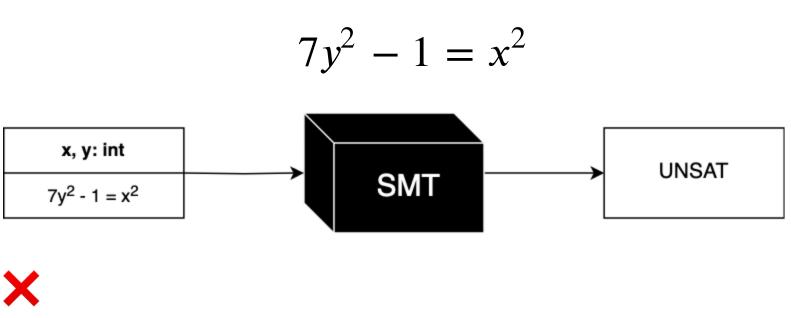


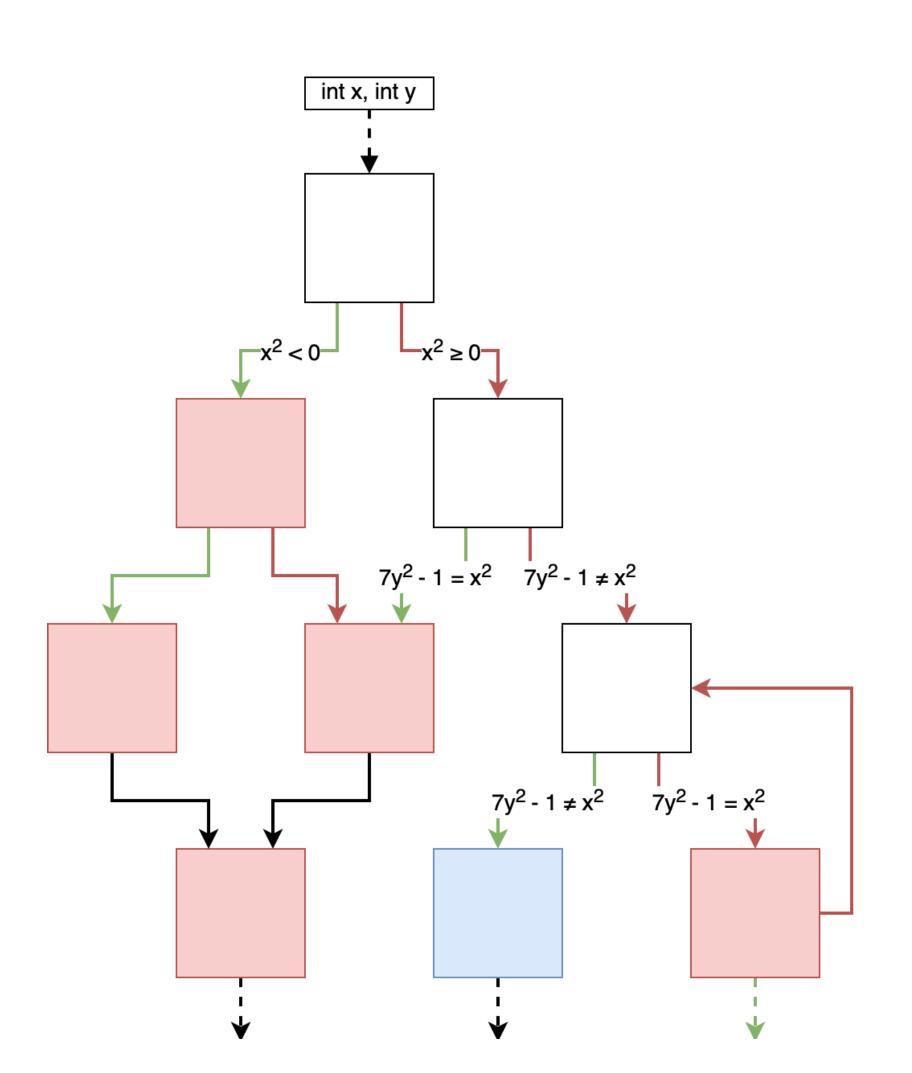


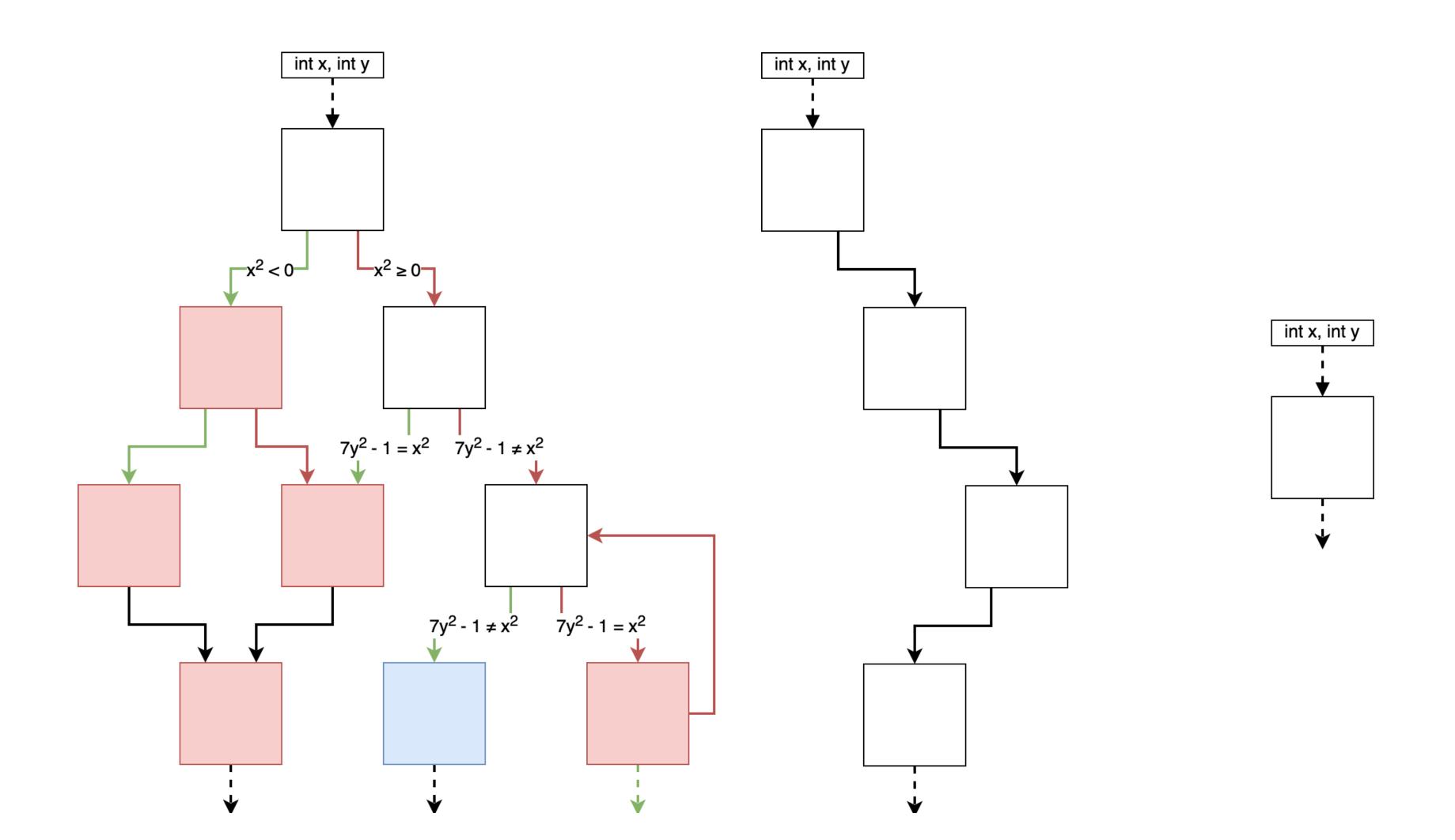
Path constraint T branch



Path constraint F branch







Example

Example

XTunnel @ APT28: ac3e087e43be67bdc674747c665b46c2

Example

XTunnel @ APT28: ac3e087e43be67bdc674747c665b46c2

Based on: https://github.com/mrphrazer/r2con2020_deobfuscation/blob/master/remove_opaque.py by Tim Blazytko (aka mrphrazer)

Demo

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Malware deobfuscation through opaque predicates removal

Fuzzing

Increase code coverage

Code coverage

Measure of the degree to which the code of a program is executed when a set of inputs is run.

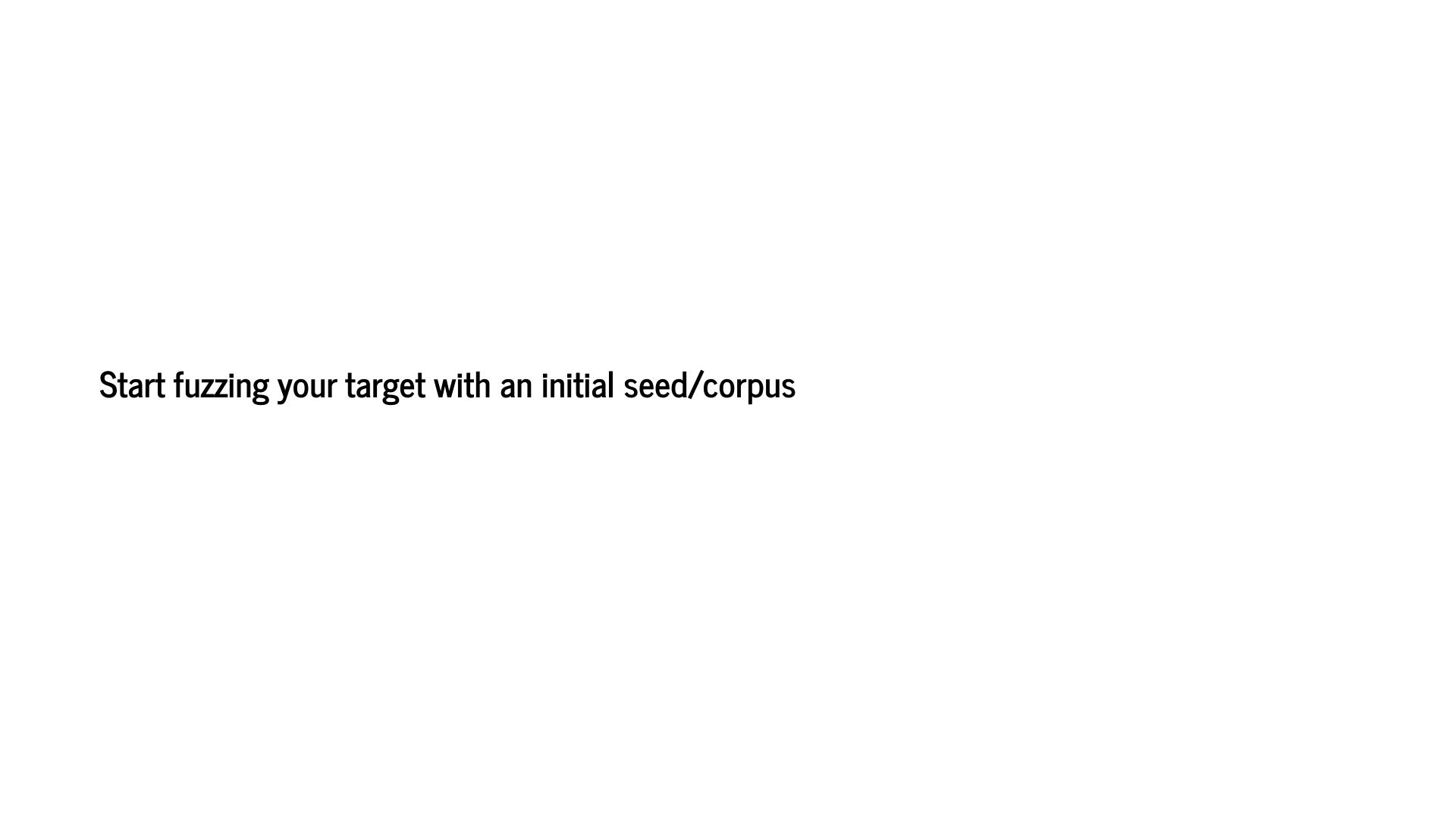
- Subroutines called
- Basic blocks reached
- Statements executed

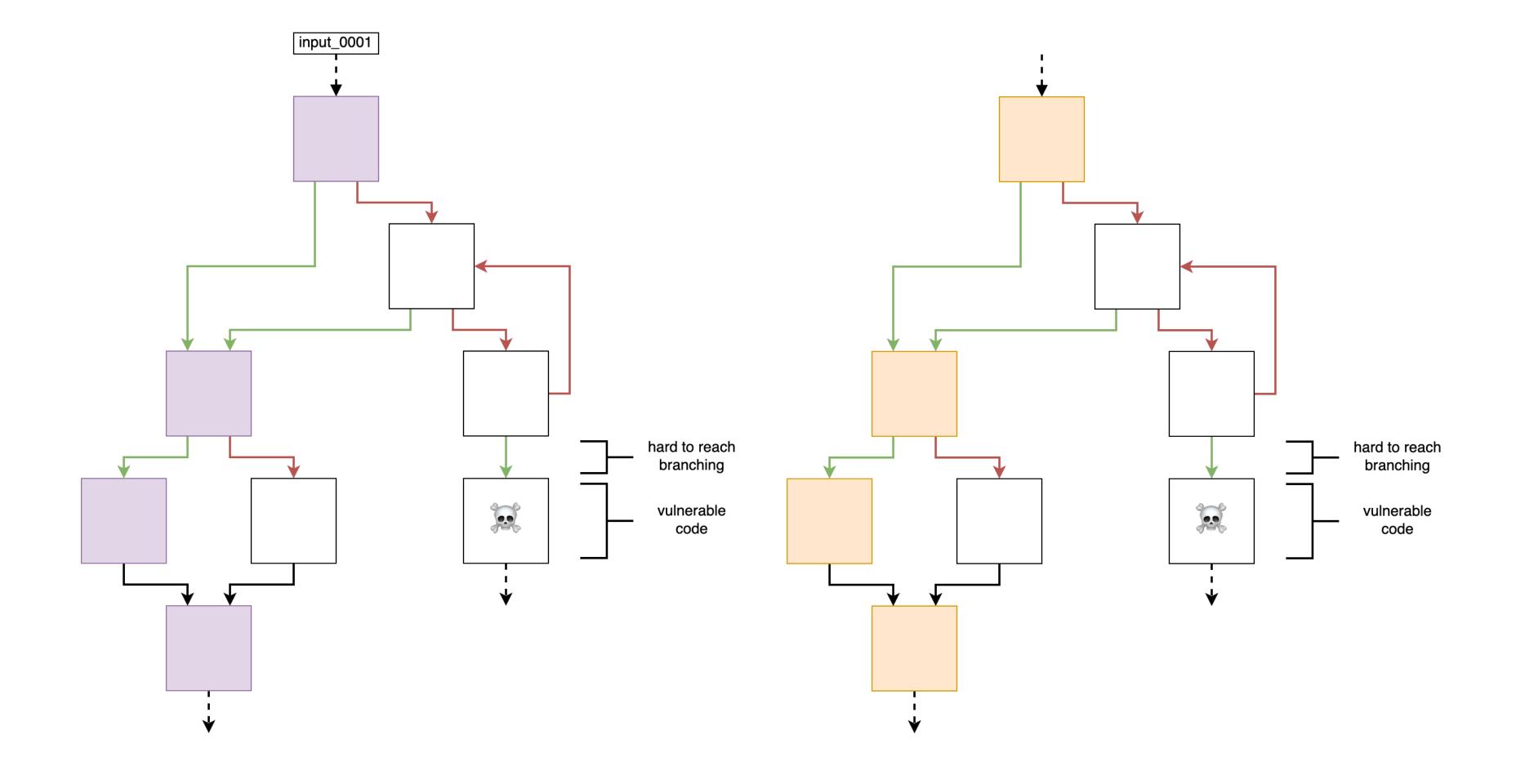
Higher code coverage → higher chance of hitting interesting (vulnerable) code

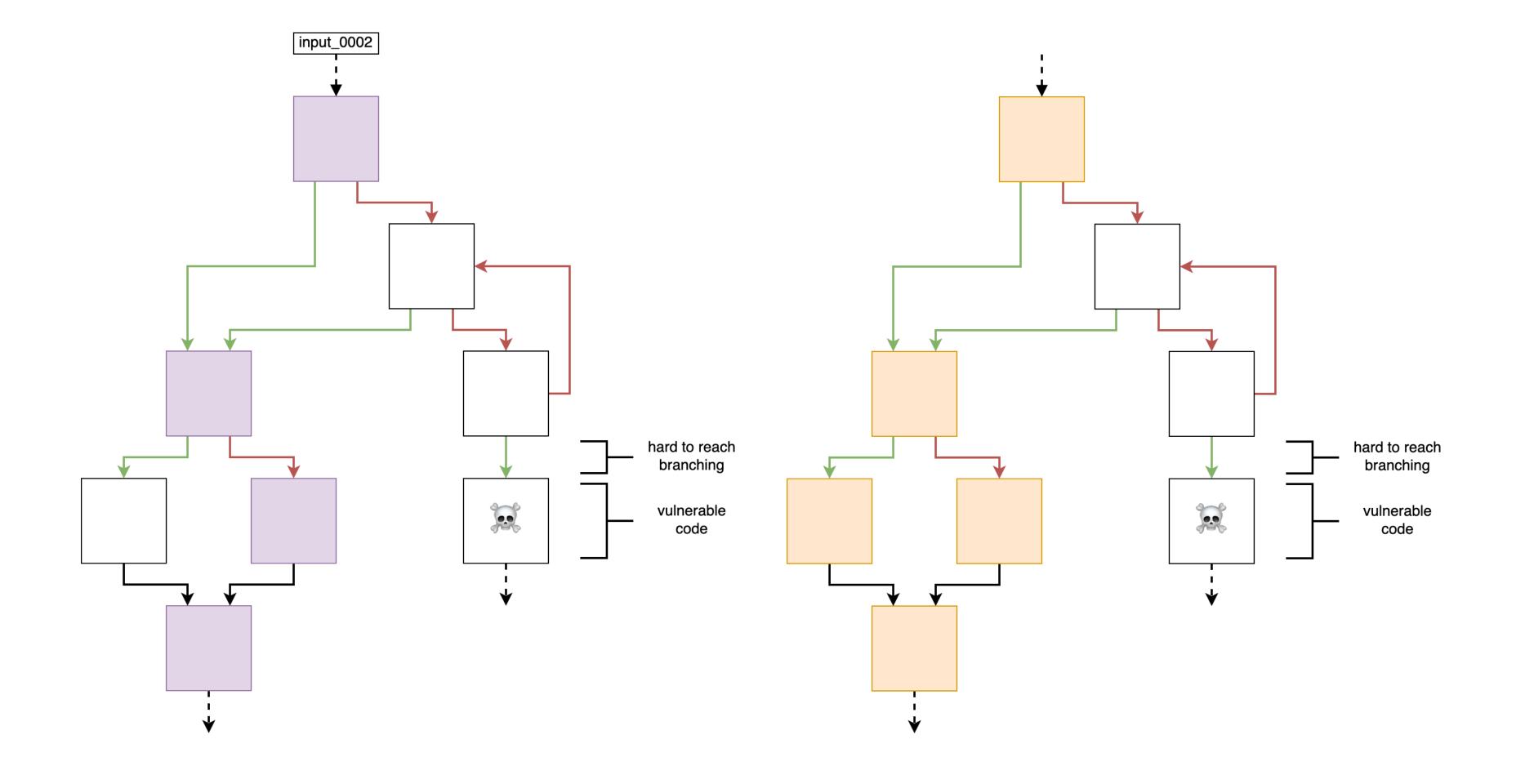
Increase code coverage

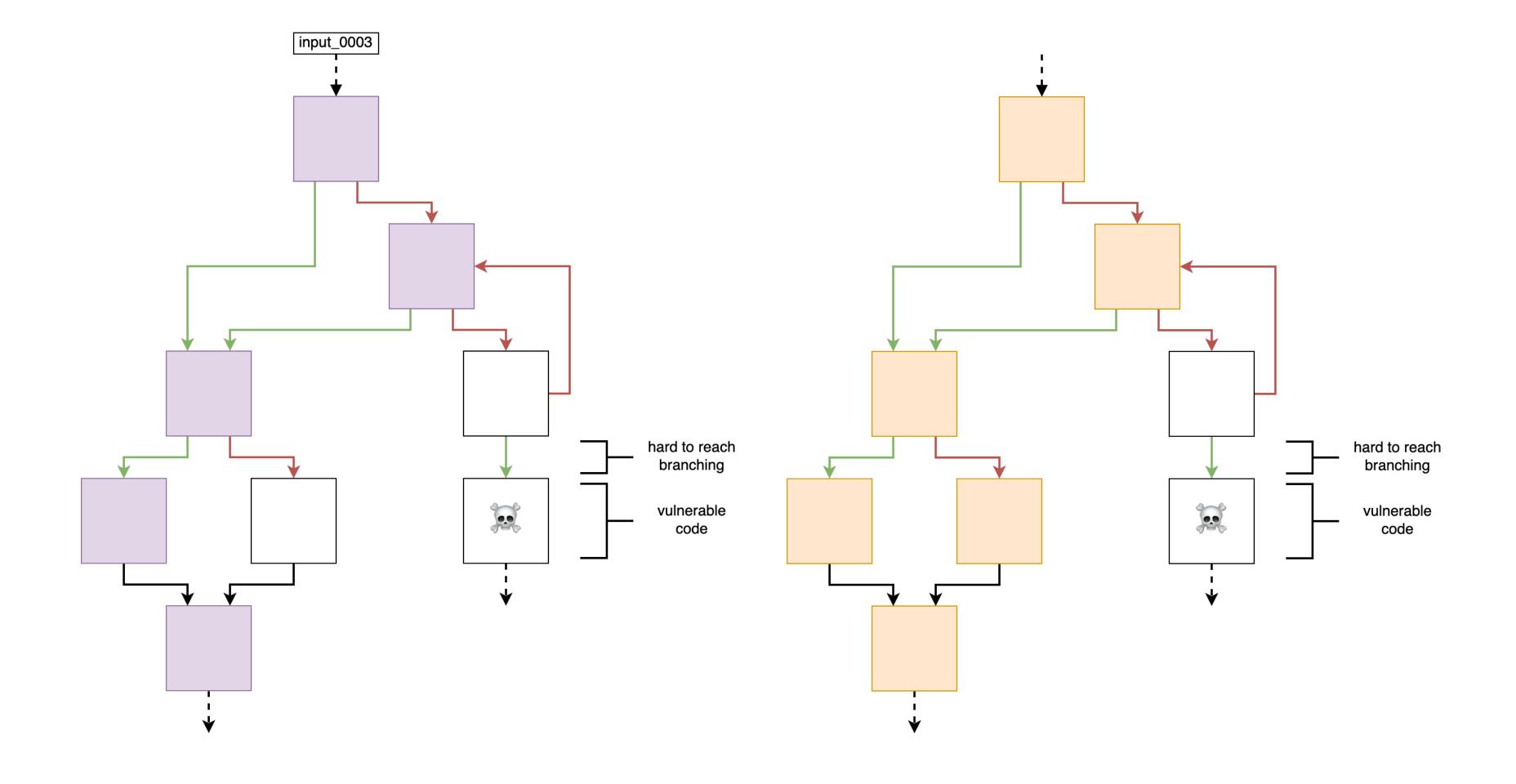
- 1. Start fuzzing your target with an initial seed/corpus
- 2. Use symbolic execution to generate inputs that trigger non-explored paths
- 3. Feed these new inputs into the fuzzer
- 4. Repeat

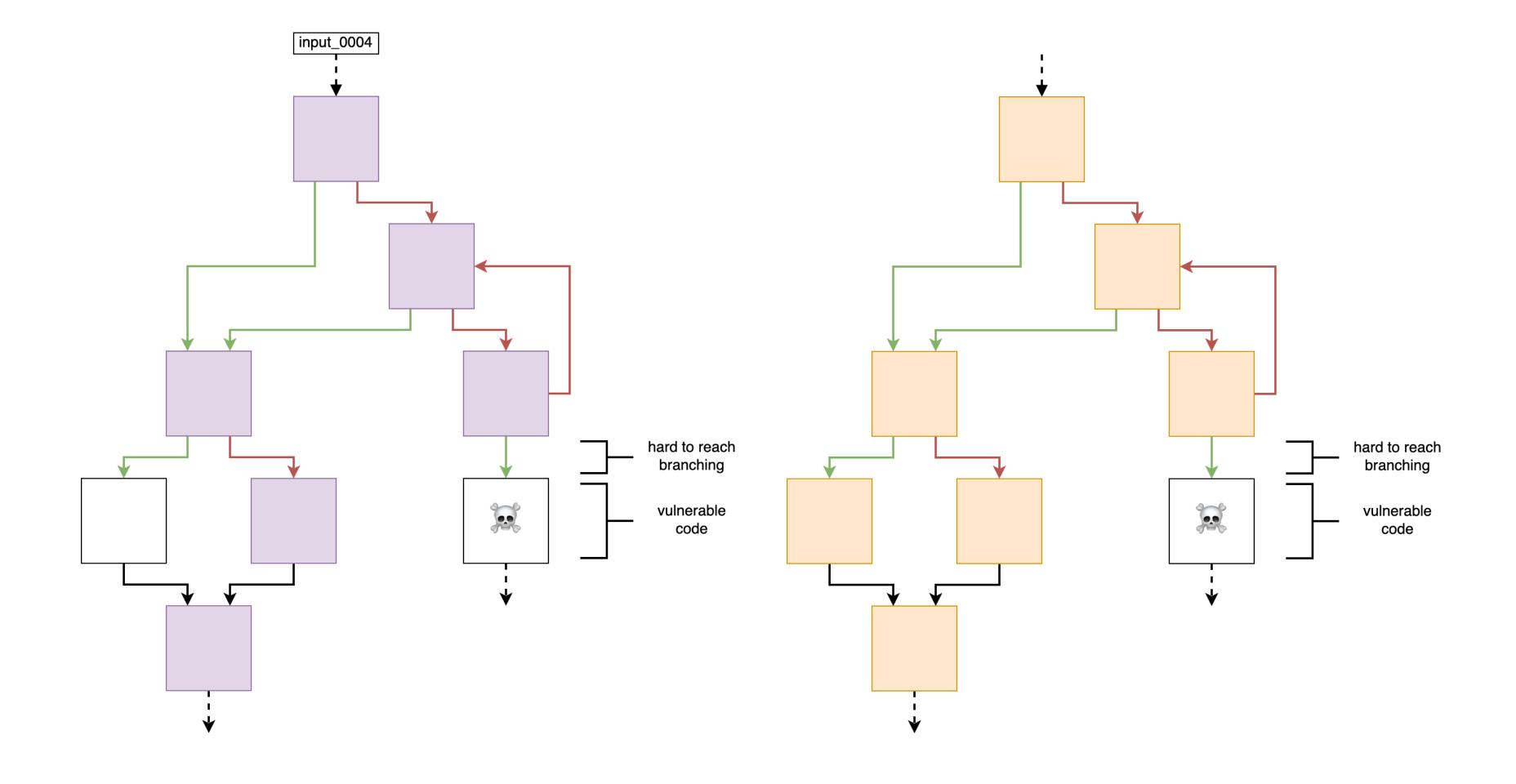
Visual example

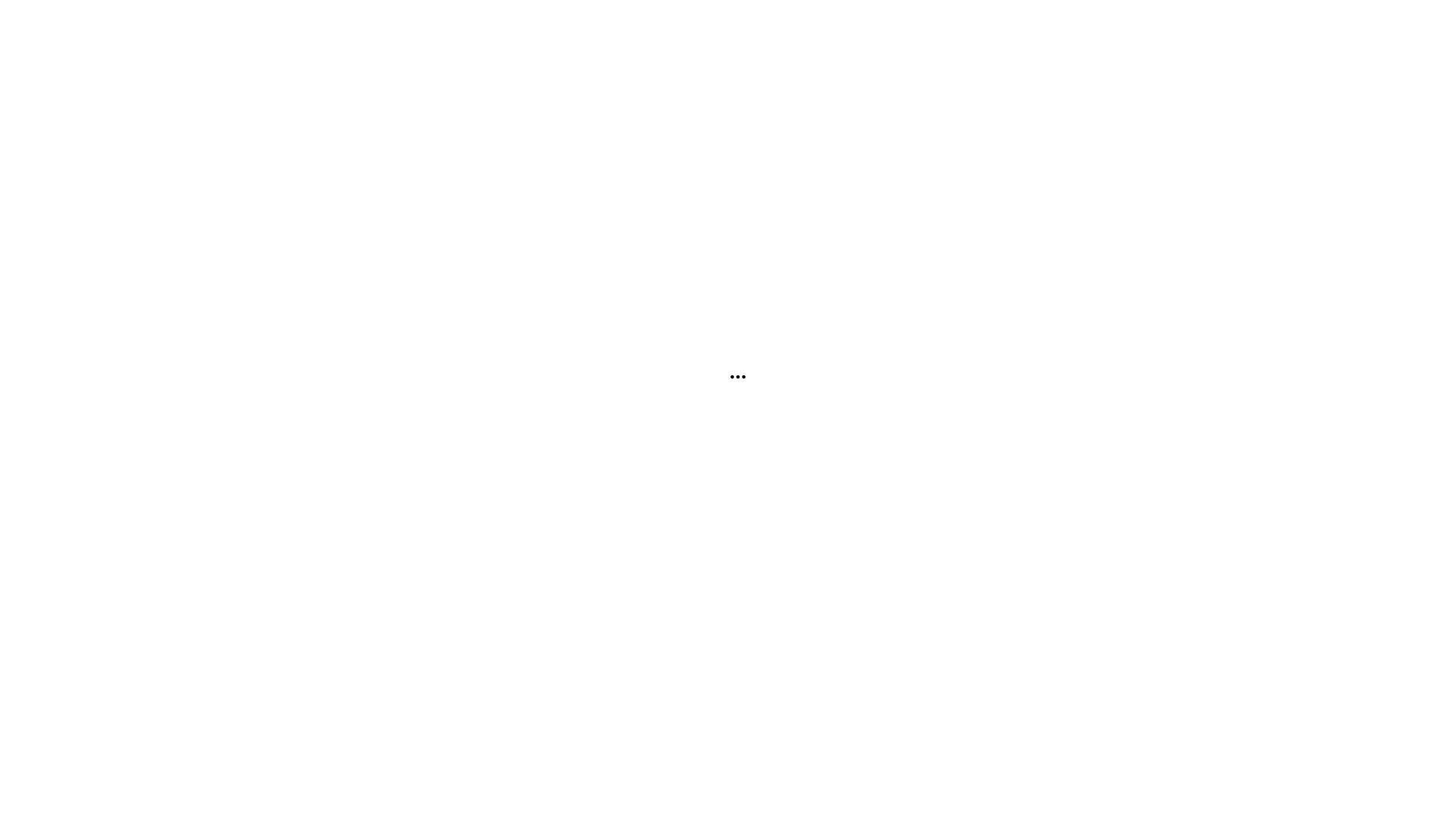


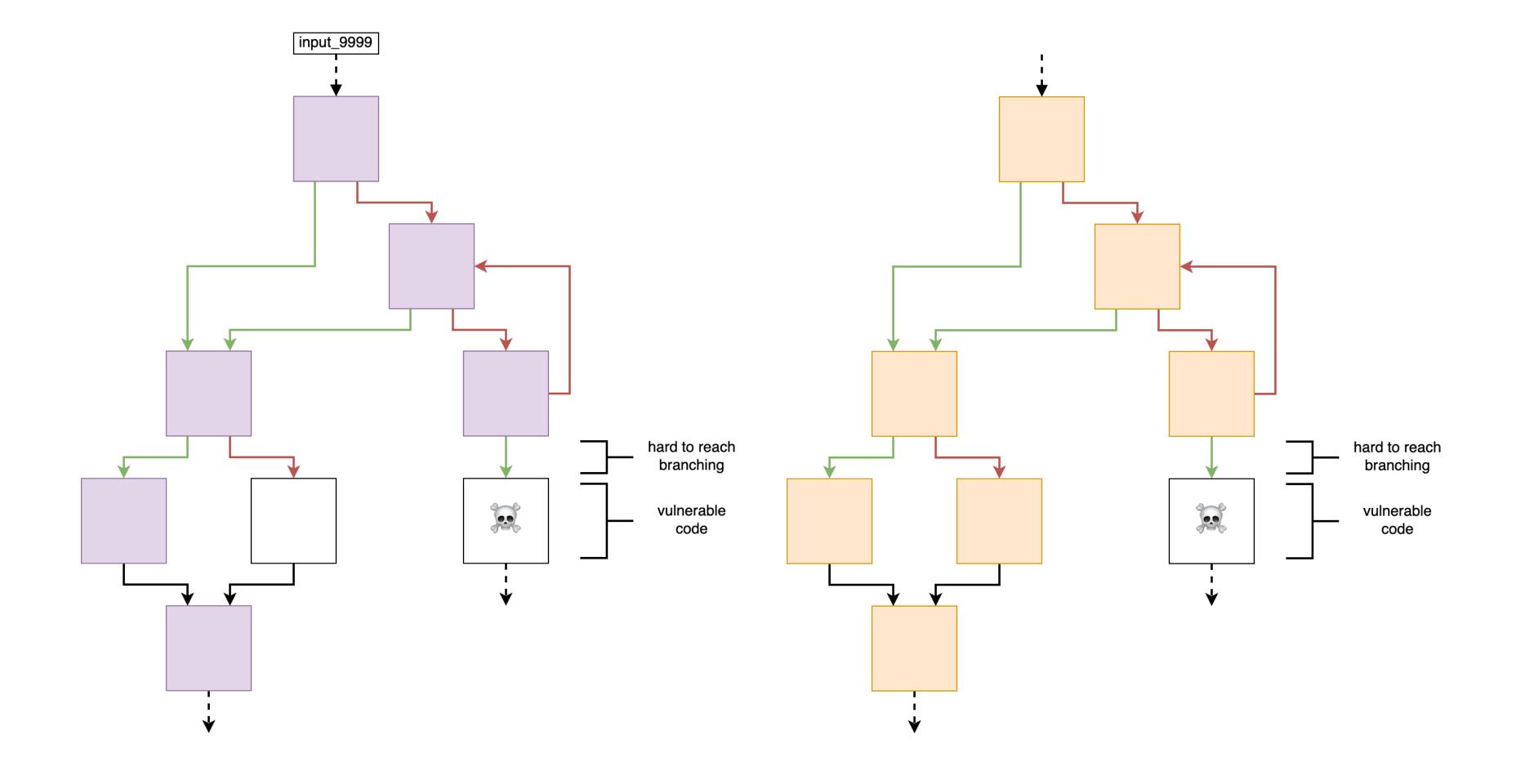




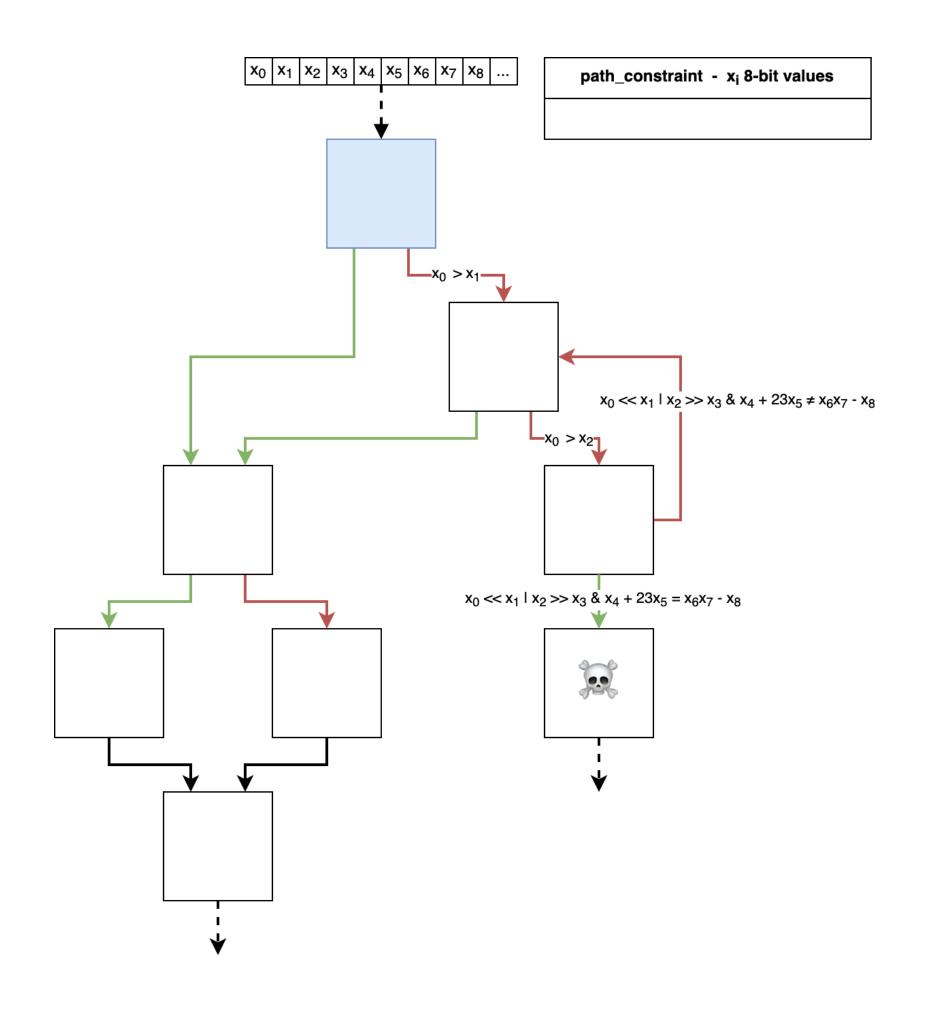


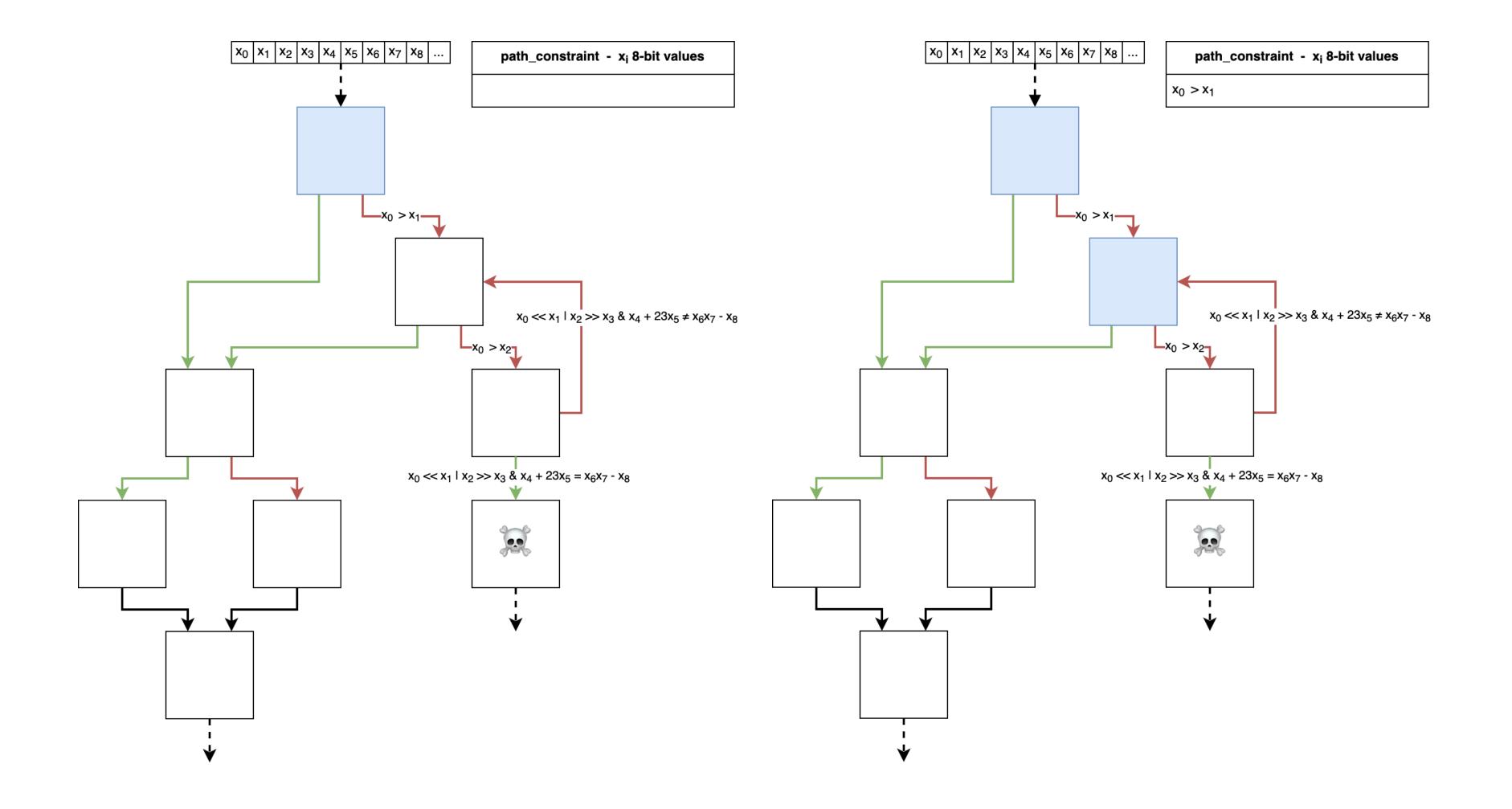


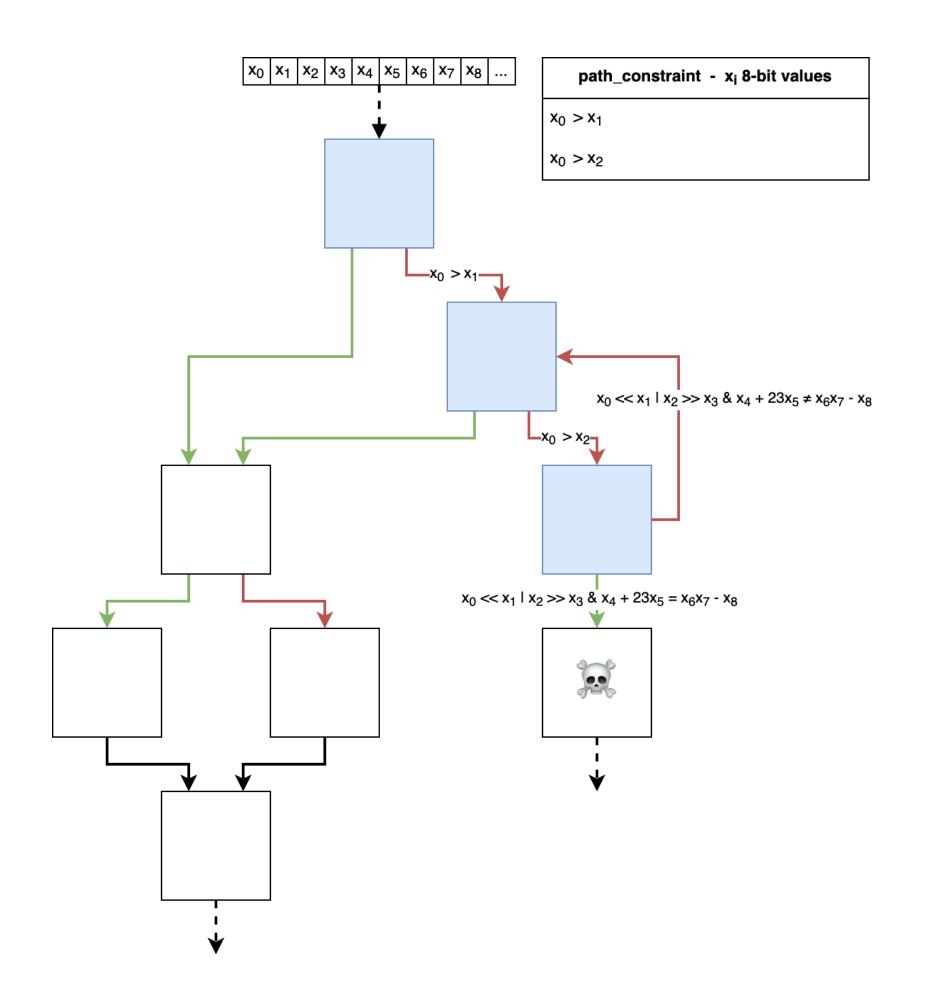


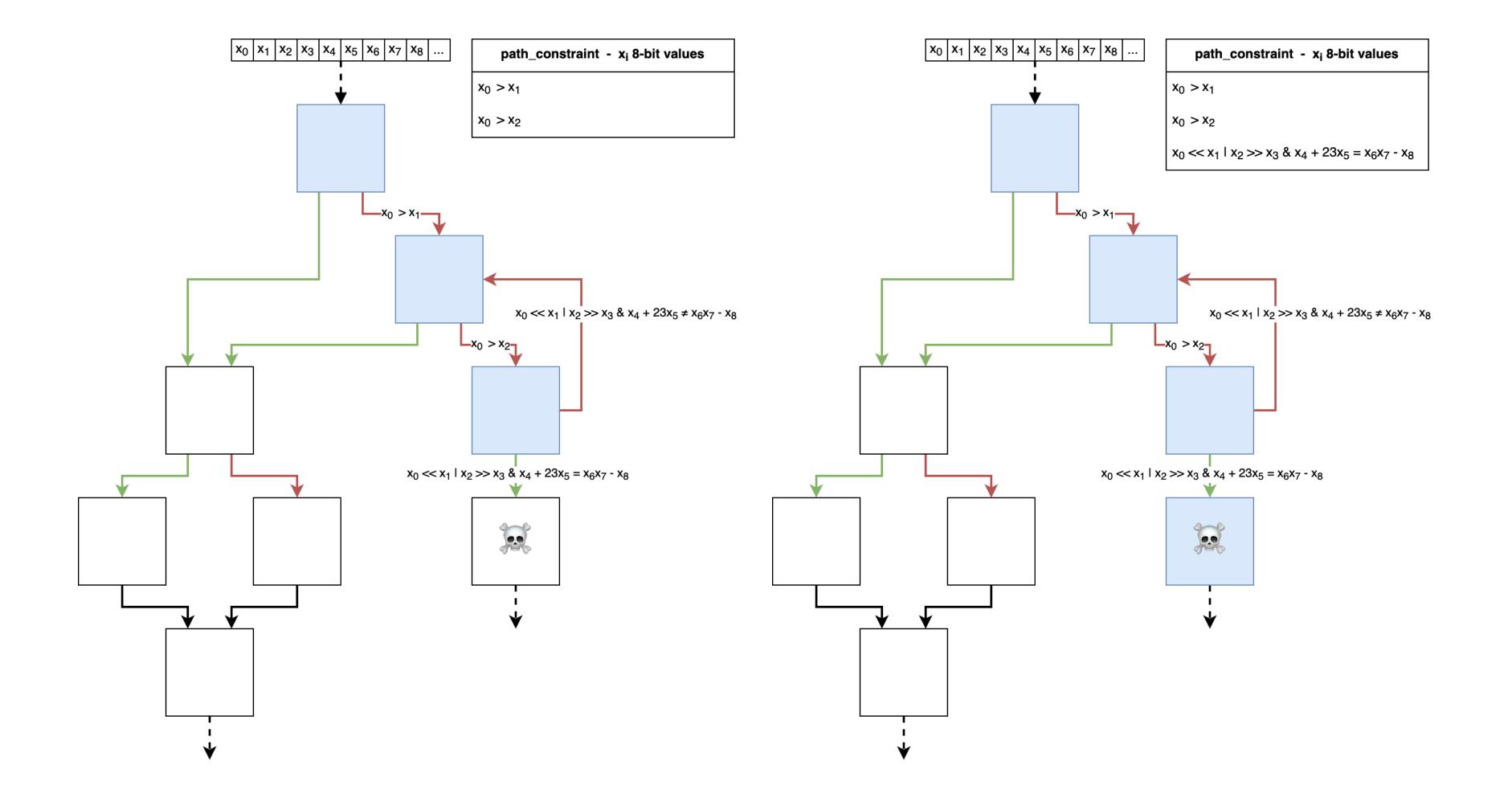


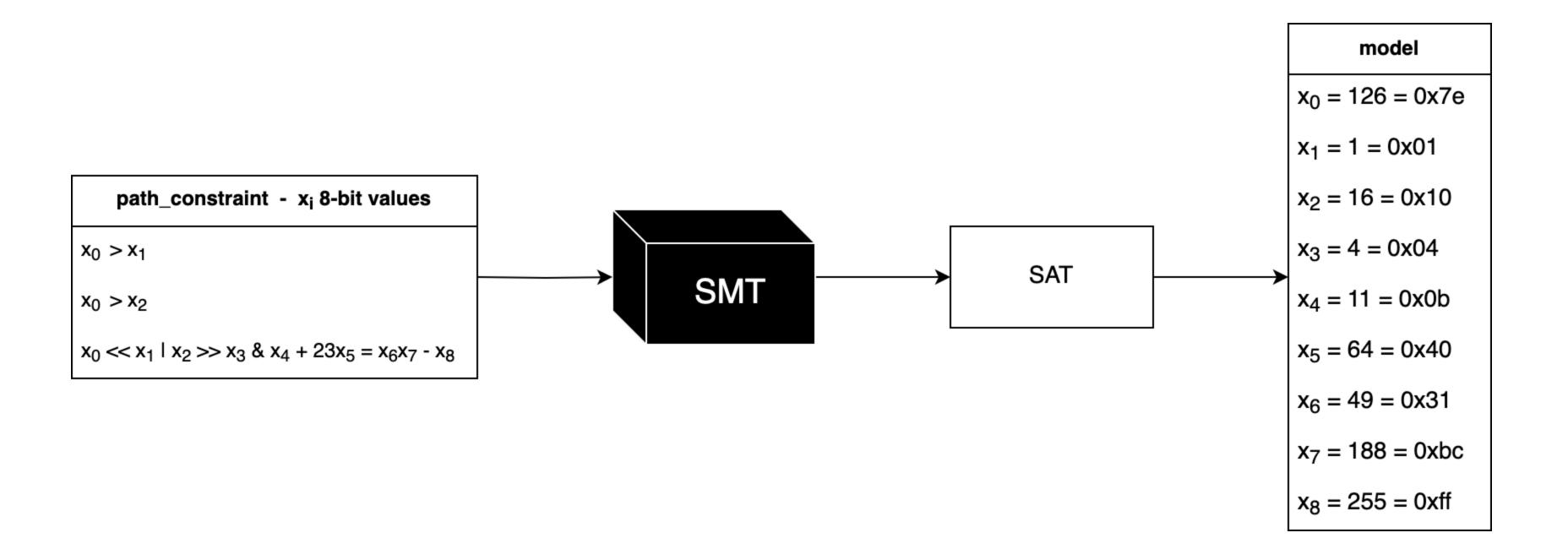
Use symbolic execution to generate inputs that trigger non-explored paths

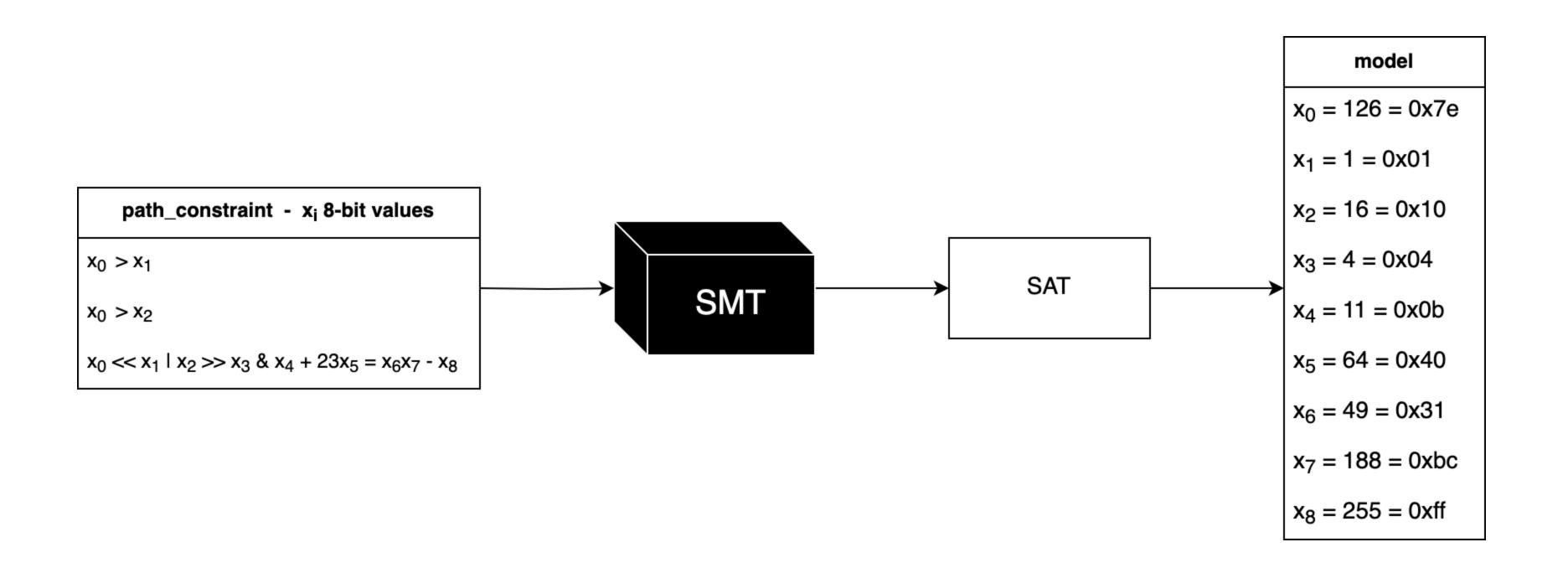


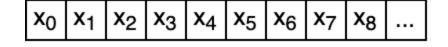


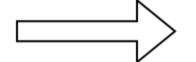




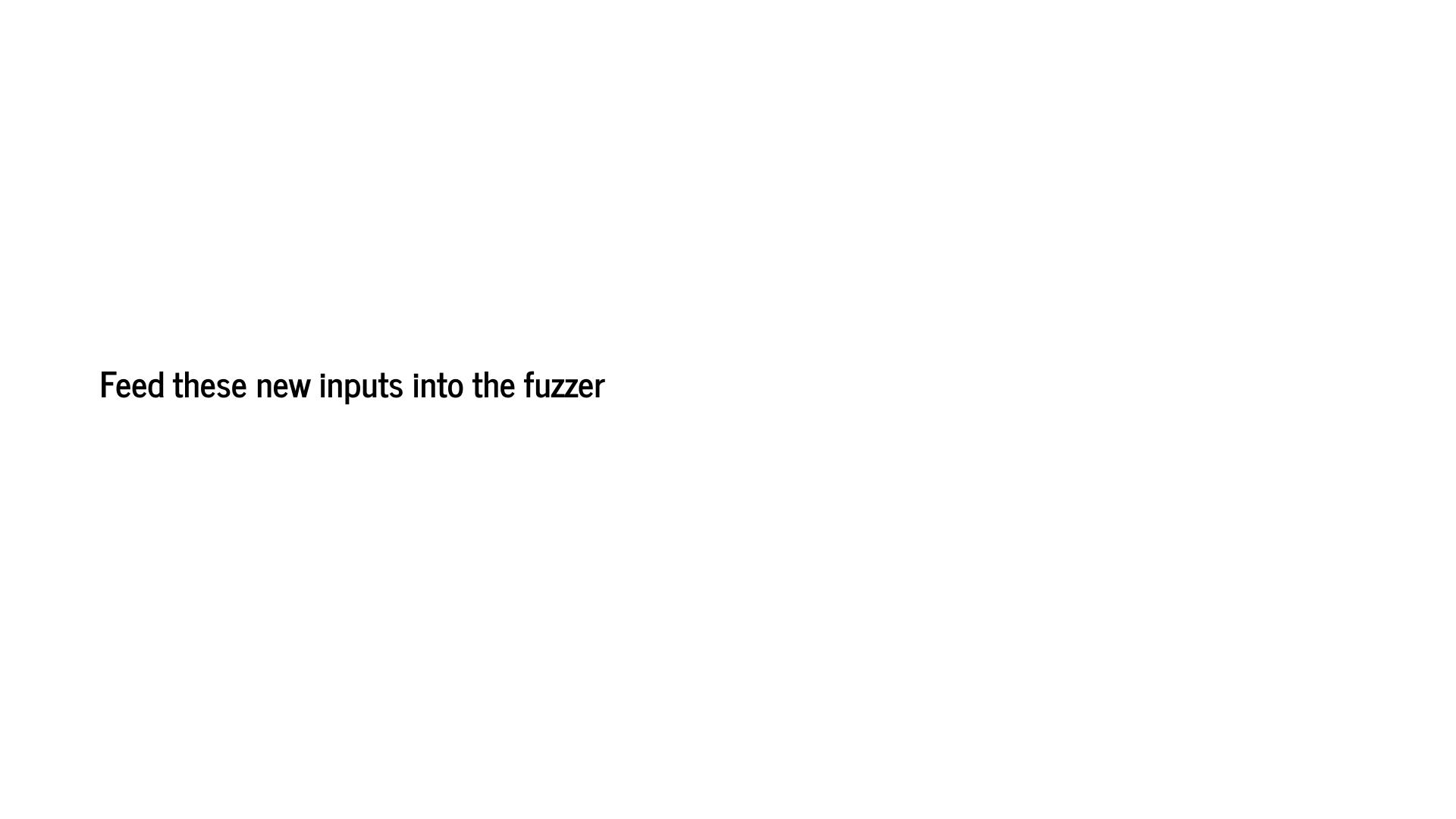


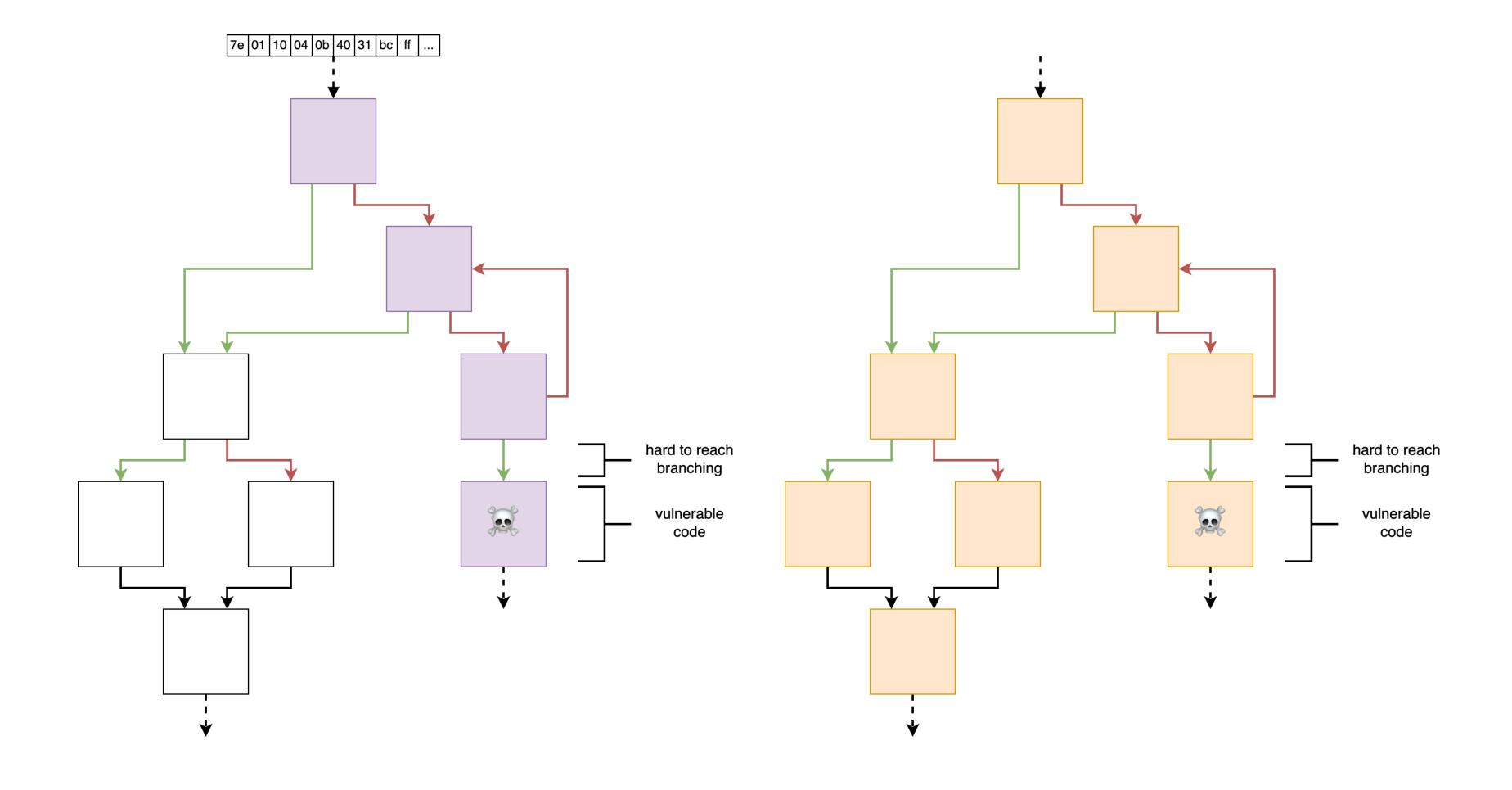






7e 01 10 04 0b 40 31 bc ff ...





Repeat o

Example

Example

Trigger a hard-to-reach division by zero

Example

Trigger a hard-to-reach division by zero

Based on: <u>Fuzzing combined with symbolic execution</u>: a demonstration on <u>SymCC and AFL</u> by AdaLogics

```
#include <stdio.h>
#include <stdlib.h>
#include <string.h>
int stuff(char *data, long fsize) {
    for (size_t i = 0; i < fsize; i++) {</pre>
        if (data[i] == ('F' ^ i)):
            return i+1;
    if (*(int*)data != 0xfalafel):
        return 0;
    return (int)(0x1337/(fsize - 10)); // <== TRIGGER DIV BY 0 HERE
```

I made a (dumb) SymCC fork (SymCC++) to make it work with AFL++

https://github.com/arnaugamez/symccpp

Demo

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Run AFL++ with SymCC++ to reach the division by zero

Limitations

Limitations

And some ideas to overcome them

- Path explosion: the number of control-flow paths grows exponentially ($\rightarrow \infty$ for unbounded loops)
 - Manual location of interesting code
 - Concolic (concrete + symbolic) execution

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 - Same as with any emulator: hook 'em all

- Path explosion: the number of control-flow paths grows exponentially ($\rightarrow \infty$ for unbounded loops)
 - Manual location of interesting code
 - Concolic (concrete + symbolic) execution
- Support for syscalls, standard C library functions, etc.:
 - Same as with any emulator: hook 'em all
- Limits of SMT solvers (e.g. due to high algebraic complexity through MBA transformations):
 - Program synthesis
 - Math[™]
 - Imagination

Training

Advanced Software Protection - Attacks and Defense

For Security Researchers and Developers

- Public / Private
- In-person / Remote
- 4 days (flexible)
- Details: https://furalabs.com/trainings

Upcoming

• February 20-23, 2024 @ Ringzer0 - Austin, TX (USA)

Thank you

Q&A