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# Hands-on binary (de)obfuscation

**Arnau Gàmez i Montolio**

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# About

Arnau Gàmez i Montolio

Hacker, Reverse Engineer & Mathematician

## Occupation

- Senior Expert Security Engineer @ Activision
- Founder, Researcher & Trainer @ Fura Labs
- PhD @ City, University of London

## Contact

[arnaugamez.com](http://arnaugamez.com)

**Introduction**

**Software protection landscape**

Context

## Context

Protection against Man-At-The-End (MATE) attacks.

The attacker has an instance of the program and completely controls the environment where it is executed.

**Protection against end users**

## Protection against end users

### *Technical*

- Obfuscation
- Cryptography
- Server-side execution
- Trusted execution environment (TEE)
- Device attestation
- ...

### *Legal*

- Lawyers
- Luck
  - Jurisdiction
  - Adversary's strength
- Patience
- ...



# Obfuscation

Transform a (part of a) program  $P$  into a functionally equivalent (part of a) program  $P'$  which is harder to analyze and extract information from than  $P$ .

$$P \longrightarrow \boxed{\text{Obfuscation}} \longrightarrow P'$$

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## Motivation

~~Prevent~~ complicate reverse engineering.

**Presence**

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Commercial software:

- Intellectual property
- Digital Rights Management (DRM)
- (Anti-)cheating

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Malware (mostly obfuscation):

- Avoid automatic signature detection
- Slow down analysis → time → money

## Methodology

Apply semantics-preserving transformations to data flow procedures and control flow structures.

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  - Source code
  - Intermediate representation
  - Assembly listing
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- At different target units
  - Whole program
  - Function
  - Basic block
  - Instruction



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  - Intermediate representation
  - Assembly listing
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  - Whole program
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  - Basic block
  - Instruction

**Remark:** Several *weak* techniques can be combined to create *hard* obfuscation transformations.

# Deobfuscation

Transform an obfuscated (part of a) program  $P'$  into a (part of a) program  $P''$  which is easier to analyze and extract information from than  $P'$ .

$$P'' \longleftarrow \boxed{\text{Deobfuscation}} \longleftarrow P'$$

# Deobfuscation

Transform an obfuscated (part of a) program  $P'$  into a (part of a) program  $P''$  which is easier to analyze and extract information from than  $P'$ .

$$P'' \longleftarrow \boxed{\text{Deobfuscation}} \longleftarrow P'$$

Ideally  $P'' \approx P$ , but this is rarely the case

- Lack of access to original program  $P$
- Interest in specific parts rather than whole program
- Interest in understanding rather than rebuilding

**Preliminary**

**SMT**

## Satisfiability Modulo Theories (SMT)

- **Satisfiability (SAT)**: determine if a (boolean) formula can be satisfied (can be true)
- **Modulo**: take into account (not only boolean formulas but also)...
- **Theories**: ...integer numbers, real numbers, floating point, **bit vectors**, and more

## Satisfiability Modulo Theories (SMT)

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- Modulo: take into account (not only boolean formulas but also)...
- Theories: ...integer numbers, real numbers, floating point, **bit vectors**, and more

## SMT solver

From a very practical standpoint: a *magic black-box* that can only answer a very simple question.

## Question

Given some variables of some type, and some constraints on these variables:

- Is there any variable assignment that makes the set of constraints satisfiable, i.e. such that (all) the constraints hold true?

## Question

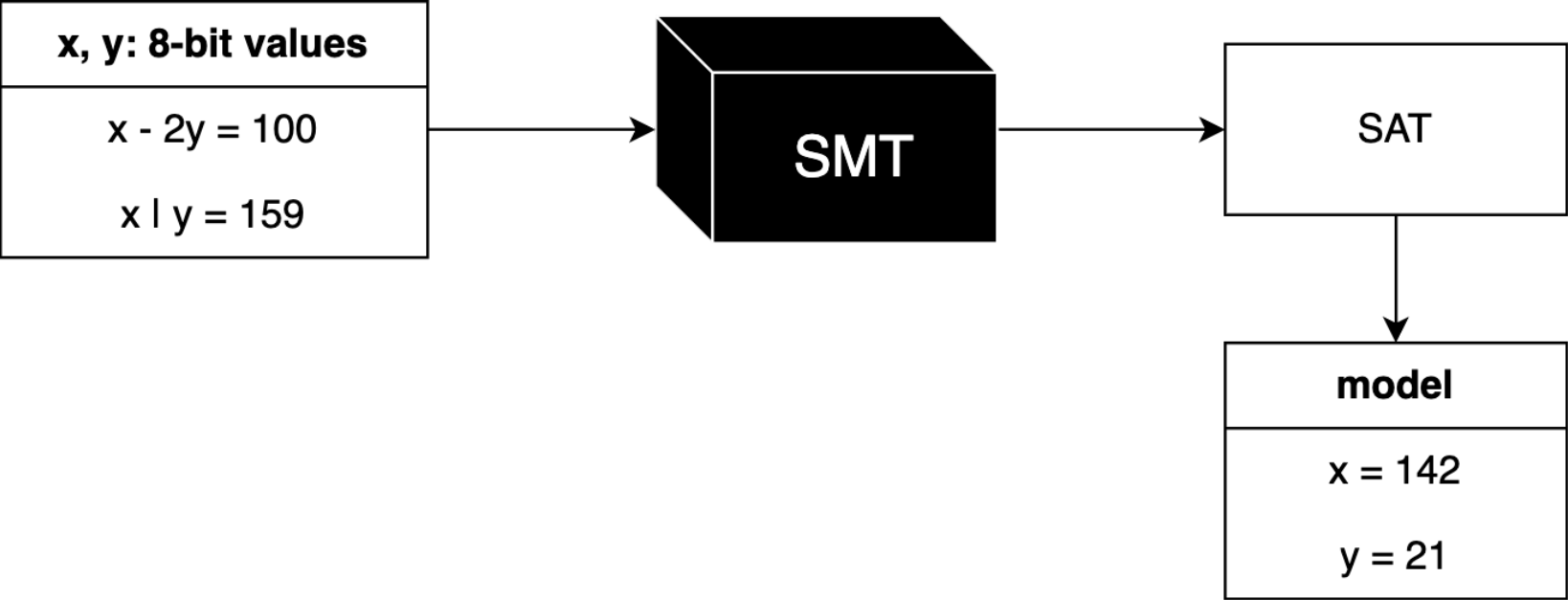
Given some variables of some type, and some constraints on these variables:

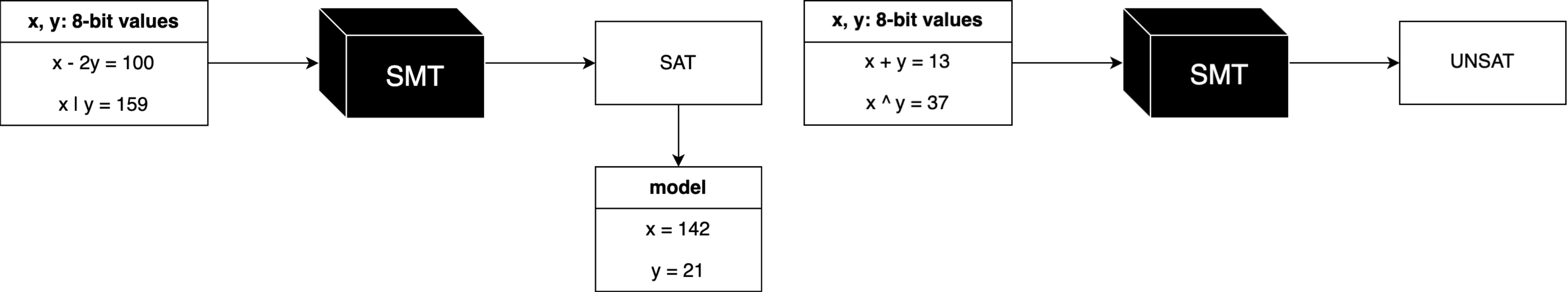
- Is there any variable assignment that makes the set of constraints satisfiable, i.e. such that (all) the constraints hold true?

## Outcomes

- **SAT**: there is a variable assignment that makes all the constraints hold true.
  - It will actually find a model, which is a particular solution (a concrete variable assignment)
- **UNSAT**: there is NO variable assignment that makes all the constraints hold true.
- **UNKNOWN**: unable to answer the question (usually due to a time-out)







## Program analysis with an SMT solver

- Check semantic equivalence
- Simplification engine
- Solve complex constraints
- Input crafting
- Model counting

## Limitations

- Resource exhaustion
- Since SAT is NP-complete, SMT problems are *at least* NP-complete
- Expression complexity
  - Due to underlying semantic complexity (e.g. any decent cryptosystem)
  - Due to deliberate obfuscation (e.g. complex algebraic transformations)

# Part I

## Mixed Boolean-Arithmetic (MBA) obfuscation

## MBA expressions

Algebraic expressions composed of integer arithmetic operators ( $+$ ,  $-$ ,  $\times$ ) and bitwise operators ( $\wedge$ ,  $\vee$ ,  $\oplus$ ,  $\neg$ ).

### Notation reminder

Operation	Math	Code
AND	$\wedge$	$\&$
OR	$\vee$	$ $
XOR	$\oplus$	$\wedge$
NOT	$\neg$	$\sim$

**Note:** I will use interchangeably the terms *boolean*, *bitwise* and *logic* operators.

Linear MBA expressions

$$(x \oplus y) + 2 \times (x \wedge y)$$

Polynomial MBA expressions

$$43(x \wedge y \vee z)^2((x \oplus y) \wedge z \vee t) + 2x + 123(x \vee y)zt^2$$

## Obfuscate expressions

Given an MBA expression  $E_1$ , generate an expression  $E_2$  that is:

- Semantically equivalent to  $E_1$
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For that, we have *rewrite rules* and *insertion of identities*.

## Rewrite rules

Replace an expression with an equivalent (more complex) one.

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**Note:** They can be applied iteratively (due to composability of polynomial MBA expressions).

$$x + y \rightarrow (x \oplus y) + 2(x \wedge y)$$

$$\hookrightarrow$$

$$x' + y'$$

$$x' = (x \oplus y)$$

$$y' = 2(x \wedge y)$$

$$x + y \rightarrow (x \oplus y) + 2(x \wedge y)$$

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$$x' + y'$$

$$x' = (x \oplus y)$$

$$y' = 2(x \wedge y)$$

$$x' + y' \rightarrow (x' \oplus y') + 2(x' \wedge y') \equiv ((x \oplus y) \oplus 2(x \wedge y)) + 2((x \oplus y) \wedge 2(x \wedge y))$$

$$\hookrightarrow$$

$$x' + y'$$

$$x' = ((x \oplus y) \oplus 2(x \wedge y))$$

$$y' = 2((x \oplus y) \wedge 2(x \wedge y))$$

## Insertion of identities

Wrap an expression with a pair of invertible mappings.

$$e = (x \oplus y) + 2 \times (x \wedge y) \quad f : x \mapsto 39x + 23 \quad f^{-1} : x \mapsto 151x + 111$$

$$f^{-1}(f(e)) = 151 \times (39 \times ((x \oplus y) + 2 \times (x \wedge y)) + 23) + 111$$

## Insertion of identities

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$$f^{-1}(f(e)) = 151 \times (39 \times ((x \oplus y) + 2 \times (x \wedge y)) + 23) + 111$$

**Note:** In general, affine functions (or permutation polynomials).

# Obfuscate constants

Replace a constant by a computational process (expression) on a given number of variables that will always evaluate to the target constant at runtime.



## Opaque constants

- $K$  constant
- $P, Q$  inverse permutation polynomials
- $E$  non-trivially equal to zero MBA expression

Conceal constant:  $K \equiv P(E + Q(K))$

## Opaque constants

- $K$  constant
- $P, Q$  inverse permutation polynomials
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Conceal constant:  $K \equiv P(E + Q(K))$

Proof:

$$P(E + Q(K)) = P(0 + Q(K)) = P(Q(K)) = K$$

$$K = 123$$

$$P(X) = 97X + 248X^2$$

$$Q(X) = 161X + 136X^2$$

$$E(x, y) = x - y + 2(\neg x \wedge y) - (x \oplus y)$$


---

$$\begin{aligned} P(E + Q(K)) &= 195 + 97x + 159y + 194\neg(x \vee \neg y) + 159(x \oplus y) \\ &+ (163 + x + 255y + 2\neg(x \vee \neg y) + 255(x \oplus y)) \times (232 + 248x + 8y + 240\neg(x \vee \neg y) \\ &\quad + 8(x \oplus y)) \end{aligned}$$

## Fact

State-of-the-art software protection mechanisms leverage MBA transformations to obfuscate code.

## Why?

Combinations of operators from these different fields *do not interact well together*

- No general rules (distributivity, factorization...) or theory
- Computer algebra systems do not support bitwise operators with symbolic variables

SMT solvers support for mixing operators (*bit vector* theory)

- Reasonably good at proving semantic equivalence
  - It can be easily thwarted with deliberate MBA transformations as well
- Pretty bad at simplification for general MBA expressions

# Part II

## Analysis - Symbolic execution

# Calculator

Concrete calculations

$$\begin{vmatrix} 1 & 2 \\ 3 & 4 \end{vmatrix} = 4 - 2 \cdot 3 = -2$$

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Concrete calculations

$$\begin{vmatrix} 1 & 2 \\ 3 & 4 \end{vmatrix} = 4 - 2 \cdot 3 = -2$$

## Computer Algebra System (CAS)

Symbolic calculations and expression manipulation

$$\begin{vmatrix} 1 & 2 \\ a & 4 \end{vmatrix} = 4 - 2a = 2(2 - a)$$



**What is symbolic execution?**

# What is symbolic execution?

Roughly speaking, just a **computer algebra system** for:

- Programming languages: C, C++, Java, Rust...
- Assembly languages: x86, x86-64, ARM64, MIPS, RISC-V...
- Intermediate languages: LLVM-IR, SMT-LIB, r2 ESIL, IDA Microcode, \$YOUR\_OWN...

More specifically, symbolic execution is a **program analysis technique**:

- Represent inputs as *symbolic* variables instead of *concrete* values (normal execution or emulation)
- Derive constraints that encode control-flow and data-flow with respect to these symbolic variables

Use these constraints to reason about and extract information from the program.

But how does it *actually* work?

## But how does it *actually* work?

1. Define two data structures:
  - **state\_map**: symbolic mapping for the variables (registers, memory locations)
  - **path\_constraint**: conditions required to reach current instruction
2. Extract the semantics of each statement (instruction)
3. Update these two data structures to account for the effects of the *executed* statement (instruction)
4. If there is control-flow branching, *fork* these structures to keep track of different execution paths

The **state\_map** represents *data-flow* updates, i.e. the (computational) process through which a variable ends up holding a certain value at a given point in the program execution.

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The **path\_constraint** represents *control-flow* tracking, i.e. the set of constraints (conditions) on the variables that need to be satisfied for the execution to reach a given point in the program.

**Visual example**



```
_start:
    mov rax, 123    <=0=
    add rax, rsi
    xor rax, rdi
    mov rbx, 2
    add rax, rbx
    mov rdi, 3
    mov rsi, rax
    add rax, rbx
    xor rax, rdi
    mov rbx, 7
    and rax, rbx
    mov rdi, 1336
    add rax, rdi
```

```
        cmp rax, 1337
        jnz bad

good:
    xor rdi, rdi
    jmp exit

bad:
    mov rdi, 1

exit:
    mov rax, 60
    syscall
```

```
path_constraint  true
state_map
    rax -> rax
    rbx -> rbx
    rdi -> rdi
    rsi -> rsi
    zf  -> zf
```

<pre> _start:     mov rax, 123     add rax, rsi    &lt;=0=     xor rax, rdi     mov rbx, 2     add rax, rbx     mov rdi, 3     mov rsi, rax     add rax, rbx     xor rax, rdi     mov rbx, 7     and rax, rbx     mov rdi, 1336     add rax, rdi </pre>	<pre>         cmp rax, 1337         jnz bad  good:         xor rdi, rdi         jmp exit  bad:         mov rdi, 1  exit:         mov rax, 60         syscall </pre>
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<pre> path_constraint state_map </pre>	<pre> true rax -&gt; 123 rbx -&gt; rbx rdi -&gt; rdi rsi -&gt; rsi zf  -&gt; zf </pre>
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<pre> _start:     mov rax, 123     add rax, rsi     xor rax, rdi    &lt;=0=     mov rbx, 2     add rax, rbx     mov rdi, 3     mov rsi, rax     add rax, rbx     xor rax, rdi     mov rbx, 7     and rax, rbx     mov rdi, 1336     add rax, rdi </pre>	<pre>         cmp rax, 1337         jnz bad  good:         xor rdi, rdi         jmp exit  bad:         mov rdi, 1  exit:         mov rax, 60         syscall </pre>
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<pre> path_constraint state_map </pre>	<pre> true rax -&gt; (123 + rsi) rbx -&gt; rbx rdi -&gt; rdi rsi -&gt; rsi zf  -&gt; zf </pre>
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<pre> _start:     mov rax, 123     add rax, rsi     xor rax, rdi     mov rbx, 2     add rax, rbx     mov rdi, 3     mov rsi, rax     add rax, rbx     xor rax, rdi     mov rbx, 7     and rax, rbx     mov rdi, 1336     add rax, rdi </pre>	<pre>     cmp rax, 1337     jnz bad  good:     xor rdi, rdi     jmp exit  bad:     mov rdi, 1  exit:     mov rax, 60     syscall </pre>
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<pre> path_constraint state_map </pre>	<pre> true rax -&gt; (((123 + rsi) ^ rdi) + 2) rbx -&gt; 2 rdi -&gt; rdi rsi -&gt; rsi zf  -&gt; zf </pre>
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<b>_start:</b>		<b>cmp rax, 1337</b>
mov rax, 123		jnz bad
add rax, rsi		
xor rax, rdi		<b>good:</b>
mov rbx, 2		xor rdi, rdi
add rax, rbx		jmp exit
mov rdi, 3		
mov rsi, rax		<b>bad:</b>
add rax, rbx	<b>&lt;=0=</b>	mov rdi, 1
xor rax, rdi		
mov rbx, 7		<b>exit:</b>
and rax, rbx		mov rax, 60
mov rdi, 1336		syscall
add rax, rdi		

<b>path_constraint</b>	<b>true</b>
<b>state_map</b>	
rax	-> (((123 + rsi) ^ rdi) + 2)
rbx	-> 2
rdi	-> 3
rsi	-> (((123 + rsi) ^ rdi) + 2)
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    mov rsi, rax
    add rax, rbx
    xor rax, rdi    <=0=
    mov rbx, 7
    and rax, rbx
    mov rdi, 1336
    add rax, rdi

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<pre> path_constraint  true state_map </pre>	<pre> rax -&gt; (((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) rbx -&gt; 7 rdi -&gt; 3 rsi -&gt; (((123 + rsi) ^ rdi) + 2) zf  -&gt; zf </pre>
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    mov rax, 123
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    mov rdi, 3
    mov rsi, rax
    add rax, rbx
    xor rax, rdi
    mov rbx, 7
    and rax, rbx
    mov rdi, 1336 <=0=
    add rax, rdi

                                cmp rax, 1337
                                jnz bad

                                good:
                                xor rdi, rdi
                                jmp exit

                                bad:
                                mov rdi, 1

                                exit:
                                mov rax, 60
                                syscall

```

```

path_constraint  true
state_map
    rax -> ((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) & 7)
    rbx -> 7
    rdi -> 3
    rsi -> (((123 + rsi) ^ rdi) + 2)
    zf  -> zf

```

<b>_start:</b>		<b>cmp rax, 1337</b>
mov rax, 123		jnz bad
add rax, rsi		
xor rax, rdi		<b>good:</b>
mov rbx, 2		xor rdi, rdi
add rax, rbx		jmp exit
mov rdi, 3		
mov rsi, rax		<b>bad:</b>
add rax, rbx		mov rdi, 1
xor rax, rdi		
mov rbx, 7		<b>exit:</b>
and rax, rbx		mov rax, 60
mov rdi, 1336		syscall
add rax, rdi	<b>&lt;=0=</b>	

<b>path_constraint</b>	<b>true</b>
<b>state_map</b>	
rax	-> ((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) & 7)
rbx	-> 7
rdi	-> 1336
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<code>_start:</code>		<code>cmp rax, 1337 &lt;=0=</code>
<code>mov rax, 123</code>		<code>jnz bad</code>
<code>add rax, rsi</code>		
<code>xor rax, rdi</code>	<code>good:</code>	
<code>mov rbx, 2</code>		<code>xor rdi, rdi</code>
<code>add rax, rbx</code>		<code>jmp exit</code>
<code>mov rdi, 3</code>		
<code>mov rsi, rax</code>	<code>bad:</code>	
<code>add rax, rbx</code>		<code>mov rdi, 1</code>
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<code>and rax, rbx</code>		<code>mov rax, 60</code>
<code>mov rdi, 1336</code>		<code>syscall</code>
<code>add rax, rdi</code>		

<code>path_constraint</code>	<code>true</code>
<code>state_map</code>	
<code>rax</code>	<code>-&gt; (((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) &amp; 7) + 1336)</code>
<code>rbx</code>	<code>-&gt; 7</code>
<code>rdi</code>	<code>-&gt; 1336</code>
<code>rsi</code>	<code>-&gt; (((123 + rsi) ^ rdi) + 2)</code>
<code>zf</code>	<code>-&gt; zf</code>

<pre> _start:     mov rax, 123     add rax, rsi     xor rax, rdi     mov rbx, 2     add rax, rbx     mov rdi, 3     mov rsi, rax     add rax, rbx     xor rax, rdi     mov rbx, 7     and rax, rbx     mov rdi, 1336     add rax, rdi </pre>	<pre>         cmp rax, 1337         jnz bad          &lt;=0=  good:     xor rdi, rdi     jmp exit  bad:     mov rdi, 1  exit:     mov rax, 60     syscall </pre>
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<pre> path_constraint  true state_map </pre>	<pre> rax -&gt; (((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) &amp; 7) + 1336) rbx -&gt; 7 rdi -&gt; 1336 rsi -&gt; (((123 + rsi) ^ rdi) + 2) zf  -&gt; (((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) &amp; 7) + 1336)       == 1337 ? 1 : 0 </pre>
--	---

<pre> _start:     mov rax, 123     add rax, rsi     xor rax, rdi     mov rbx, 2     add rax, rbx     mov rdi, 3     mov rsi, rax     add rax, rbx     xor rax, rdi     mov rbx, 7     and rax, rbx     mov rdi, 1336     add rax, rdi </pre>	<pre>         cmp rax, 1337         jnz bad  good:     xor rdi, rdi    &lt;=1=     jmp exit  bad:     mov rdi, 1      &lt;=2=  exit:     mov rax, 60     syscall </pre>
--	---

<pre> path_constraint state_map </pre>	<pre> (((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) &amp; 7) + 1336) == 1337 ... zf    -&gt; 1 </pre>
<pre> path_constraint state_map </pre>	<pre> (((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) &amp; 7) + 1336) != 1337 ... zf    -&gt; 0 </pre>



How do we *reason* about this information?

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With an SMT solver

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With an SMT solver

Mostly

## Data-flow analysis

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2. The formula is fed into the SMT solver
3. The SMT can:
  - Attempt to simplify the formula to get a nicer representation
  - Craft inputs value that will make the formula evaluate to a desired output (i.e. inputs that will make the function return a desired value)

## Compiler optimization techniques

Embedded into the **state\_map** population process:



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Embedded into the `state_map` population process:

- Constant propagation: by construction
- Constant folding: evaluate intermediate expressions on constant values
- Reaching definitions: calculate at a given point the set of definitions that reach it
- Liveness analysis: calculate at a given point the *live* variables (may be read before updated)

# Control-flow analysis

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2. The constraints are fed into the SMT solver
3. The SMT solver can prove the feasibility of the constraints, meaning the path is reachable
  - If it is, retrieve a model for it, i.e. input values that will make the program execution to reach it
  - If it is not, we have detected an obfuscating opaque predicate and can ignore/patch it away

## Example

```
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```

Given 64-bit variables **rdi** and **rsi**:

- Is there any variable assignment (for **rdi** and **rsi**) that makes the **path\_constraint** satisfiable?

**rdi, rsi: 64-bit values**

$((((((((123 + \text{rsi}) \wedge \text{rdi}) + 2) + 2) \wedge 3) \& 7) + 1336) == 1337$

**SMT**

SAT

**model**

rdi = 2

rsi = 1



```
import z3

rdi, rsi = z3.BitVecs('rdi rsi', 64)
path_constraint = (((((((123 + rsi) ^ rdi) + 2) + 2) ^ 3) & 7) + 1336) == 1337

solver = z3.Solver()
solver.add(path_constraint)

if solver.check() == z3.sat:
    print(solver.model())
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**Tooling**

# Tooling

Welcome to the jungle

## Implementation technology

- **Interpreter based:** Miasm, Triton, Angr, Maat, radius2
- **Instrumentation based:** QSYM
- **Compiler based:** KLEE, SymCC, SymQEMU

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## Focus

- **Analysis:** Miasm, Triton, Maat
- **Automagic:** Angr, radius2
- **Test generation:** QSYM, KLEE, SymCC, SymQEMU

## Limitations

And some ideas to overcome them



- Path explosion: the number of control-flow paths grows exponentially ( $\rightarrow \infty$  for unbounded loops)
  - Manual location of interesting code
  - Concolic (**con**crete + **sym**bo**lic**) execution

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  - Manual location of interesting code
  - Concolic (**con**crete + **sym**bo**lic**) execution
- Support for syscalls, standard C library functions, etc.:
  - Same as with any emulator: *hook 'em all*
- Limits of SMT solvers (expression complexity):
  - Program synthesis
  - Math™
  - Imagination

# Part III

## Analysis - Program synthesis

## Motivating example

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Consider the following obfuscated expression:

$$f(x, y, z) = (((x \oplus y) + ((x \wedge y) \times 2)) \vee z) + (((x \oplus y) + ((x \wedge y) \times 2)) \wedge z)$$

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$$f(x, y, z) = (((x \oplus y) + ((x \wedge y) \times 2)) \vee z) + (((x \oplus y) + ((x \wedge y) \times 2)) \wedge z)$$

Treat  $f$  as a *black-box* and observe its behavior:

$$\begin{array}{llll} (1, 1, 1) & \longrightarrow & \boxed{f(x, y, z)} & \longrightarrow 3 \\ (2, 3, 1) & \longrightarrow & \boxed{f(x, y, z)} & \longrightarrow 6 \\ (0, -7, 2) & \longrightarrow & \boxed{f(x, y, z)} & \longrightarrow -5 \\ & & \dots & \end{array}$$

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We want to *synthesize* a simpler function with the same I/O behavior:

$$h(x, y, z) = x + y + z$$



**What is program synthesis?**

## What is program synthesis?

The process of automatically constructing *programs* (code, expressions, etc.) that satisfy a given specification.

## Specification

Describe the expected behavior of the resulting synthesized candidate.

The implementation details are carried out by the synthesizer.

- Formal specification in some logic (e.g. first-order logic):

$$\forall x \in \mathbb{Z}/2^{64} \mathbb{Z}, P(x) = x + 7$$

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$$(0, 7), (-4, 3), (123, 130), (-368, -361), \dots$$

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- A reference implementation (oracle) to generate I/O pairs

# Synthesis approach

Enumerative program synthesis (oracle-guided)

- (Pre)generate an (offline) exhaustive list of potential candidates



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- Select the candidates that match the oracle's I/O behavior
- If possible, verify semantic equivalence

No candidates?

- Extend the pool of candidates (warning: exponential growth)

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Multiple candidates?

- Check for semantic equivalence between them
- Generate more I/O pairs

# QSynth

Combines symbolic execution and enumerative program synthesis iteratively.

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- Synthesize the subexpressions individually
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Paper: <https://profs.scienze.univr.it/~ceccato/papers/2020/bar2020.pdf>



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Public implementations of the QSynth algorithm.

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**msynth**

Built on top of Miasm

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Public implementations of the QSynth algorithm.

**msynth**

Built on top of Miasm

**qsynthesis**

Built on top of Triton

# Limitations

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- Semantic complexity (e.g. non-toy cryptography)
- Non-determinism  $\neg(\text{!})$
- Point functions: constant output except for a single distinguished (small finite set of) input(s)

EOF