

Boolean Algebra

Any mathematics begins with an invention of some kind. Boole invented the basis for his algebra by forming a series of postulates or axioms upon which he built the rest of the algebra. One form of these contains five postulates that relate very nicely to what we'll need for developing digital logic circuits.

Basic Operations:
$$-NOT(-)$$
, $AND(\cdot)$ and $OR(+)$ $B\{0,1\}$

Postulates (No Proof Required)

P1: The operations are closed.

$$x,y \in B\{0,1\}$$

$$x, y \in B\{0,1\}$$
 $\bar{x} \in B\{0,1\}; x \cdot y \in B\{0,1\}; \text{ and } x + y \in B\{0,1\}$

P2: For every operation there exists and identity element

$$x \cdot 1 = x \qquad \qquad x + 0 = x$$

Postulates

P3: The operations are commutative.

$$x + y = y + x$$
 and $x \cdot y = y \cdot x$

$$x \cdot y = y \cdot x$$

- P4: (a) The operator (·) is distributive over (+); i.e., $x \cdot (y+z) = (x \cdot y) + (x \cdot z)$
 - (b) The operator (·) is distributive over (·); i.e., $x + (y \cdot z) = (x + y) \cdot (x + z)$ ス・ストか、モナリ・ルナリモ

P5: For every element $x \in B$, there exists an element $\bar{x} \in B$ (called complement of x) such that $(a)x + \bar{x} = 1$ and $(b)x \cdot \bar{x} = 0$

P6: There exist at least two elements $x, y \in B$ such that $x \neq y$.

Boolean Algebra Duality:

A+13

Dual -> Operation but to AND -> OR AND 70 MOT

• Given a logical expression, its dual is obtained by replacing $+(\cdot)$ operators with operators $\cdot(+)$ respectively and by replacing 0 (1) with 1 (0) respectively.

• The dual of any true statement in Boolean algebra is also a true

statement

Boolean Algebra ---- Some Theorems/Properties

Theorems (Proof & S)	Expression	Dual Expression
T1: Idempotency	x + x = x	$x \cdot x = x$
T2: Complete Cover	x + 1 = 1	$\mathbf{x} \cdot 0 = 0$
T3: Involution	$\overline{(\bar{x})} = x$	$\overline{(\bar{x})} = x$
T4: Associativity	$(x + y) + z = x + (y + z)$ _	$(x \cdot y) \cdot z = x \cdot (y \cdot z)$
T5: DeMorgan's Law	$\overline{(x+y)} = \bar{x}; \bar{y}$	$\overline{(x\cdot y)} = \overline{x} + \overline{y}$
T6: Absorption/Combining	$x + x \cdot y = x .$	$x \cdot (x + y) = x$
T6a: Second Absorption	$x + \bar{x} \cdot y = x + y \cdot$	$x \cdot (\bar{x} + y) = x \cdot y$
T7: Consensus	$x \cdot y + \bar{x} \cdot z + y \cdot z'$ $= x \cdot y + \bar{x} \cdot z$	$(x+y) \cdot (\bar{x}+z) \cdot (y+z)$ $= (x+y) \cdot (\bar{x}+z)$

Property	Expression	Dual Expression
Py1: Combination	$x \cdot y + x \cdot z = x \cdot (y + z) \leftarrow$	$(x+y)\cdot(x+z) = x+y\cdot z$
PY2: Combination	$x \cdot y + x \cdot \overline{y} = x$	$(x+y)\cdot(x+\overline{y})=x$

Theorems

Application of of DeMorgan's Theorem that is easier to say in words than in mathematical form.

To complement a complete expression,

- (a) put in all parentheses to group terms correctly—don't rely on the assumed hierarchy of over +,
- (b) change all the · to + and all the + to ·, and
- (c) change all complemented variables to uncomplemented ones and vice versa.
- (d) Now clean up the parentheses if you need too.

 Complement of a function is obtained by taking the dual of the function and complement each literal.

$$F_{1}(x,y) = x + (\bar{x} \cdot y) - - \rightarrow F_{1D}(x,y) = x \cdot (\bar{x} + y)$$

$$F_{1}(x,y) = x + \bar{x} \cdot y - - \rightarrow F_{1}(x,y) = \bar{x} \cdot y \cdot (x + \bar{y})$$

$$F_{1}(x,y) = x + \bar{x} \cdot y - - \rightarrow F_{1}(x,y) = \bar{x} \cdot y \cdot (x + \bar{y})$$
De Morganis law

Applying DeMorgan Theorem

DeMorgan's Theorem is quite useful because complementing an expression arises from time to time.

To illustrate the use of the generalized form of the theorem, we will do an example.

Consider the logic expression that I want to complement:

$$Z = A + B \bullet \bar{C} + \overline{(\bar{D} + E \bullet \bar{F} \bullet G)}$$

We want
$$\overline{Z} = A + B \cdot \overline{C} + (\overline{D} + (E \cdot \overline{F} \cdot G))$$

First, we will install the parentheses to group terms so that we are not relying on the assumed hierarchy of • over +:

The med merarchy of
$$\cdot$$
 over $+$.

$$\bar{Z} = A + (B \cdot \bar{C}) + (\bar{D} + (E \cdot \bar{F} \cdot G))$$

$$A \cdot (B + C) \cdot (D \cdot (\bar{E} + F + \bar{G}))$$

Applying DeMorgan Theorem

Then we will unprime the primed terms and prime the unprimed ones and also exchange • and + operators.

Note that there is a "big" term in the parentheses at the right that has a prime on it (just after double parentheses).

I remove this prime and leave all the terms and operators inside alone.

$$\bar{Z} = \bar{A} \bullet (\bar{B} + C) \bullet (\bar{D} + (E \bullet \bar{F} \bullet G)).$$

Finally, I clean up any unnecessary parentheses:

$$Z' = \bar{A} \bullet (\bar{B} + C) \bullet (\bar{D} + E \bullet \bar{F} \bullet G).$$

The result is now the complement of the original expression.

This can be carried to any depth of the parentheses or complication of expression, but "real-world" logic functions rarely get that messy.

- Let's use Boolean algebra to prove one of the theorems.
- We want to demonstrate a way of showing that something is true (or false, for that matter).
- The simplest approach, which is almost a no-brainer, uses perfect induction to demonstrate that every possible combination of 0s and 1s yields the correct result.
- Perfect induction is most easily carried out by writing a truth table that tells the whole truth and nothing but the truth.
- We will prove using perfect induction the second half of property PY1, which says $(A + B) \cdot (A + C) = A + B \cdot C$.

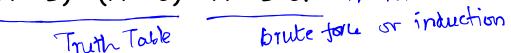


Figure shows the truth table for the given function

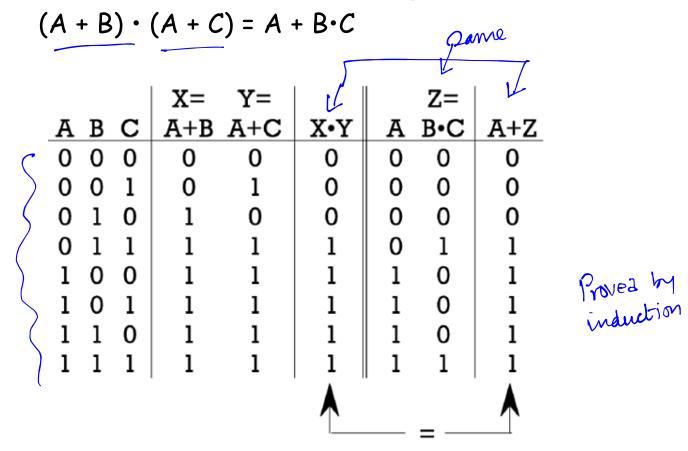


Figure Truth Table

- Here we developed the table by creating the "input" columns A, B, and C and listing all eight of the possible arrangements of 0s and 1s. (There are three variables, each of which can have one of two values, so there are $2^3 (= 8)$ rows in this truth table.) We list them in ascending binary number order
- Compute two columns of the parenthesized combinations (A + B) and (A + C) using postulates P3, P4, and P5 that tell how the + operation works.
- Now combine these two columns using the · operation and postulates P3, P4, and P5.

That's the result for the left-hand side of the = sign of the theorem we are proving.

Now do the same for the right-hand side:

- Repeat the A column for convenience.
- Compute the value of B·C using postulates P3, P4, and P5.
- Now combine these two columns using the + operation and postulates P3, P4, and P5.

That's the result for the right-hand side of the = sign.

Are the two columns equal all

 the way down? If so, we have proven the theorem by showing that it works for every possible combination of values of A, B, and C. That's proof by perfect induction

De Morgan's Law

Proving $(A + B) \cdot (A + C) = A + B \cdot C$, using postulates and Theorems

Let
$$A + B = X$$
 and write $(A + B) \cdot (A + C) = X \cdot (A + C) = X \cdot A + X \cdot C$ (by postulate 4)

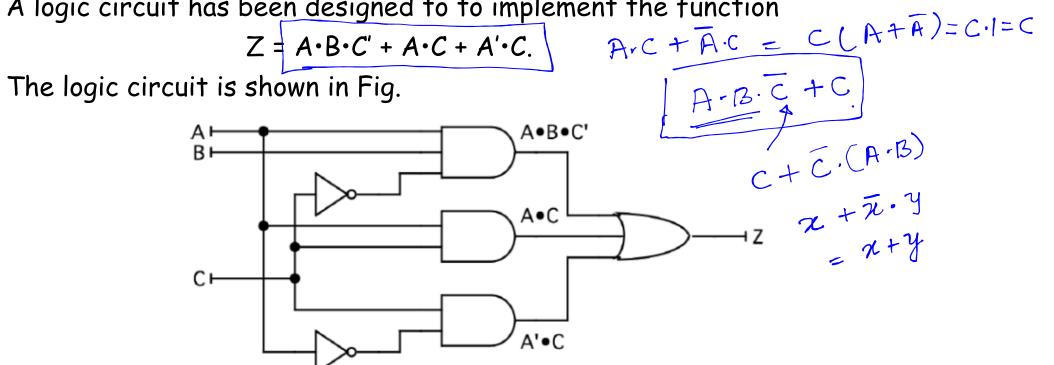
Let
$$A + B = X$$
 and write $(A + B) \cdot (A + C) = X \cdot (A + C) = X \cdot A + X \cdot C$ (by postulate 4)

$$X \cdot A + X \cdot C = (A + B) \cdot A + (A + B) \cdot C = A + A \cdot C + B \cdot C = A + B \cdot C$$
 (using T6 and postulate 4)

$$A \cdot A + A \cdot B \Rightarrow A \cdot C + B \cdot C = A + A \cdot C + B \cdot C = A + A \cdot B \Rightarrow A \cdot C + A \cdot C + A \cdot B \Rightarrow A \cdot C + A \cdot C + A \cdot C + A \cdot C \Rightarrow A \cdot C + A \cdot C + A \cdot C \Rightarrow A \cdot C + A \cdot C + A \cdot C \Rightarrow A \cdot C + A \cdot C + A \cdot C \Rightarrow A \cdot C + A \cdot C + A \cdot C \Rightarrow A \cdot C + A \cdot C + A \cdot C \Rightarrow A \cdot C \Rightarrow A \cdot C + A \cdot C \Rightarrow A \cdot C$$

Logic Simplification

A logic circuit has been designed to to implement the function



This circuit requires four gates and two inverters.

Logic Simplification

We will use Boolean algebra to reduce the number of gates if possible.

The process is by no means algorithmic, which means we can't write any definitive rules to tell how to proceed.

This depends on combining and simplifying terms.

Our goal is to reduce the number of gates, which means combining terms to get rid of as many + and • operations as we can.

Logic Simplification

Here's the original expression:

 $Z = A \cdot B \cdot \overline{C} + A \cdot C + \overline{A} \cdot C.$ the last two terms, reducing + $A \cdot B \cdot C + A \cdot B \cdot C$ $R \cdot \overline{C}$ • Apply PY2 to the last two terms, reducing them to simply C:

$$Z = A \bullet B \bullet \bar{C} + C.$$

 Use T4(associativity) and P3 (commutativity) to regroup the remaining terms:

$$Z = C + \bar{C} \bullet (A \bullet B).$$

 $Z = C + \bar{C} \cdot (A \cdot B).$ Now expand this expression somewhat by using T6a $(A + \bar{A}B = A + B)$ to get the simplified form:

$$Z = C + (A \cdot B)$$
 ----- A much simpler expression

introduce définitions.

The real test, though, is whether the gate circuit is simpler. Look at Figure Have we succeeded in reducing the circuit from six logic gates to only two logic gates.

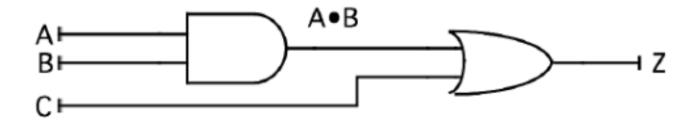


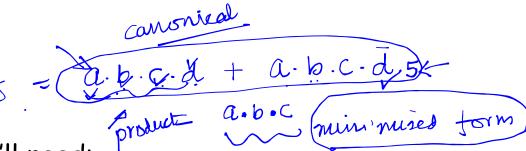
FIGURE A logic circuit simplified

CANONICAL FORMS

- The starting point for most of the algorithms for simplification is an expression in canonical form.
- While that sounds like something awful, it isn't. It's just a neat starting point that gets the algorithmic process off to a proper start.
- To get through this, we have some new terms to learn.
- In every field there are terms that are specific to that field, that help define things sharply.
- Digital logic is no different.

Definitions





Here are the important definitions that we'll need:

- Product term is a term where the variables are connected by and operators, terms such as $A \bullet B$, $\overline{C} \bullet D \bullet E$, and even the simple term F.
- Sum term is a term where the variables are connected by or operators, terms such as A + B, $\bar{C} + D + E$, and even the simple term F.
- Literal is any appearance of a variable in a term, whether primed or not. In the terms above, the literals are A, B, \bar{C}, D, E , and F. If the variable appears both primed and unprimed, as in G and \bar{G} , each is a literal.
- Sum-of-products is an expression consisting of product terms linked by or operators, as in $A \cdot B + \bar{C} \cdot D \cdot E + FR^{\text{Norm}} \text{SOP}$ expression $F = F \cdot C$
- Product-of-sums is an expression consisting of sum terms linked by and operators, as in $(A + B) \cdot (\bar{C} + D + E) \cdot F$.

Definitions

SOP SOP + A.BC + A.BC, SOP

Normal term is a term, either a product term or a sum term, in which no variable appears more than once.

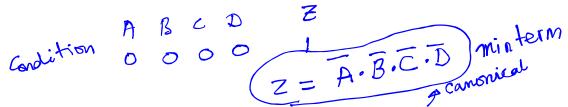
For example, the term $A \cdot B \cdot C \cdot C$ is not normal because we can use theorem T3 to remove one appearance of C to yield $A \cdot B \cdot C$.

 \triangleright Also, the term $\underline{D \bullet E \bullet \overline{E}}$ is not normal because we can use theorem T5 to reduce the term to 0.

Non-normal terms are pretty obvious and represent unnecessary complications in our logic circuits.

ABC

Definitions



- Minterm is a normal product term that contains all the variables of the problem.
 - For example, if the problem contains the variables A, B, and C, then all minterms must contain these three variables, with or without primes.
 - ightharpoonup So $\overline{A} \bullet B \bullet C$ and $A \bullet \overline{B} \bullet \overline{C}$ are two possible minterms in this case.
 - \succ The term $A \cdot B$ is not a minterm in this case because it does not contain C.

> To make life a little more orderly, we usually write the variable names in alphabetical order.

 $Z = \overline{A \cdot B \cdot C \cdot D}$ $\overline{A \cdot B$