

OF:

- Decode the Inst<sup>n</sup>
- Fetch the register operands from RFs.
- Compute the branch target. (PC + offset)
- Compute the immediate (16 bit + 2 bit modifier)  
32 bit number.
- Generate Control Signals.

Ex Stage:Contains ALU

- Perform all arithmetic & logical operation.
- Contains the branch unit for computing branch condition (beq, bgt)
- Contains Flag register (update by cmp inst<sup>n</sup>)