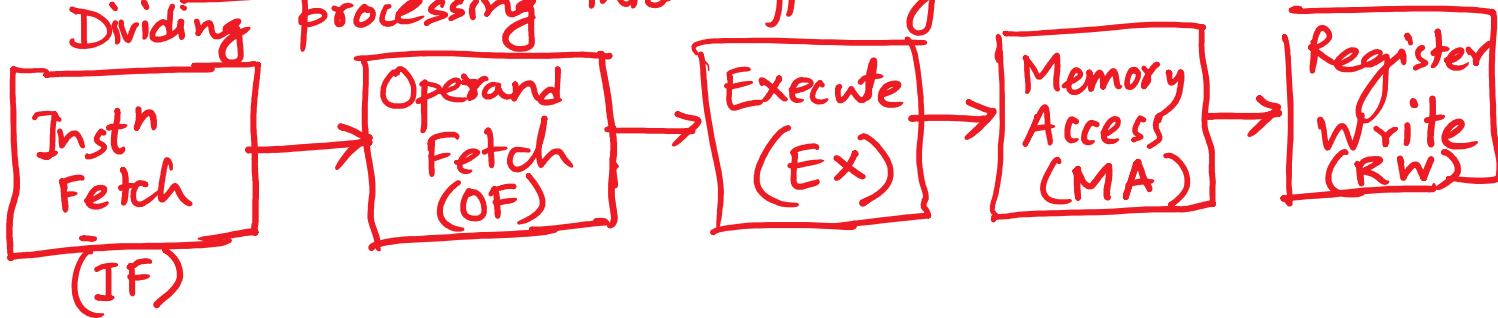
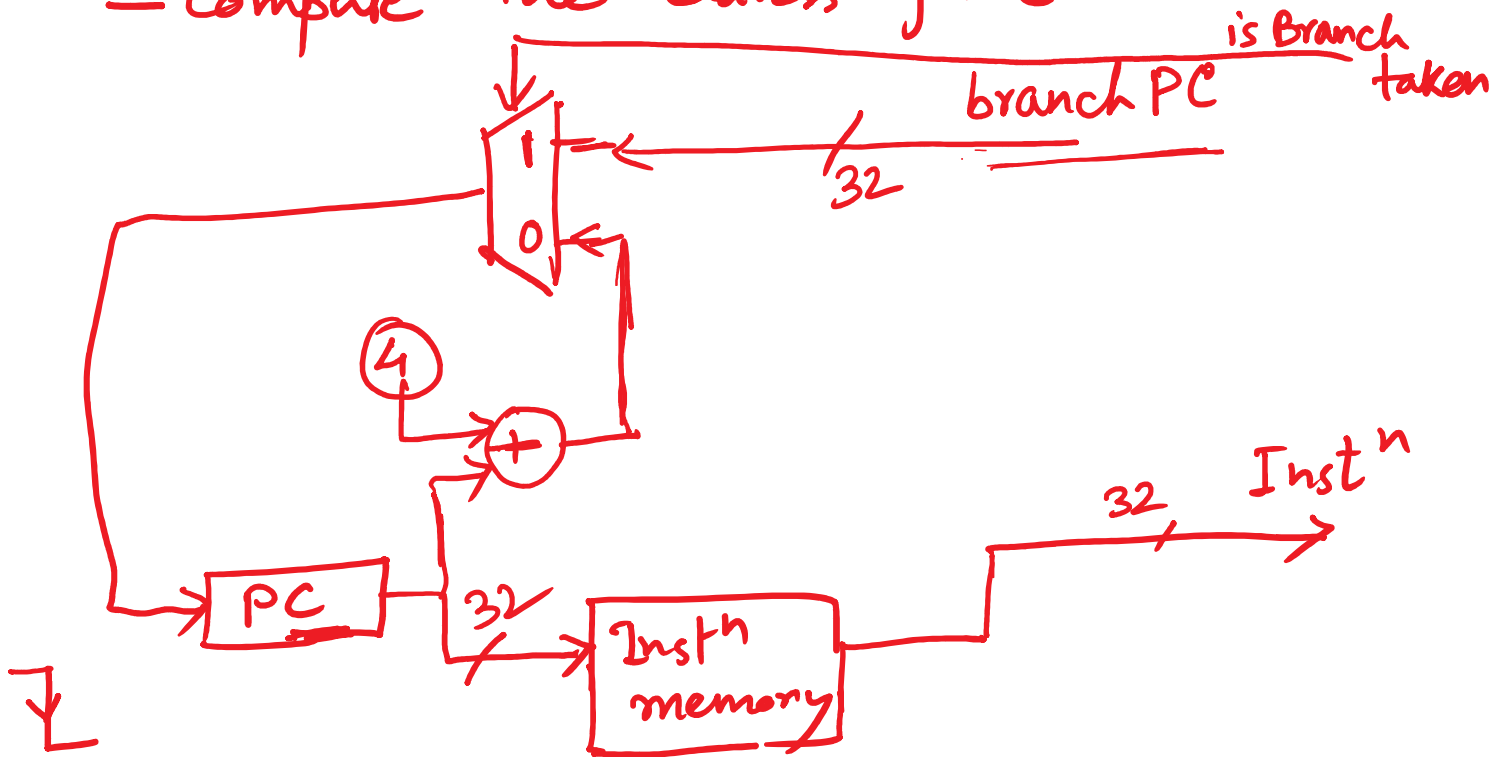


CSE 112Processor Design

Dividing processing into diffⁿ stages.

IF

- Fetch an instⁿ from Instⁿ memory
- Compute the address of the next Instⁿ.

OF

- Decode the Instⁿ

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- Decode the Instⁿ
- Fetch the register operands from reg. files.
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