INDRAPRASTHA INSTITUTE OF INFORMATION TECHNOLOGY, DELHI

ECE111 Digital Circuits

Practice problems 1

Q.1 Show that different realizations given Fig. 1(a), 1(b) and 1(c) result in same output.

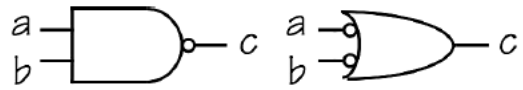

Fig. 1(a)

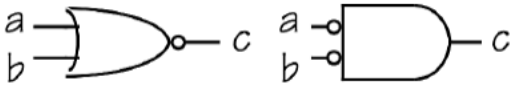

Fig. 1(b)

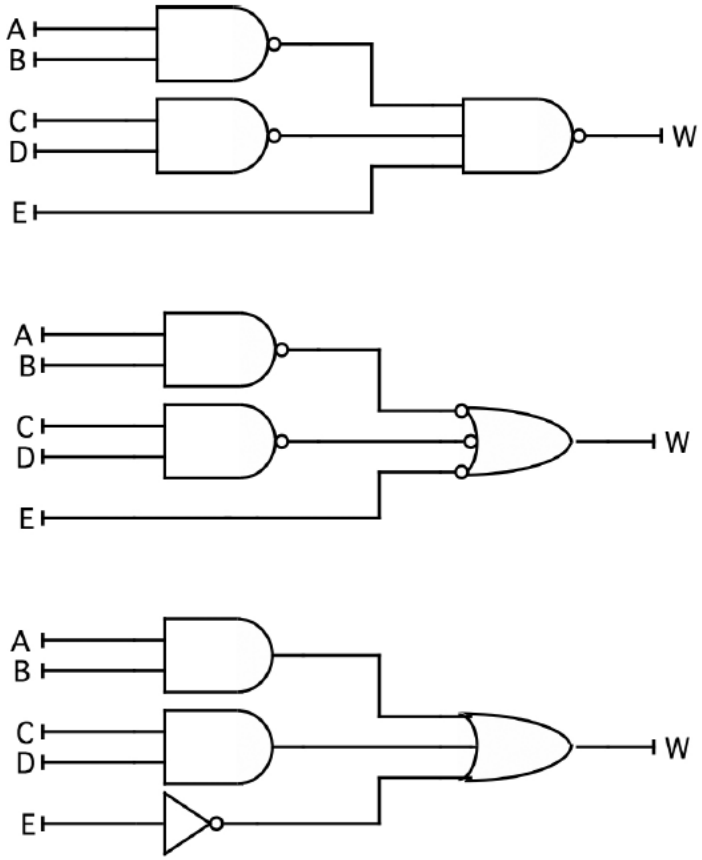

Fig. 1(c)

Q.2 Realize W = AB + CD + EF using 2-input NAND gates only

Q.3 Show that $W\bar{Y} + W\bar{Y} + XY = X + \bar{Y}$ using Boolean algebra axioms and verify this by using truth table.

Q.4 Find the output for the logic circuit given in Fig. 2(a) – 2(e)

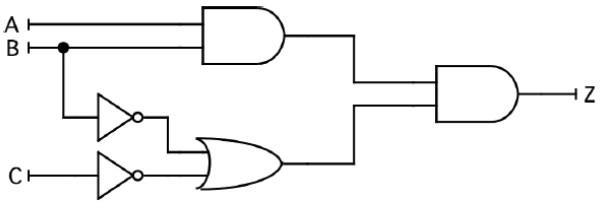

Fig. 2(a)

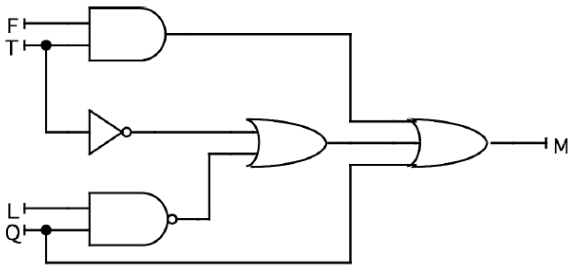

Fig. 2(b)

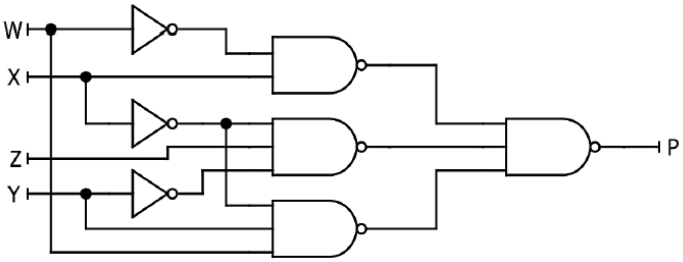

Fig. 2(c)

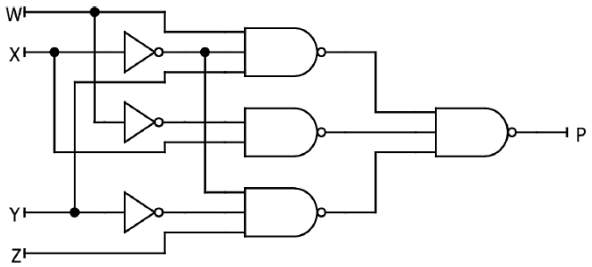

Fig. 2(d)

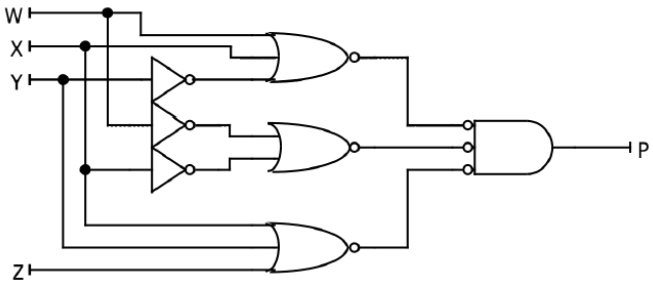

Fig. 2(e)

5. For the circuits given write the expression for the output S and C of the circuits given below and also obtain the corresponding Truth Tables:

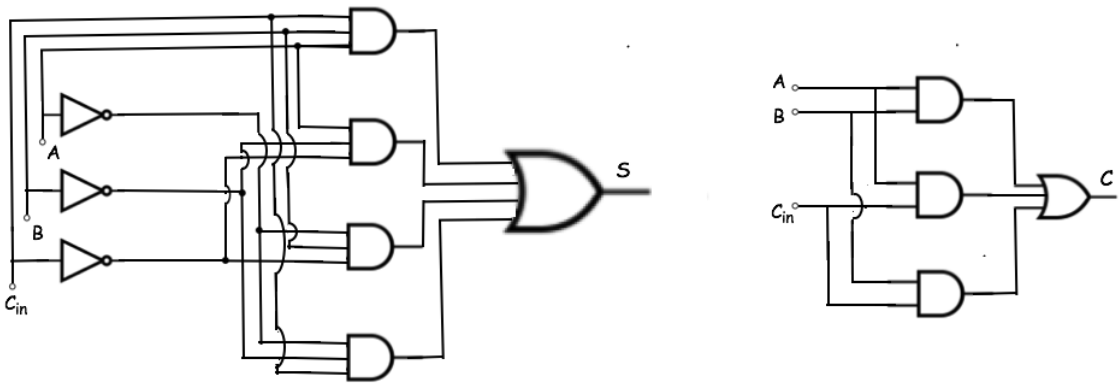

Fig. 3

6. For the above circuit draw the timing diagram with ABC going through the values 000, 001, 010, 011, 100, 101, 110, 111, 000.

7. Verify the following expressions with the help of truth Table:

   (i) A + AB' + ABC' = A#

   (ii) x.y + x'z + y.z = x.y + x'.z

8. Show the following equivalences using Boolean Algebra Axioms and verify with Truth Table.

$$(i)\ A + A\bar{B} + AB\bar{C} = A \qquad (ii)\ x.y + y.z + \bar{x}.z = x.y + \bar{x}.z \qquad (iii)\ x + \bar{x}.y = x + y$$