Name of Certifying Engineer(s):
Email of Certifying Engineer(s):
Name(s) of System Under Test:

Division (check one):
- ❏ Open
- ☑ Closed

Category (check one):
- ☑ Available
- ❏ Preview
- ❏ Research, Development, and Internal (RDI)

Benchmark(s) (check all that apply):
- ☑ Visual Wake Words
- ☑ Keyword Spotting
- ☑ Anomaly Detection
- ☑ Image Classification

Please fill in the following table adding lines as necessary:

System Under Test Name	Benchmark	Accuracy/AUC
ADP-XC7K160/410 FPGA (AndesCore_D25F/AE350)	vww-int8	85.8% / 0.94
ADP-XC7K160/410 FPGA (AndesCore_D25F/AE350)	kws-int8	90.10% / 0.99
ADP-XC7K160/410 FPGA (AndesCore_D25F/AE350)	ad-int8	77.4% / 0.86
ADP-XC7K160/410 FPGA (AndesCore_D25F/AE350)	ic-int8	87% / 0.98
ADP-XC7K160/410 FPGA (AndesCore_D45/AE350)	vww-int8	85.8% / 0.94
ADP-XC7K160/410 FPGA (AndesCore_D45/AE350)	kws-int8	90.10% / 0.99
ADP-XC7K160/410 FPGA (AndesCore_D45/AE350)	ad-int8	77.40% / 0.86
ADP-XC7K160/410 FPGA (AndesCore_D45/AE350)	ic-int8	87% / 0.98
Xilinx VCU118 FPGA	ad-int8	77.4% / 0.86

(AndesCore_NX27V/AE350)		

For each SUT, is the benchmark Accuracy/AUC target met? (Not a requirement for the Open division) (check all that apply):
- ☑ Yes (Visual Wake Words … 80% Accuracy)
- ☑ Yes (Keyword Spotting … 90% Accuracy)
- ☑ Yes (Anomaly Detection … 0.85 AUC)
- ☑ Yes (Image Classification ... 85% Accuracy)
- ❏ No, for some combination of benchmark, scenario and SUT

For each SUT and benchmark, did the submission run on the whole validation set in accuracy mode? (check one):
- ☑ Yes
- ❏ No

For each SUT and benchmark, does the submission use the EEMBC Runner? (check one)
- ☑ Yes
- ❏ No

For each SUT and benchmark, is the same code run in accuracy and performance modes? (check one)
- ☑ Yes
- ❏ No

Are the weights calibrated using data outside of the official calibration set? (check one)
- ❏ Yes
- ☑ No

What numerics does the submission use? (check all that apply)
- ❏ INT4
- ☑ INT8
- ❏ INT16
- ❏ UINT8
- ❏ UINT16
- ❏ FP11
- ❏ FP16
- ❏ BF16
- ❏ FP32
- ❏ Other, please specify:

What backend does the submission use? (check all that apply)
- ❏ Vendor backend, please name:

☑ TF-Lite Micro
❏ Micro TVM
❏ Other, please specify:

Which of the following caching techniques does the submission use? (check all that apply, ideally none):
❏ Caching Inputs between iterations
❏ Caching responses between iterations
❏ Caching intermediate computations between iterations

Which of the following techniques does the submission use? (check all that apply, ideally none if submitting to the closed division.)
❏ Quantization aware training
❏ Wholesale weight replacement
❏ Weight supplements
❏ Discarding non-zero weight elements
❏ Pruning
❏ Modifying weights during the timed portion of an inference run
❏ Hard coding the total number of queries
☑ None of the above

Is the submission congruent with all relevant MLPerf rules?
☑ Yes
❏ No

If the answer to the above question is no, please explain:

For each SUT, have you filled out the JSON system description file?
☑ Yes
❏ No

For each SUT, does the submission accurately reflect the real-world performance of the SUT?
☑ Yes
❏ No

Does your submission include the following: (check all that apply)
☑ System description file
☑ Code that implements the benchmarks
❏ Code/scripts that train the model(s) (Open Division)
❏ Metadata that describes each system-implementation combination tested
☑ Scripts that set up and execute each system implementation tested

This checklist must be submitted as a PDF as part of your submission.

- ☑ Result logs for each system implementation tested
- ☑ This Checklist