Exp No	Date	Name	Faculty	TAs	Lab Staff		
1	Thursday, 7 August, 2025	Objective 1.1 -Introduction for lab instruments Objective 1.2 LT spice simulation	Dr Mitradip	Team 1	Mr Anurag		Suyash, Vaishali
						TA Te	eam 1 Ishita, Rajul, Yogesh
2	Thursday, 14 August, 2025	Familiarization with DC and AC Sources, practice soldering; connection of resistors in series and parallel Objective 2.1: IV characteristics of Ohm's law (DC input). Objective 2.2: IV characteristics of Ohm's law (AC input).	Dr Sujit	Team 2	Mr Anurag		Rishabh
							Asif, Ariba
3	Friday, 22 August, 2025	Objective 3.1: Charging and discharging characteristics of a capacitor using DC source. Objective 3.2: Effect on the output of RC circuit for a square wave input signal. Objective 3.3: Experiment with RL circuit with DC source. Record the voltage and current across the L with time.	Dr Ankur	Team 1	Mr Anurag	TA Te	Harshita, Shubham, Nidhi Shrestha
4	Friday, 29 August, 2025	Objective 4.1 -Design a passive low pass filter in LT spice and verify with experiment Objective 4.2- Design a passive High pass filter in LT spice and verify with experiment	Dr Arijit	Team 2	Mr Anurag		
5	Thursday, 4 September, 2025	Objective 5.1: For the given RLC circuit, apply input signals of different frequencies and vary the resistance values for a fixed value of L and C and measure output voltage, Vout(t). Objective 5.2 Measuring Vout for these combinations for observing underdamp, overdamp and critically damped behavior after adjusting the appropriate values of RLC.	Dr Santanu	Team 1	Mr Anurag		
6	Friday, 12 September, 2025	Objective 6.1: I-V characteristics of a diode. Objective 6.2: Study of Full-wave and Half-wave rectifier circuit Objective 6.3: Study of Clipper and clamper circuit.	Dr Kundan	Team 2	Mr Anurag		
	Friday, 19 September, 2025	MAKE-UP SESSION/RESERVE DAY		ALL TAs	Mr Anurag		
Mid-sem	Thursday, 25 September, 2025				Mr Anurag		
Lab Exam	Friday, 26 September, 2025	MID SEM ECS 327 EXAM	All Faculties	ALL TAs	Mr Anurag		
No Lab	Sept 20 to Sept 28 2025	MID SEM EXAM WEEK			I WII Ariurag		
	Sept 29 to Oct 05 2025	MID SEM BREAK WEEK					
7	Friday, 10 October, 2025	Objective 7.1: Study of common emitter configuration (Ic vs Vce for different Vbe). NPN -BC547	Dr Poonam	Team 1	Mr Anurag		
8	Friday, 17 October, 2025	Objective 8.1: Design and realize Inverting amplifier using IC- 741 Op-amp. Objective 8.2: To study the working of op-amp as a differentiator. Objective 8.3: To study the working of op-amp as an integrator.	Dr Aditya	Team 2	Mr Anurag		
9	Friday, 24 October, 2025	9. To design a Schmitt trigger using Op-amp IC-741.	Dr Ganga	Team 1	Mr Anurag		
10	Friday, 31 October, 2025	10. Digital Logic – logic gate Design using BJT BC 547	Dr Haroon	Team 2	Mr Anurag		
	Friday, 7 November, 2025	MAKE-UP SESSION/RESERVE DAY		ALL TAs	Mr Anurag		
End-sem Lab Exam	Thursday, 13 November, 2025	FINAL EXAM ECS 327 EXAM	All Faculties	ALL TAs	i Alidiag		
	Friday, 14 November, 2025	FINAL EXAM ECS 327 EXAM	All Faculties		Mr Anurag		
Exams	Date	Labs	Faculty	TAs	Lab Staff		
Mid-sem Lab Exam	Thursday, 25 September, 2025						
	Friday, 26 September, 2025	Experiments 1 to 6	All Faculties	All TAs	Anurag		
End-sem Lab Exam	Thursday, 13 November, 2025				Anurag		
	Friday, 14 November, 2025	All Experiments	All Faculties	All TAs			