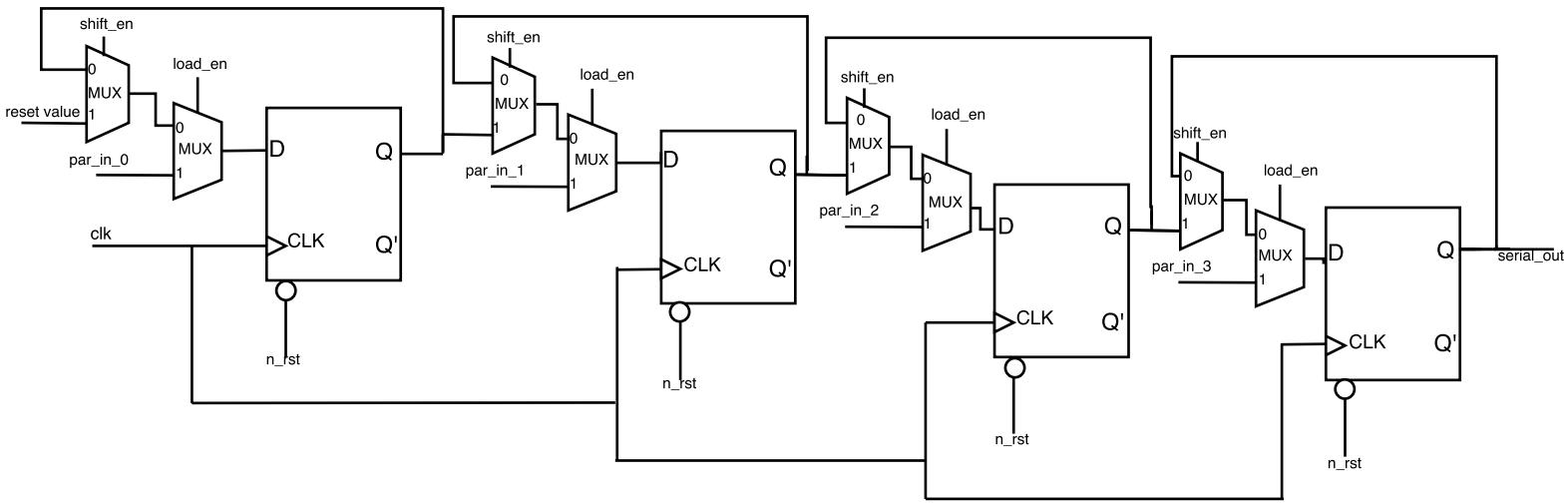


Parallel To Serial Shift-Register

RTL Diagram



Schematic Diagram

