

Batch read in bit units (command: 0401)

Read values from devices in bit units.



When accessing any of the following devices, use the device extension specification (subcommand: 008□).

- Link direct device
- Module access device
- CPU buffer memory access device

For the message format for device extension specification, refer to the following section.

Page 438 Read/Write by Device Extension Specification

Message format

The following shows the message format of the request data and response data of the command.

Request data

Command	Subcommand	Head device	Number of device points
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Response data

The value of read device is stored in bit units. The data order differs between ASCII code or binary code. (Page 72 Read data, write data)

Data specified by request data

Command

Frame	ASCII code	Binary code
4C/3C/4E/3E frame	<div><div>0401</div><div>30H, 34H, 30H, 31H</div></div>	<div><div></div><div>01H, 04H</div></div>
2C frame	<div><div>1</div><div>31H</div></div>	—

Subcommand

Type	ASCII code	Binary code										
For MELSEC-Q/L series	<table><tr><td>0</td><td>0</td><td>0</td><td>1</td></tr><tr><td>30_H</td><td>30_H</td><td>30_H</td><td>31_H</td></tr></table>	0	0	0	1	30 _H	30 _H	30 _H	31 _H	<table><tr><td>01_H</td><td>00_H</td></tr></table>	01 _H	00 _H
0	0	0	1									
30 _H	30 _H	30 _H	31 _H									
01 _H	00 _H											
For MELSEC iQ-R series	<table><tr><td>0</td><td>0</td><td>0</td><td>3</td></tr><tr><td>30_H</td><td>30_H</td><td>30_H</td><td>33_H</td></tr></table>	0	0	0	3	30 _H	30 _H	30 _H	33 _H	<table><tr><td>03_H</td><td>00_H</td></tr></table>	03 _H	00 _H
0	0	0	3									
30 _H	30 _H	30 _H	33 _H									
03 _H	00 _H											

For 2C frame, the specification is not required. Functions and specification methods are equivalent to the subcommands for MELSEC-Q/L series.

Head device

Specify the head device of the consecutive devices. (Page 65 Devices)

Restriction

- The following devices cannot be specified.
- Long timer (contact: LTS, coil: LTC)
 - Long retentive timer (contact: LSTS, coil: LSTC)
 - Long index register (LZ)
- Page 69 Considerations when accessing long timer, long retentive timer, or long counter
- Page 69 Considerations when accessing long index register

Number of device points

Specify the number of device points to be read within the following range. (Page 70 Number of device points)

Access target	C24	E71	
		ASCII code	Binary code
MELSEC iQ-R series module MELSEC iQ-L series module MELSEC-Q/L series module	1 to 7904 points	1 to 3584 points	1 to 7168 points
MELSEC-QnA series module Module on other station via MELSEC-QnA series network module	1 to 3952 points	1 to 1792 points	1 to 3584 points
MELSEC-A series module	1 to 256 points		

Communication example

Read values of M100 to M107. (Subcommand: for MELSEC-Q/L series)

Data communication in ASCII code

(Request data)

Subcommand				Device code	Head device number				Number of device points				
0	4	0	1	0	0	0	1	M	*	0	0	0	8
30 _H	34 _H	30 _H	31 _H	30 _H	30 _H	30 _H	31 _H	4D _H	2A _H	30 _H	30 _H	30 _H	38 _H

(Response data)

0	0	0	1	0	0	1	1	0 = OFF
30H	30H	30H	31H	30H	30H	31H	31H	1 = ON
M100				to				M107

Data communication in binary code

(Request data)

Subcommand		Device code		Head device number		Number of device points	
01H	04H	01H	00H	64H	00H	00H	90H
01H	00H	08H	00H				

(Response data)

00H	01H	00H	11H	0 = OFF
				1 = ON
M101				M107
to				M106
M100				