Errata

VOR-ER1001: Internal Voltage regulator		
Description	**Workaround**	**Comment**
The internal 1.5V digital and analog regulators are non-functional	An external 3.3V and 1.5V supply must be applied to the device	Present in VA41620/30 Rev A Silicon

VOR-ER1002: External Parallel Bus Interface		
Description	**Workaround**	**Comment**
EBI not functioning as intended	Boot from an EBI memory (EBI_BOOT=VDD33).	Present in VA41620/30 Rev A Silicon

VOR-ER1003: EBI Boot		
Description	**Workaround**	**Comment**
Boot from EBI causes pins PORTC[1:0], PORTE[11:10], and PORTF[15:2] do not function as intended	Boot from internal NVM(VA41630) or external NVM via ROM_SPI port(VA41620)	Present in VA41620/30 Rev A Silicon

VOR-ER1004: Serial Wire Debug		
Description	**Workaround**	**Comment**
Serial Wire Debug (SWD) not functioning as intended without code in boot memory	VA41630: Will ship devices with code in NVM, do not erase internal NVM and leave blank VA41620: External memory must have code programmed into it	Present in VA41620/30 Rev A Silicon

VOR-ER1005: PLL_LOST_LOCK DET		
Description	**Workaround**	**Comment**
PLL_LOST_LOCK_DET function can lock up the chip	Do not use PLL_LOST_LOCK_DET function	Present in VA41620/30 Rev A Silicon Programmers Guide V0.83

VOR-ER1006: RST_CNTL_ROM Register		
Description	**Workaround**	**Comment**
RST_CNTL_ROM not functioning as intended	Do not clear any bits in this register	Present in VA41620/30 Rev A Silicon Programmers Guide V0.83

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VOR-ER1007: Clock Generation Control Register (CTRL0)		
Description	Workaround	Comment
Setting REF_CL_SEL bits to b11 doesn't match specification	Do not set REF_CL_SEL bits to b11	Present in VA41620/30 Rev A Silicon Programmers Guide V 0.83

VOR-ER1008: True Random Number Generator (TRNG)		
Description	Workaround	Comment
A read of EHR_DATA[5] will always return a value of 0x0000_0000 and will automatically clear the contents of EHR_DATA[4:0]	Do not read EHR_DATA[5] until after EHR_DATA[4:0] has been read	Present in VA41620/30 Rev A Silicon Programmers Guide V 0.83

VOR-ER1009: Space Wire Port		
Description	Workaround	Comment
Receive pins do not work as specified	None at this time	Present in VA41620/30 Rev A Silicon

VOR-ER1010: Digital-to-Analog converter (DAC)		
Description	Workaround	Comment
Intermittent functionality at Power-up	Power Cycle until DAC is functioning	Present in VA41620/30 Rev A Silicon

VOR-ER1011: Digital-to-Analog converter (DAC) Output Voltage		
Description	Workaround	Comment
DAC output voltage may not be within the specified range	If the output voltage does not meet the specification, minor adjustments can be done in software to compensate for this issue.	Present in VA41620/30 Rev A Silicon VORAGO can help with this software.

VOR-ER1012: Analog-to-Digital converter (ADC) Voltage Reading		
Description	Workaround	Comment
ADC voltage reading may be not be within the specified range	If the input voltage reading does not meet the specification, minor adjustments can be done in software to compensate for this issue	Present in VA41620/30 Rev A Silicon VORAGO can help with this software.

VOR-ER1013: Power OK (POK)		
Description	**Workaround**	**Comment**
Device may not operate if VDD33 is below 3.3V	Set VDD33 to 3.3V or higher	Present in VA41620/30 Rev A Silicon Programmers Guide V 0.83

VOR-ER1014: EBI_CFG registers		
Description	**Workaround(s)**	**Comment**

EBI_CFG1-3 bits 16-18 and 25 are read from EBI_CFG0	1.) Use same sized external memories with the longest read cycle time of the all them memories being stored in bits 16-18.
	
	2.) Set bits 16-18 and 25 for EBI_CFG0 to appropriate values each time memory is accessed.
	    A.) Data transfers from one external memory to the other have to be temporarily stored in internal SRAM
	    B.) External memory read and writes while running out of separate external memory cannot be executed.
		Present in VA41620/30 Rev A Silicon

VORAGO Technologies
1501 S MoPac Expressway, Suite #350
Austin, TX 78746
www.voragotech.com

Email: info@voragotech.com
Phone: (512) 347-1800