

SPEC® CPU2017 Integer Rate Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

My Corporation

SPECrate2017_int_base = 0.00

SPECrate2017_int_peak = 0.00

CPU2017 License: nnn (Your SPEC license number)

Test Sponsor: My Corporation

Tested by: My Corporation

Test Date: Apr-2019

Hardware Availability:

Software Availability:

Copies

500.perlbench_r

502.gcc_r

505.mcf_r

520.omnetpp_r

523.xalancbmk_r

525.x264_r

531.deepsjeng_r

541.leela_r

548.exchange2_r

557.xz_r

Hardware

CPU Name: Intel Core i5-7500

Max MHz.:

Nominal:

Enabled: cores, 1 chip, threads/core

Orderable:

Cache L1:

L2:

L3:

Other:

Memory: 7.677 GB fixme: If using DDR4, the format is:
'N GB (N x N GB nRxn PC4-nnnnX-X)'

Storage: 341 GB add more disk info here

Other:

Software

OS: Ubuntu 18.04.1 LTS

4.15.0-46-generic

Compiler: C/C++: Version 3.9.0 of Clang, the
LLVM Compiler Infrastructure
Fortran: Version 4.8.2 of GCC, the
GNU Compiler Collection
DragonEgg: Version 3.5.2, the
LLVM Compiler Infrastructure

Parallel: No

Firmware:

File System: ext4

System State: Run level 5 (add definition here)

Base Pointers: 64-bit

Peak Pointers: 64-bit

Other:

Errors

Complete set of valid runs for peak rate unavailable (520.omnetpp_r missing)

Complete set of valid runs for peak rate unavailable (541.leela_r missing)

Complete set of valid runs for peak rate unavailable (505.mcf_r missing)

(Continued on next page)

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Errors (Continued)

Complete set of valid runs for peak rate unavailable (557.xz_r missing)

Complete set of valid runs for peak rate unavailable (548.exchange2_r missing)

Complete set of valid runs for peak rate unavailable (525.x264_r missing)

Complete set of valid runs for peak rate unavailable (502.gcc_r missing)

Complete set of valid runs for peak rate unavailable (531.deepsjeng_r missing)

Complete set of valid runs for peak rate unavailable (500.perlbench_r missing)

Complete set of valid runs for peak rate unavailable (523.xalancbmk_r missing)

There is no set of valid runs with the same number of copies for base

'reportable' flag not set during run

520.omnetpp_r (base) did not have enough runs!

541.leela_r (base) did not have enough runs!

505.mcf_r (base) did not have enough runs!

557.xz_r (base) did not have enough runs!

548.exchange2_r (base) did not have enough runs!

525.x264_r (base) did not have enough runs!

502.gcc_r (base) did not have enough runs!

531.deepsjeng_r (base) did not have enough runs!

500.perlbench_r (base) did not have enough runs!

523.xalancbmk_r (base) did not have enough runs!

557.xz_r (base) had invalid runs!

520.omnetpp_r (peak) did not have enough runs!

541.leela_r (peak) did not have enough runs!

505.mcf_r (peak) did not have enough runs!

557.xz_r (peak) did not have enough runs!

548.exchange2_r (peak) did not have enough runs!

525.x264_r (peak) did not have enough runs!

502.gcc_r (peak) did not have enough runs!

531.deepsjeng_r (peak) did not have enough runs!

500.perlbench_r (peak) did not have enough runs!

523.xalancbmk_r (peak) did not have enough runs!

557.xz_r (peak) had invalid runs!

Input set must be 'refrate' for a valid run (set to 'test' for this run)

Run of 557.xz_r (base) was not valid; status is RE

Run of 557.xz_r (peak) was not valid; status is RE

Unknown flags were used! See

<https://www.spec.org/cpu2017/Docs/runcpu.html#flagsurl>
for information about how to get rid of this error.

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~~Test Date:~~ Apr-2019

Hardware Availability:

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Peak

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r														
502.gcc_r														
505.mcf_r														
520.omnetpp_r														
523.xalancbmk_r														
525.x264_r														
531.deepsjeng_r														
541.leela_r														
548.exchange2_r														
557.xz_r	1	0.000576	0.00					1	0.000585	0.00				

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Results appear in the order in which they were run. **Bold underlined text indicates a median measurement.**

The config file option 'submit' was used.

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/usr/lib/llvm-6.0/lib"
```

```
LIBRARY_PATH = "/usr/lib/llvm-6.0/lib"
```

```
PATH = "/usr/lib/llvm-6.0/bin:/home/iliid/Desktop:/usr/bin:/home/iliid/.local/bin:/usr/local/sbin:/usr/local/bin:/usr/sbin:/usr/bin:/sbin:/bin:/usr/games:/usr/local/games:/snap/bin:/usr/lib/jvm/java-8-oracle/bin:/usr/lib/jvm/java-8-oracle/db/bin:/usr/lib/jvm/java-8-oracle/jre/bin:/home/iliid/Downloads/pintos/src/util"
```

```
Sysinfo program /home/iiitd/Desktop/hw11/bin/sysinfo
Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on iiit-d Sat Apr 6 20:08:30 2019
```

SUT (~~System Under Test~~) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
```

```
model name : Intel(R) Core(TM) i5-7500 CPU @ 3.40GHz
```

```
1 "physical id"s (chips)
```

4 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 4
```

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Platform Notes (Continued)

siblings : 4
physical 0: cores 0 1 2 3

From lscpu:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 4
On-line CPU(s) list: 0-3
Thread(s) per core: 1
Core(s) per socket: 4
Socket(s): 1
NUMA node(s): 1
Vendor ID: GenuineIntel
CPU family: 6
Model: 158
Model name: Intel(R) Core(TM) i5-7500 CPU @ 3.40GHz
Stepping: 9
CPU MHz: 3605.024
CPU max MHz: 3800.0000
CPU min MHz: 800.0000
BogoMIPS: 6816.00
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 256K
L3 cache: 6144K
NUMA node0 CPU(s): 0-3

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3
sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer
aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb invpcid_single
pti ssbd ibrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust
bmi1 hle avx2 smep bmi2 erms invpcid rtm mpx rdseed adx smap clflushopt intel_pt
xsaveopt xsavec xgetbv1 xsaves dtherm ida arat pln pts hwp hwp_notify hwp_act_window
hwp_epp flush_lld

/proc/cpuinfo cache data
cache size : 6144 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

From /proc/meminfo
MemTotal: 8049856 kB

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Platform Notes (Continued)

HugePages_Total: 128
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Ubuntu 18.04.1 LTS

From /etc/*release* /etc/*version*
debian_version: buster/sid
os-release:

NAME="Ubuntu"
VERSION="18.04.1 LTS (Bionic Beaver)"
ID=ubuntu
ID_LIKE=debian
PRETTY_NAME="Ubuntu 18.04.1 LTS"
VERSION_ID="18.04"
HOME_URL="https://www.ubuntu.com/"
SUPPORT_URL="https://help.ubuntu.com/"

uname -a:
Linux iiit-d 4.15.0-46-generic #49-Ubuntu SMP Wed Feb 6 09:33:07 UTC 2019 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 (Meltdown): Mitigation: PTI
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full generic retpoline, IBPB, IBRS_FW

run-level 5 2019-04-06 10:17

SPEC is set to: /home/iiitd/Desktop/hw11

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	ext4	341G	143G	180G	45%	/

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

Compiler Version Notes

=====
CC 557.xz_r(base, peak)
=====

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Compiler Version Notes (Continued)

clang version 6.0.0-lubuntu2 (tags/RELEASE_600/final)
Target: x86_64-pc-linux-gnu
Thread model: posix
InstalledDir: /usr/lib/llvm-6.0/bin
Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/7
Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/7.3.0
Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/8
Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/8.0.1
Selected GCC installation: /usr/lib/gcc/x86_64-linux-gnu/7.3.0
Candidate multilib: .;@m64
Selected multilib: .;@m64

Base Unknown Flags

557.xz_r: "-pgARRAY(0x73bec78)"

Peak Unknown Flags

557.xz_r: "-pgARRAY(0x73bec78)"

Base Runtime Environment

C benchmarks:

557.xz_r: No flags used

Base Compiler Invocation

C benchmarks:

557.xz_r: clang

Base Portability Flags

557.xz_r: -DSPEC_LP64

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Hardware Availability:

Software Availability:

Base Optimization Flags

C benchmarks:

557.xz_r: -m64 -z muldefs -O3 -mavx

Base Other Flags

C benchmarks:

557.xz_r: -Wall

Peak Runtime Environment

Same as Base Runtime Environment

Peak Compiler Invocation

Same as Base Compiler Invocation

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

557.xz_r: -m64 -z muldefs -Ofast -mavx

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

557.xz_r: No flags used

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-04-06 20:08:29+0530.

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