#### SPEC® CPU2017 Integer Rate Result Copyright 2017-2019 Standard Performance Evaluation Corporation My Corporation SPECrate2017 int base 0.00SPECrate2017 int 0.00Test Date: CPU2017 License: nnn (Your SPEC license number) Apr-2019 Hardware Availability: **Test Sponsor:** My Corporation Tested by: Software Availability: My Corporation Copies 500.perlbench\_r 502.gcc\_r 505.mcf\_r 520.omnetpp\_r 523.xalancbmk\_r 525.x264\_r 531.deepsjeng\_r 541.leela\_r 548.exchange2\_r 557.xz\_r Hardware **Software** CPU Name: Intel Core i5-7500 Ubuntu 18.04.1 LTS OS: Max MHz.: 4.15.0-46-generic Nominal: Compiler: C/C++: Version 3.9.0 of Clang, the Enabled: LLVM Compiler Infrastructure cores, 1 chip, threads/core Fortran: Version 4.8.2 of GCC, the Orderable: 🗘 ache L1: **GNU** Compiler Collection L2: DragonEgg: Version 3.5.2, the L3: LLVM Compiler Infrastructure Other; Parallel: 7.677 GB fixme: If using DDR4, the format is: Memory Firmware: 'N GB (X x N GB nRxn PC4-nnnnX-X)' File System: ext4 Storage: 341.68 add more disk info here System State: Run level 5 (add definition here)

### **Errors**

Other:

Base Pointers: 64-bit

Peak Pointers: 64-bit

Complete set of valid runs for peak rate unavailable (525.x264\_r missing)

Complete set of valid runs for peak rate unavailable (531.deepsjeng\_r missing)

Complete set of valid runs for peak rate unavailable (502.gcc\_r missing)

#### (Continued on next page)

Other:

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SPECrate2017 int base = 9.00

SPECrate2017\_int\_peak

0.00

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Hardware Availability: Software Availability:

### **Errors** (Continued)

Complete set of valid runs for peak rate unavailable (557.xz\_r missing)

Complete set of valid runs for peak rate unavailable (505.mcf\_r missing)

Complete set of valid runs for peak rate unavailable (500.perlbench\_r missing)

Complete set of valid runs for peak rate unavailable (541.lectar missing)

Complete set of valid runs for peak rate unavailable (520.omnetpp\_r missing)

Complete set of valid runs for peak rate unavailable (548.exchange2\_x missing)

Complete set of valid runs for peak rate unavailable (523.xalancbmk\_r missing)

There is no set of valid runs with the same number of copies for base

'reportable' flag not set during run

525.x264\_r (base) did not have enough runs!

531.deepsjeng\_r (base) did not have enough runs!

502.gcc\_r (base) did not have enough runs!

557.xz\_r (base) did not have enough runs!

505.mcf\_r (base) did not have enough runs!

500.perlbench\_r (base) did not have enough runs!

541.leela\_r (base) did not have enough runs!

520.omnetpp\_r (base) did not have enough runs!

548.exchange2\_r (base) did not have enough runs

523.xalancbmk\_r (base) did not have enough runs!

500.perlbench\_r (base) had invalid runs!

525.x264\_r (peak) did pot have enough runs!

531.deepsjeng\_r (peak) did not have enough runs!

502.gcc\_r (peak) did not have enough runs!

557.xz\_r (peak) did not have enough runs!

505.mcf\_r (peak) did not have enough runs!

500.perlbench\_r (peak) did not have enough runs!

541.leelar (peak) did not have enough runs!

520.omnetpp\_r (peak) did not have enough runs!

548.exchange2\_r (peak) did not have enough runs!

528 xalancbmk\_r (peak) did not have enough runs!

500.perlbench\_r (peak) had invalid runs!

Input set must be refrate for a valid run (set to 'test' for this run)

Run of 500.perbench\_r (base) was not valid; status is RE

Run of 500. erlbench\_r (peak) was not valid; status is RE

Unknown flags were used! See

https://www.spec.org/cpu2017/Docs/runcpu.html#flagsurl

for information about how to get rid of this error.

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0.00

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Hardware Availability:

Software Availability:

#### Results Table

	Base							Peak						
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	1	0.000578	0.00					1	0.000576	0,00				
502.gcc_r														
505.mcf_r						$\langle \cdot \rangle$			)/					
520.omnetpp_r									<b>&gt;</b>					
523.xalancbmk_r														
525.x264_r						<i>  (</i> (		$\searrow$						
531.deepsjeng_r								\ \ \						
541.leela_r				)	_			$\mathcal{D}^{\sim}$						
548.exchange2_r			_											
557.xz_r							$\Big)$							

SPECrate2017\_int\_base =

0.00

SPECrate2017\_int\_peak = 0.00

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### **Submit Notes**

The config file option 'stopmit' was used.

#### **General Notes**

Environment variables set by runcpu before the start of the run: LD\_LIBRARY\_PATH = "/usr/lib/llvm-6.0/lib" LIBRARY\_PATH = "/usr/lib/llvm-6.0/lib"

#### **Platform Notes**

Sysinfo program /home/iiitd/Desktop/hwl1/bin/sysinfo Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9

running on iiit-d Sat Apr 6 20:00:41 2019

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Core(TM) i5-7500 CPU @ 3.40GHz

- 1 "physical id"s (chips)
- 4 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.) cpu cores: 4

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SPECrate2017 int

(),()()

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Apr-2019 Test Date: Hardware Availability: Software Availability:

#### Platform Notes (Continued)

```
siblings : 4
```

physical 0: cores 0 1 2 3

From lscpu:

Architecture: x86 64 CPU op-mode(s): 32-bit, 64-bit Byte Order: Little Endian

CPU(s): On-line CPU(s) list: 0-3 Thread(s) per core: Core(s) per socket:

Socket(s): NUMA node(s):

Vendor ID: GenuineIntel CPU family:

Model: 158

i5-7500 CPU @ 3.40GHz Model name: Intel(R) Core(TM)

Stepping: CPU MHz:

3601.457 CPU max MHz: 38/00,0000 CPU min MHz: 800.0000 BogoMIPS: 6816.0Ø

Virtualization: Lld cache:

Lli cache 32K L2 cache 256K 6144K L3 cache: 0-3 NUMA node0 CPU(s):

Flags fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 cM lush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp donstand tackart arch\_perfmon pebs bts rep\_good nopl xtopology nonstop\_tsc cpuid perfraperf tscknown\_freq pni pclmulqdq dtes64 monitor ds\_cpl vmx smx est tm2 ssse3 sdbg fma cx1 xtpr pdcm pcid sse4\_1 sse4\_2 x2apic movbe popcnt tsc\_deadline\_timer aes xsave avx f16c rdrand lahf\_lm abm 3dnowprefetch cpuid\_fault epb invpcid\_single pti ssbd ibrs ibpb stibp tpr\_shadow vnmi flexpriority ept vpid fsgsbase tsc\_adjust bmil hle avx2 smep bmi2 erms invpcid rtm mpx rdseed adx smap clflushopt intel\_pt xsaveopt xsavec xgetbv1 xsaves dtherm ida arat pln pts hwp hwp\_notify hwp\_act\_window hwp\_opp flush\_lld

/proc/cpuinfo cache data cache size : 6144 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

From /proc/meminfo

MemTotal: 8049856 kB

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0.00

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#### Platform Notes (Continued)

HugePages\_Total: 128
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d Ubuntu 18.04.1 LTS

From /etc/\*release\* /etc/\*version\*
 debian\_version: buster/sid
 os-release:

NAME="Ubuntu"

VERSION="18.04.1 LTS (Bionic Reaver)

ID=ubuntu

ID\_LIKE=debian

PRETTY\_NAME="Ubuntu 18.04.1 LTS"

VERSION\_ID="18.04"

HOME\_URL="https://www.ubuhtu.com/"

SUPPORT\_URL="https://help.ubuntu.com/

uname -a:

Linux iiit-d 4.15.0-46-generic #49 Ubuntu SMP Wed Feb 6 09:33:07 UTC 2019 x86\_64 x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2017-5754 Meltdown: Mitigation: PTI

CVE-2017-5753 (Spectre variant 1): Mitigation: \_user pointer sanitization

CVE-2017-5715 (Spectre variant 2): Mitigation: Full generic retpoline, IBPB, IBRS\_FW

run-leve 5 2019-04-06 10:17

SPEC set to: /home/iiitd/Desktop/hwll

Filtesystem Type Size Used Avail Use% Mounted on

/dev/sda4 /ext4 341G 143G 180G 45% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

### **Compiler Version Notes**

CC 500.perlbench\_r(base, peak)

(Continued on next page)

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Hardware Availability:
Software Availability:

### Compiler Version Notes (Continued)

clang version 6.0.0-lubuntu2 (tags/RELEASE\_600/final)

Target: x86\_64-pc-linux-gnu

Thread model: posix

InstalledDir: /usr/lib/llvm-6.0/bin

Found candidate GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/7
Found candidate GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/7.3.0
Found candidate GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/8
Found candidate GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/8.0.1
Selected GCC installation: /usr/lib/gcc/x86\_64-linux-gnu/7.3.0
Candidate multilib: .;@m64

Candidate multilib: .;@m64 Selected multilib: .;@m64

### Base Unknown Flags

500.perlbench\_r: "-pgARRAY(0x7c4ca88)

# Peak Unknown Flags

500.perlbench\_r: "-pgARRAY (0x7c4ca88)

### Base Runtime Environment

C benchmarks

500.perlbench\_r: No flags used

### **Base Compiler Invocation**

C benchmarks:

500.perlbench\_r: clang

### **Base Portability Flags**

500.perlbench\_r: -DSPEC\_LINUX\_X64 -DSPEC\_LP64

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0.00

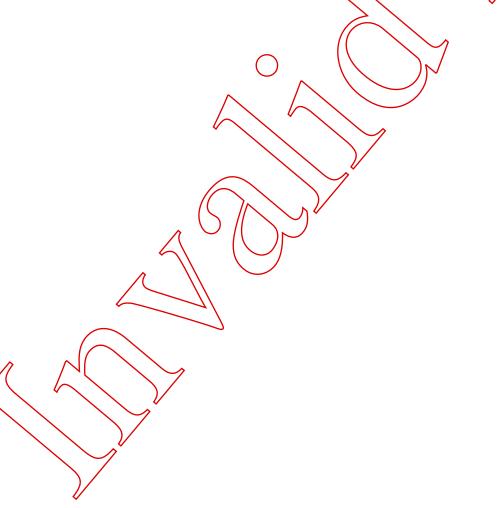
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Hardware Availability:
Software Availability:

### Peak Other Flags (Continued)

500.perlbench\_r: No flags used



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Tested with SPEC CPU2017 v1.0.5 on 2019-04-06 20:00:40+0530.

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