SPEC® CPU2017 Integer Rate Result Copyright 2017-2019 Standard Performance Evaluation Corporation My Corporation SPECrate2017 int base 0.00SPECrate2017 int 0.00Test Date: CPU2017 License: nnn (Your SPEC license number) Apr-2019 Hardware Availability: **Test Sponsor:** My Corporation Tested by: Software Availability: My Corporation Copies 500.perlbench_r 502.gcc_r 505.mcf_r 520.omnetpp_r 523.xalancbmk_r 525.x264_r 531.deepsjeng_r 541.leela_r 548.exchange2_r 557.xz_r Hardware **Software** CPU Name: Intel Core i5-7500 Ubuntu 18.04.1 LTS OS: Max MHz.: 4.15.0-46-generic Nominal: Compiler: C/C++: Version 3.9.0 of Clang, the Enabled: LLVM Compiler Infrastructure cores, 1 chip, threads/core Fortran: Version 4.8.2 of GCC, the Orderable: 🗘 ache L1: **GNU Compiler Collection** L2: DragonEgg: Version 3.5.2, the <u>L3</u>: LLVM Compiler Infrastructure Other; Parallel: 7.677 GB fixme: If using DDR4, the format is: Memory Firmware: 'N GB (X x N GB nRxn PC4-nnnnX-X)' File System: ext4 Storage: 341.68 add more disk info here System State: Run level 5 (add definition here)

Errors

Other:

Base Pointers: 64-bit

Peak Pointers: 64-bit

Complete set of valid runs for peak rate unavailable (523.xalancbmk_r missing) Complete set of valid runs for peak rate unavailable (520.omnetpp_r missing) Complete set of valid runs for peak rate unavailable (502.gcc_r missing)

(Continued on next page)

Other:

Copyright 2017-2019 Standard Performance Evaluation Corporation

My Corporation

SPECrate2017 int base = 9.00

SPECrate2017_int_peak

0.00

CPU2017 License: nnn (Your SPEC license number)

Test Sponsor: My Corporation **Tested by:** My Corporation

Test Date: Apr-2019

Hardware Availability: Software Availability:

Errors (Continued)

Complete set of valid runs for peak rate unavailable (525.x264 r missing)

Complete set of valid runs for peak rate unavailable (557.xz_r missing)

Complete set of valid runs for peak rate unavailable (541.legia_r missing)

Complete set of valid runs for peak rate unavailable (500.pertbench r missing)

Complete set of valid runs for peak rate unavailable (531.deepsjeng_r missing)

Complete set of valid runs for peak rate unavailable (505.mcf_rmissing)

Complete set of valid runs for peak rate unavailable (548.exchange2_r missing)

There is no set of valid runs with the same number of copies for base

'reportable' flag not set during run

523.xalancbmk_r (base) did not have enough runs!

520.omnetpp_r (base) did not have enough runs!

502.gcc_r (base) did not have enough runs!

525.x264_r (base) did not have enough runs!

557.xz_r (base) did not have enough runs!

541.leela_r (base) did not have enough runs!

500.perlbench_r (base) did not have enough runs!

531.deepsjeng_r (base) did not have enough runs!

505.mcf_r (base) did not have enough runs!

548.exchange2_r (base) did pot have enough runs

502.gcc_r (base) had invalid runs.

523.xalancbmk r (peak) did not have enough runs!

520.omnetpp_r (peak) did not have enough runs!

502.gcc_r (peak) did not have enough runs!

525.x264_r (peak) did not have enough runs!

557.xz_r (peak) did not have enough runs!

541.leela_r (peak) did not have enough runs!

(500.perlbench_r (peak) did not have enough runs!

581.deepsjeng r (peak) did not have enough runs!

505.mcf_r (peak) did not have enough runs!

548 exchange2_r (peak) did not have enough runs!

502.gcc_r (peak) had invalid runs!

Input set must be 'refrate' for a valid run (set to 'test' for this run)

Run of 502.gc/r (base) was not valid; status is RE

Run of 502.cc_r (peak) was not valid; status is RE

Results Table

	Base								Peak							
Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
500.perlbench_r																

Table continues on next page. Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Copyright 2017-2019 Standard Performance Evaluation Corporation

My Corporation

SPECrate2017 int base = 9.00

SPECrate2017_int_peak

0.00

CPU2017 License: nnn (Your SPEC license number)

Test Sponsor: My Corporation **Tested by:** My Corporation

Test Date: Apr-2019

Hardware Availability: Software Availability:

Results Table (Continued)

Base								Péak						
Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	
1	0.000555	0.00					1	0.000577	0,00					
					\wedge									
)/						
								4						
					/(\\ \S							
							\.\							
				_			71							
		_												
	Copies 1	_	_	Copies Seconds Ratio Seconds	Copies Seconds Ratio Seconds Ratio	Copies Seconds Ratio Seconds Ratio Seconds	Copies Seconds Ratio Seconds Ratio Seconds Ratio	Copies Seconds Ratio Seconds Ratio Copies	Copies Seconds Ratio Seconds Ratio Copies Seconds	Copies Seconds Ratio Seconds Ratio Seconds Ratio Copies Seconds Ratio	Copies Seconds Ratio Seconds Ratio Seconds Ratio Copies Seconds Ratio Seconds	Copies Seconds Ratio Seconds Ratio Seconds Ratio Copies Seconds Ratio Seconds Ratio	Copies Seconds Ratio Seconds Ratio Seconds Ratio Copies Seconds Ratio Seconds Seconds	

SPECrate2017_int_base = 0.00

SPECrate2017_int_peak = 0.00

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Environment variables set by runou before the start of the run: LD_LIBRARY_PATH / "/usn lib/llvm-6.0/lib"

LIBRARY_PATH = "/usr/lib/llvm-6.0/lib"

Platform Notes

Sysinfo program /home/iiitd/Desktop/hw11/bin/sysinfo

Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9

running on Niit d Sat Apr 6 05:18:25 2019

SUT (System under Test) info as seen by some common utilities.

For more information on this section, see

https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : Intel(R) Core(TM) i5-7500 CPU @ 3.40GHz

- 1 "physical id"s (chips)
- 4 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following

excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 4
siblings : 4

(Continued on next page)

Copyright 2017-2019 Standard Performance Evaluation Corporation

My Corporation

SPECrate2017 int base = 0.00

SPECrate2017 int

(),()()

CPU2017 License: nnn (Your SPEC license number)

Test Sponsor: My Corporation **Tested by:** My Corporation

Apr-2019 Test Date: Hardware Availability: Software Availability:

Platform Notes (Continued)

physical 0: cores 0 1 2 3

From lscpu:

Architecture: x86_64 32-bit, 64-bit CPU op-mode(s): Byte Order: Little Endian

CPU(s): On-line CPU(s) list: 0-3 Thread(s) per core: Core(s) per socket: Socket(s):

NUMA node(s): Vendor ID:

GenuineIntel CPU family:

Model: 158

Core(TM) 15-7500 CPU @ 3.40GHz Model name:

Stepping:

CPU MHz: 3605.813 CPU max MHz: 3800.0000 CPU min MHz: 800.000 6816.00

BogoMIPS: Virtualization

Lld cache: Lli cache: 32K L2 cache: 256K L3 cache 6144K

NUMA node0 CPU(s): 0 - 3

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat/pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid penfmperf tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sobg kma cxl xtpr pdcm pcid sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb invpcid_single pti sand lbrs ibpb stibp tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust kmil hle avx2 smep bmi2 erms invpcid rtm mpx rdseed adx smap clflushopt intel_pt xsaveopt xsavec xgetbv1 xsaves dtherm ida arat pln pts hwp hwp_notify hwp_act_window hwp_epp flush_11d

/proc/cprinfo cache data cache size : 6144 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

From /proc/meminfo

8049856 kB MemTotal: HugePages_Total: 128

(Continued on next page)

Copyright 2017-2019 Standard Performance Evaluation Corporation

My Corporation

SPECrate2017 int_base = 0.00

SPECrate2017 int

(),()()

CPU2017 License: nnn (Your SPEC license number)

Test Sponsor: My Corporation **Tested by:** My Corporation

Apr-2019 Test Date: Hardware Availability: Software Availability:

Platform Notes (Continued)

```
Hugepagesize:
                       2048 kB
/usr/bin/lsb_release -d
  Ubuntu 18.04.1 LTS
From /etc/*release* /etc/*version*
  debian_version: buster/sid
   os-release:
     NAME="Ubuntu"
      VERSION="18.04.1 LTS (Bion)c Beaver
      ID LIKE=debian
      PRETTY_NAME="Ubuntu/18.04.1
      VERSION_ID="18.04" //
      HOME_URL="https://www.ubuntu.com/
      SUPPORT_URL="https://help.ubuntu.com
uname -a:
  Linux iiit-d 4.15.0-46-generic #49 Wountu SMP Wed Feb 6 09:33:07 UTC 2019 x86_64
  x86_64 x86_64 GNU/Linux
Kernel self-reported vulnerabilit
                                    status:
CVE-2017-5754 (Meltdown):
                                   Mitigation: PTI
```

CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization

CVE-2017-5715 (Spectre variant 2): Mitigation: Full generic retpoline, IBPB, IBRS_FW

run-level 5 2019-04-05 19 29

SPEC is set to: Xhome/iiitd/Desktop/hw11 Filesystem

Type Size Used Avail Use% Mounted on

/dev/sda4 ext4 341G 141G 183G 44% /

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

(End of data from sysinfo program)

Compiler Version Notes

______ CC 502.gcc_r(base, peak) clang version 6.0.0-lubuntu2 (tags/RELEASE 600/final)

(Continued on next page)

Copyright 2017-2019 Standard Performance Evaluation Corporation

My Corporation

SPECrate2017 int base = 9.00

SPECrate2017_int_peak

0.00

CPU2017 License: nnn (Your SPEC license number)

Test Sponsor: My Corporation **Tested by:** My Corporation

Test Date: Apr-2019
Hardware Availability:
Software Availability:

Compiler Version Notes (Continued)

Target: x86_64-pc-linux-gnu

Thread model: posix

InstalledDir: /usr/lib/llvm-6.0/bin

Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/7 Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gru/7.3.0 Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/8 Found candidate GCC installation: /usr/lib/gcc/x86_64-linux-gnu/8.0.1 Selected GCC installation: /usr/lib/gcc/x86_64-linux-gnu/7.3.0

Candidate multilib: .;@m64 Selected multilib: .;@m64

Runtime Environment

C benchmarks:

502.gcc_r: No flags used

Compiler Invocation

C benchmarks:

502.gcc_r: clang

Portability Flags

502.gcc_r: -DSPEC_LP64

Base Optimization Flags

C benchmark

502.gcc_r: -m64 -z muldefs -O3 -mavx

Copyright 2017-2019 Standard Performance Evaluation Corporation

My Corporation

SPECrate2017 int_base = 0.00

SPECrate2017_int_peak

0.00

CPU2017 License: nnn (Your SPEC license number)

Test Sponsor: My Corporation **Tested by:** My Corporation

Test Date: Apr-2019
Hardware Availability:

Software Availability:

Runtime Environment

C benchmarks:

502.gcc_r: No flags used

Peak Optimization Flags

C benchmarks:

502.gcc_r: -m64 -z muldefs -Ofast -mavx

Other Flags

C benchmarks:

502.gcc_r: No flags used

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-04-06 05:18:24+0530.

Report generated on 2019-04-06 05:18:33 by CPU2017 PDF formatter v5866.