

# Arslan Chaudhry

<b>Contact Information</b>	Flat 164, Block D, Castle Mill, Roger Dudman Way, Oxford OX1 1GA, United Kingdom	(44)7491-875617 arslan.chaudhry@new.ox.ac.uk <a href="http://www.robots.ox.ac.uk/~arslan/">http://www.robots.ox.ac.uk/~arslan/</a>
<b>Education</b>	<b>University of Oxford</b> , United Kingdom <i>Doctor of Philosophy (DPhil) in Machine Learning and Computer Vision</i> <ul style="list-style-type: none"><li>• Advisor: Philip H.S. Torr</li><li>• Focus: Machine learning models for continual/ lifelong learning and modular representations in deep networks.</li><li>• <b>Rhodes Scholar (2016)</b></li></ul> <b>University of Engineering &amp; Technology</b> , Lahore, Pakistan <i>Bachelor of Science in Electrical Engineering</i> <ul style="list-style-type: none"><li>• CGPA: 3.95/4.0 (Highest honors)</li><li>• Multiple gold medals for graduating at the top of 400+ students graduating class</li><li>• Thesis: Load balancing of compute intensive applications on Beowulf clusters</li></ul>	<b>Oct 2016 - Present</b>          <b>August 2013</b>
<b>Professional Experience</b>	<b>Visiting Researcher</b> , Facebook AI Research (FAIR) <ul style="list-style-type: none"><li>• <i>Mentors</i>: Marc'aurelio Ranzato, Marcus Rohrbach, Mohamed Elhoseiny,</li><li>• <i>Project</i>: Efficient Lifelong Learning.</li></ul> <b>Sr. Software Development Engineer</b> , Mentor Graphics Developed a Virtual Machine Monitor (VMM)/ Hypervisor for ARM and Intel platforms. Lead the development of: <ul style="list-style-type: none"><li>• Secured (using TrustZone – ARM) and virtualized (VT-x/ VT-d – Intel) Memory Management Unit (MMU)</li><li>• Networking and console drivers based on virt-IO</li><li>• Profiling and visualization toolkit</li><li>• Build/ configure and packaging system</li><li>• → Received multiple performance excellence awards</li></ul>	<b>June 2018 - February 2019</b>          <b>June 2013 - Aug 2016</b>
<b>Publications</b>	<ol style="list-style-type: none"><li>1. <b>Arslan Chaudhry</b>, Marcus Rohrbach, Mohamed Elhoseiny, Thalaiyasingam Ajanthan, Puneet K. Dokania, Philip H.S. Torr, Marc'Aurelio Ranzato; <i>Continual Learning with Tiny Episodic Memories</i>, International Joint Conference on Artificial Intelligence (IJCAI), 2019. Under submission</li><li>2. <b>Arslan Chaudhry</b>, MarcAurelio Ranzato, Marcus Rohrbach, Mohamed Elhoseiny; <i>Efficient Lifelong Learning with A-GEM</i>, International Conference on Learning Representations (ICLR), 2019.</li><li>3. <b>Arslan Chaudhry</b>, Puneet K. Dokania, Thalaiyasingam Ajanthan, Philip Torr; <i>Riemannian Walk for Incremental Learning: Understanding Forgetting and Intertransigence</i>, In the Proceedings of the European Conference on Computer Vision (ECCV), 2018.</li><li>4. <b>Arslan Chaudhry</b>, Puneet K. Dokania, Philip Torr; <i>Discovering Class-Specific Pixels for Weakly-Supervised Semantic Segmentation</i>, In the Proceedings of the British Machine Vision Conference (BMVC), 2017. <b>(oral)</b></li></ol>	

<b>Teaching</b>	Undergraduate Tutorials, <b>Machine Learning</b> , Trinity 2018, Stanford House Graduate Teaching Assistant, <b>Networking</b> , Trinity 2018, University of Oxford Graduate Teaching Assistant, <b>Operating Systems</b> , Hilary 2018, University of Oxford Lab Demonstrator, <b>Software Engineering</b> , Hilary 2018, University of Oxford Lab Demonstrator, <b>Software Engineering</b> , Hilary 2017, University of Oxford Teaching Assistant, <b>Operating Systems</b> , Spring 2013, UET, Lahore	
<b>Computing Expertise</b>	<u>Languages:</u>	C, C++, Python, numpy, Shell Scripting, SQL, PHP, Eclipse P2 Development, Verilog (HDL), Assembly language (ARM, x86, MIPS and 80c51), GNU/Linux Programming, MATLAB, L <sup>A</sup> T <sub>E</sub> X
	<u>Programming Paradigms:</u>	CUDA Programming, System Programming, Linux Kernel and Driver Development, Parallel Programming, Application Level Programming, Agile Sprint Development
	<u>Machine Learning Frameworks:</u>	Tensorflow, Pytorch, Keras
	<u>Advanced MPS Modules:</u>	ARM TrustZone, Intel's VT-d and VT-x
<b>Leadership Activities</b>	<b>President</b> , IEEE UET Lahore	<b>June 2012 - June 2013</b> As president, I was responsible for the overall performance of the chapter. Presided all technical, budget and alumni committee meetings. Represented the society at the <i>Pakistan Student Congress 2012</i> . The chapter won the <i>Best Student Branch of Lahore Section</i> award during my tenure.
<b>Awards Grants Fellowships</b>	Murray Speight Grant, Rhodes House, <b>2018</b> Rhodes Scholar, <b>2016</b> Performance Excellence Award, Virtualization Team, Mentor Graphics ESD, <b>2015</b> Topper and Gold medalist of Electrical Engineering Department, UET Lahore, <b>2013</b> Deans Honors List, UET Lahore, <b>2009-2013</b> Nominated for IEEE Region 10 executive comity by IEEE Lahore Section, <b>2013</b> Obtained third position in BLOSSOM held by MIT officials at PYF, <b>2012</b> Winner of Race to Innovation in Pakistan Student Congress, <b>2012</b> Obtained third position in Pakistan in IEEE Xtreme Programming Competition, <b>2012</b> PCS scholarship recipient worth PKR 113980, <b>2007-2009</b>	
<b>Service</b>	Reviewer for ICML [2019-], NeurIPS [2019-], CVPR [2019-], ICCV [2019-] Reviewer for IEEE Transaction on Multimedia	
<b>Referees</b>	<b>Philip Torr</b> Professor University of Oxford, United Kingdom email: philip.torr@eng.ox.ac.uk	<b>Marc'Aurelio Ranzato</b> Research Scientist Facebook AI Research email: ranzato@fb.com