

Ramkumar Ramachandra

Compiler engineer with over a decade of experience, and a track record in open source work, specializing in LLVM middle-end optimizations. Hobby research includes constructing mathematical objects using a proof assistant.

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WORK

Jun '24 – Present	Compiler Lead Upstream-first LLVM optimization for RISC-V: landed 350 patches spanning most of the middle-end. Authored a new analysis, HashRecognize, and enabled optimization of CRC loops, with impact on several real-world programs including Linux. Significantly improved vectorization, notably authoring the CSE and constant-folder in VPlan. Drove the <i>icmp samesign</i> optimization effort, authoring the foundational structure.	Codasip, Bristol, England
Mar '23 – Feb '24	Senior Compiler Engineer LLVM optimization for a RISC-V VPU. Improved LoopStrengthReduce, LoopAccessAnalysis, and LoopVectorize, with impact on embedded benchmarks, with some patches upstreamed. Introduced vector variants of the [I]lrint intrinsics with custom lowering for RISC-V. Downstream work included improving the benchmarking infrastructure, and scheduling support for the chip.	Imagination Technologies, Kings Langley, England
Aug '19 – Jan '23	Career break Career break to pursue interest in formalized mathematics. Audited courses in algebraic topology, and worked as an apprentice learning Rocq and type theory. Found a long-term collaborator and began a line of research.	Inria, IRIF, and Université Paris Cité
Feb '15 – Aug '17	Compiler Engineer Fixed the longest-standing bugs in the organization by carefully combing through x86 assembly. Worked with a pre-SSA IR to author and ship a LICM, and incremental-update algorithms for the program structure tree and dominator. Contributed to the in-house alias analysis. Implemented polyhedral loop optimizations using the <i>Integer Set Library</i> .	MathWorks, Natick, Massachusetts

TALKS AND PUBLICATIONS

Jun '25	A parametricity-based formalization of Set_{Δ_+} and Set_\square with Hugo Herbelin <i>Mathematical Structures in Computer Science</i> Published: 10.1017/S096012952500009X. Pre-print: arXiv:2401.00512.
Apr '25	Making LoopAccessAnalysis more precise <i>EuroLLVM, Berlin</i> Program: LLVM Developers' Meeting. Recorded video: YouTube.

EDUCATION AND OPEN SOURCE

2014	Masters in Computer Engineering	Columbia University, New York
2012	Integrated Masters in Physics	Indian Institute of Technology, Kharagpur
2013 – 2014	Linux Landed 60 patches focused on improving perf tools.	
2010 – 2014	Git Landed 200 patches. Participated in Summer of Code 2010 and 2011, authoring the sequencer, enabling <i>git cherry-pick</i> to resume after conflicts. Designed and implemented <i>rebase.autostash</i> and <i>remote.pushdefault</i> .	
Summer '10	Subversion Authored svndump, a tool to import and export history from a remote svn server.	