

Fixed pins: SDI (PB3), CLK (PB5) Remappable pins: $\overline{\text{OE}}$ (PC3), LE (PC5) Restricted pins (see below): PB2, PB4

Pins marked "remappable" in the above list may be remapped to unused pins, or swapped with other remappable pins in the Makefile configuration.

Due to hardware limitations when using this SPI mode, PB2 must always remain an output pin, and PB4 must always remain an input pin, but both may be used for other purposes as long as those conditions are met.

R1 is optional, but protects against accidental resets due to noise.

D1 is optional, but provides ESD protection for the $\overline{\text{RESET}}$ pin.

C2 is optional, but provides additional noise protection for the RESET pin. (If debugWire or PDI is being used, C2 cannot be present.)

Q1, C3, and C4 are optional if the fuse bits are changed appropriately to use the internal oscillator.