



Fixed pins: SDI (PD1), CLK (PD4)  
Remappable pins: OE (PC3), LE (PC5)

Pins marked "remappable" in the above list may be remapped to unused pins, or swapped with other remappable pins in the Makefile configuration.

R1 is optional, but protects against accidental resets due to noise.

D1 is optional, but provides ESD protection for the RESET pin.

C2 is optional, but provides additional noise protection for the RESET pin. (If debugWire or PDI is being used, C2 cannot be present.)

Q1, C3, and C4 are optional if the fuse bits are changed appropriately to use the internal oscillator.