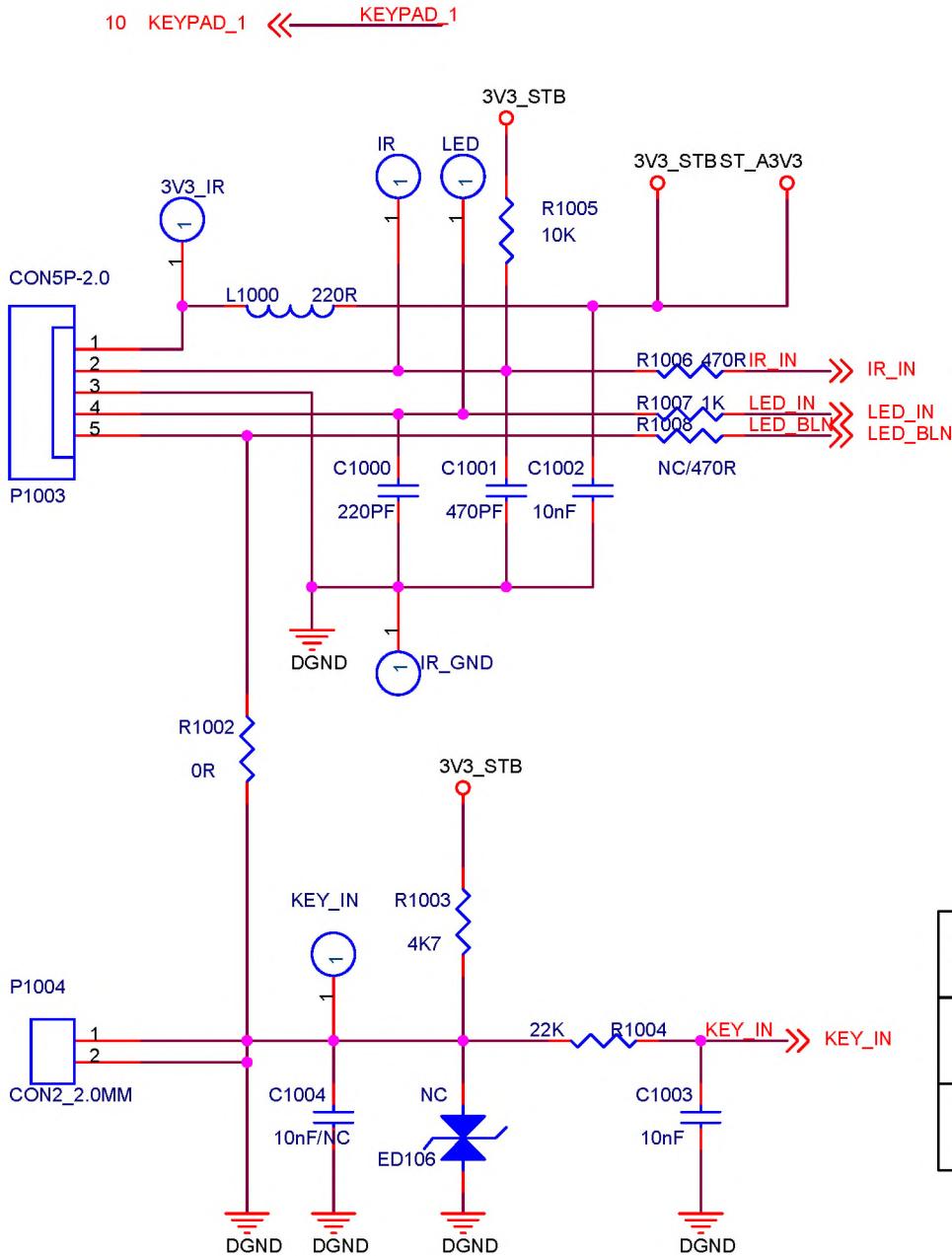


Title <Title>		Rev <RevCode>
Size B	Document Number BACKLIGHT	Rev <RevCode>
RP830, RP837	RP830, RP837	

Date: Sunday, January 20, 2019 Sheet 1 of 1

From Main Chip



Note:	P1003	P1004	R1002	R1008	LED_BLN
Key&IR 2IN1	ON	NC	ON	NC	N.A
Separate	ON	ON	NC	NC	N.A
	ON	ON	NC	ON	Separate

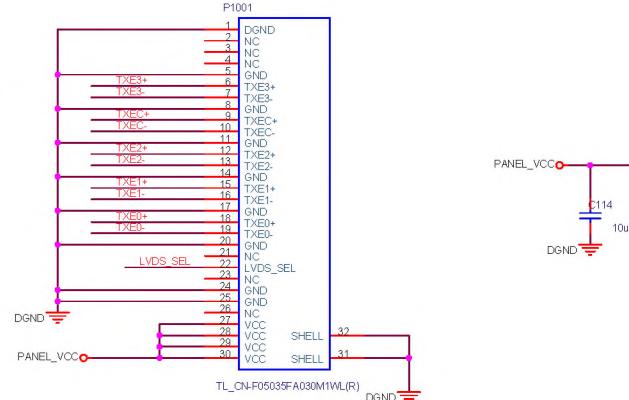
Title RTD2841PFG LVDS

Size A Schematic Name Key/LED/IrDA

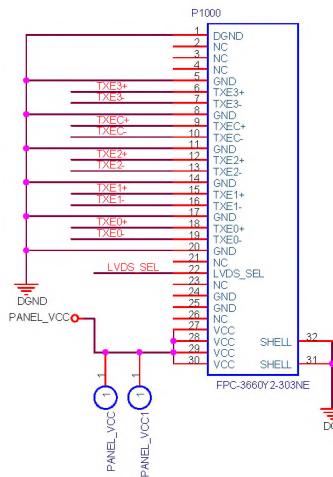
Rev T1

IC->LVDS

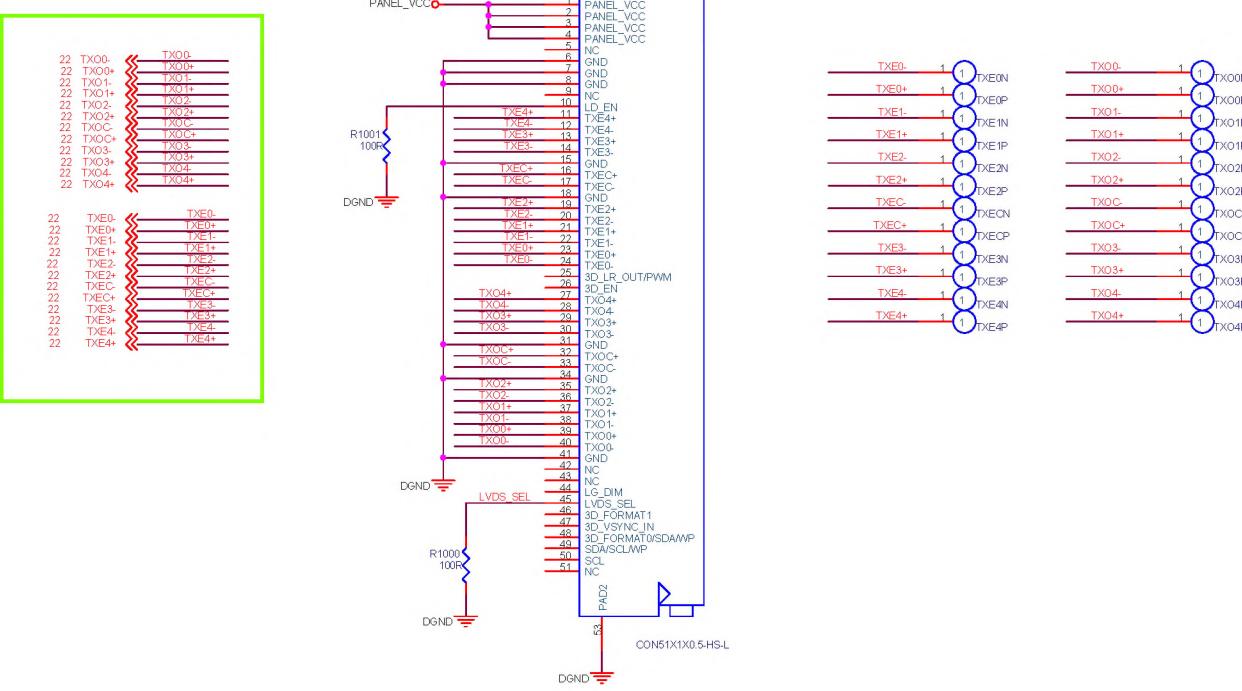
0.5MM_HD 30P FFC

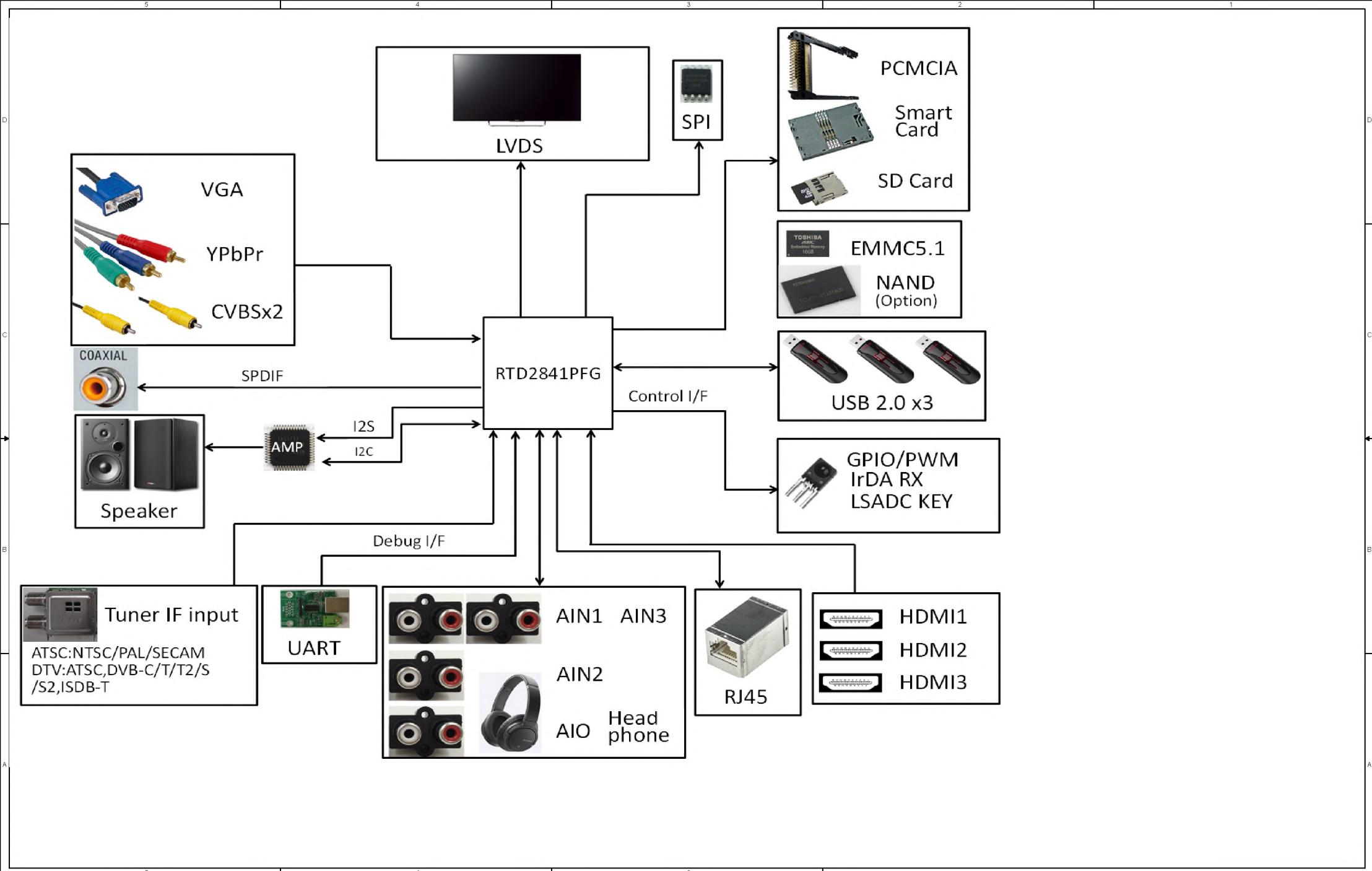


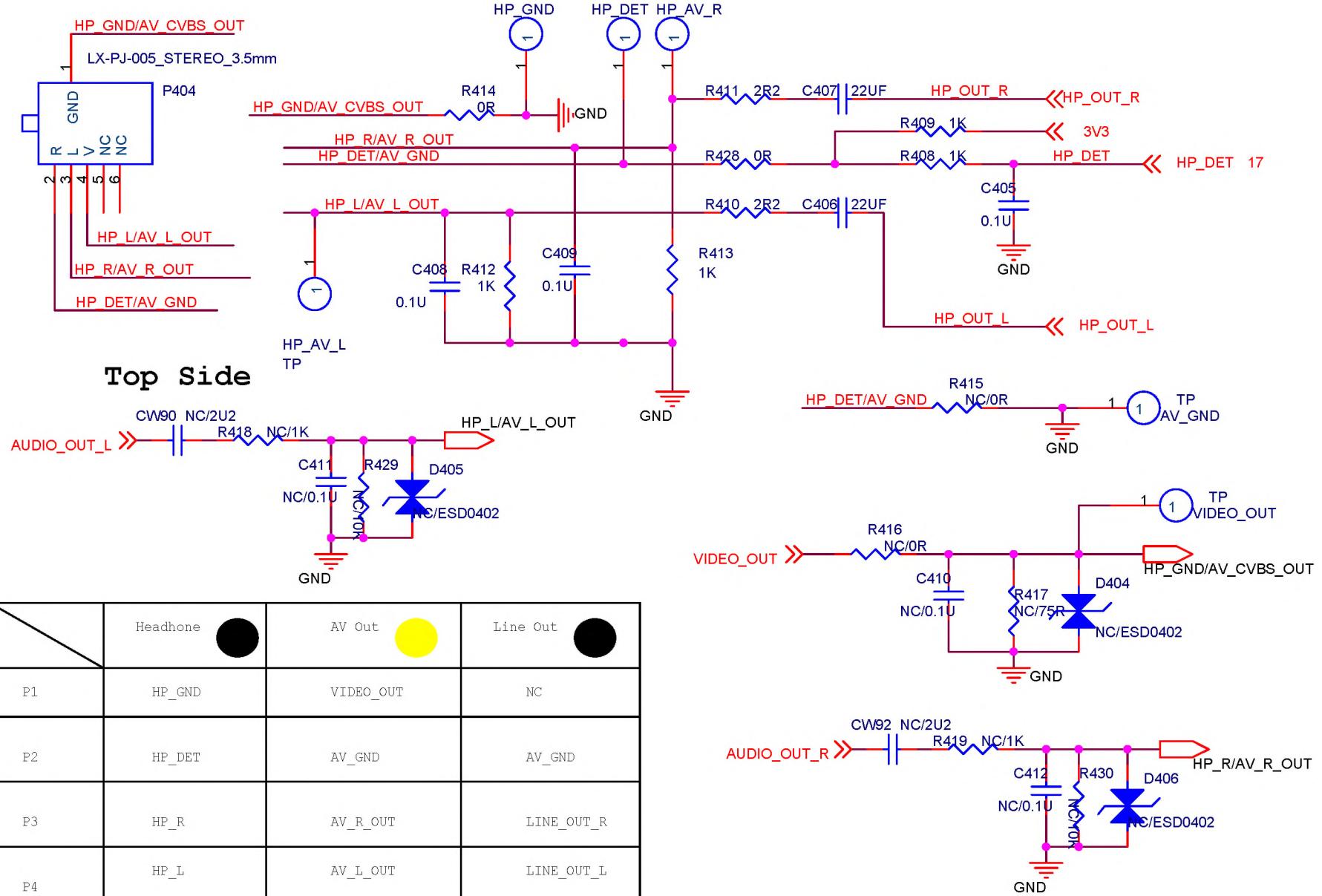
1.0MM_HD 30P FFC



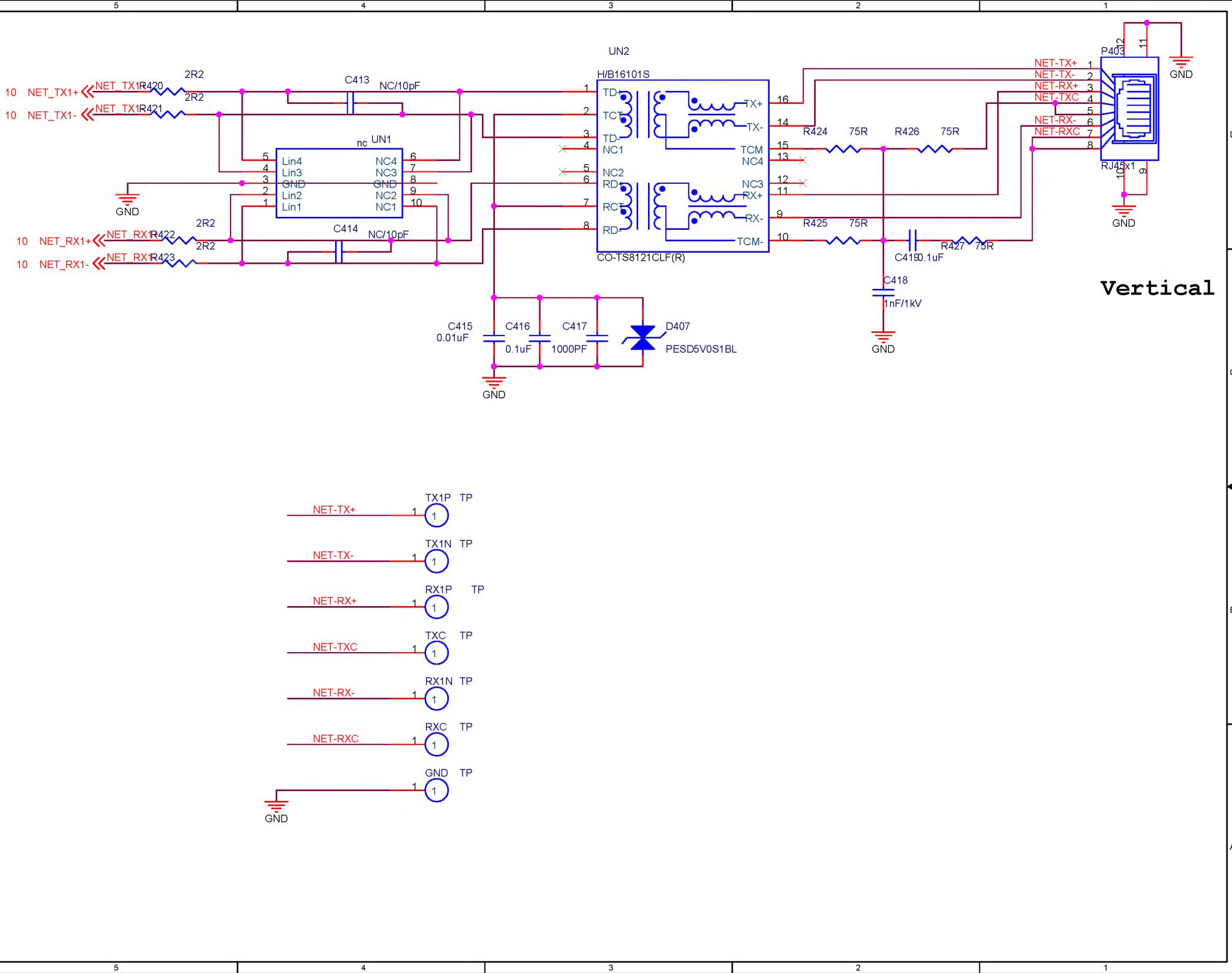
0.5MM_FHD 51P

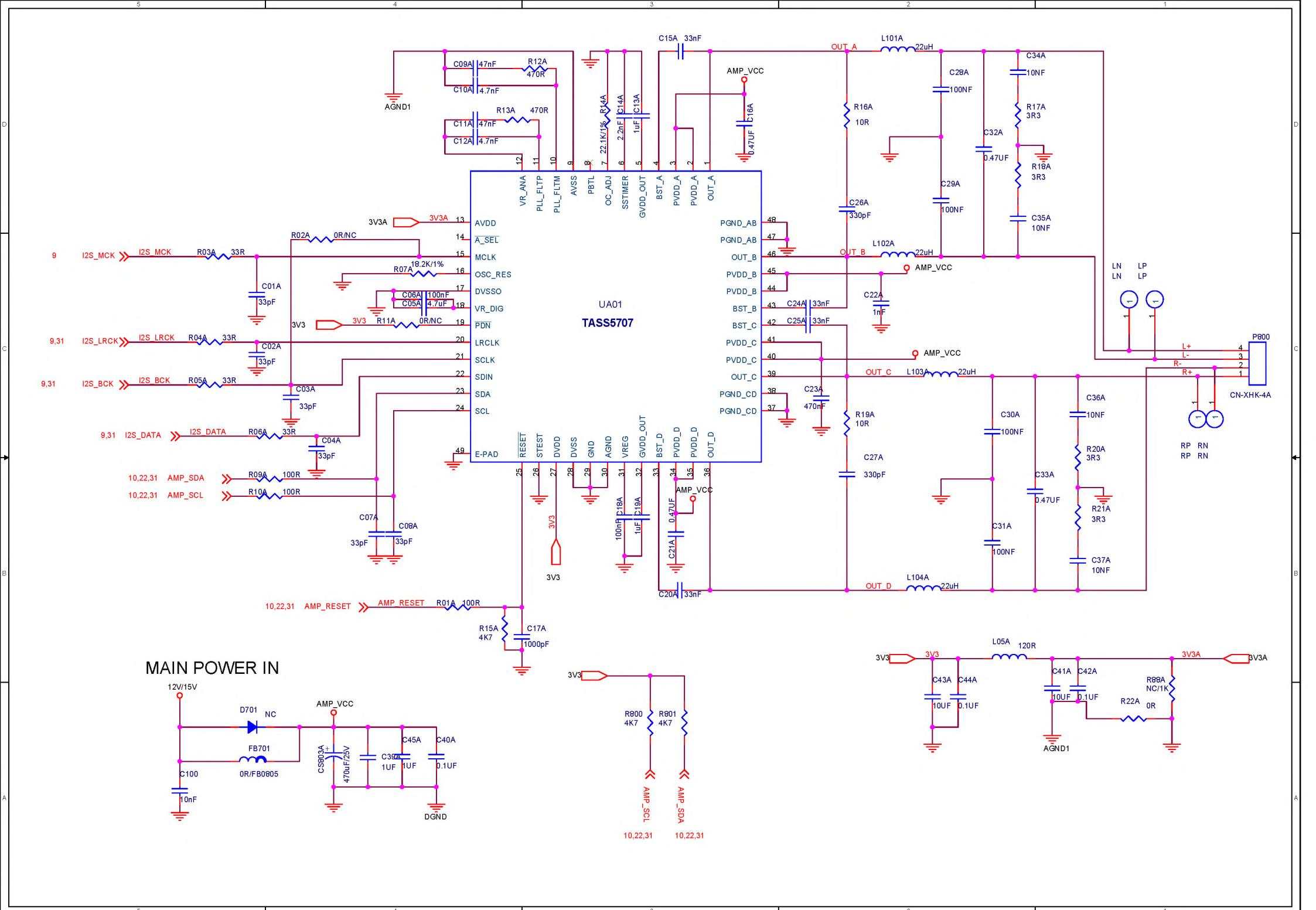


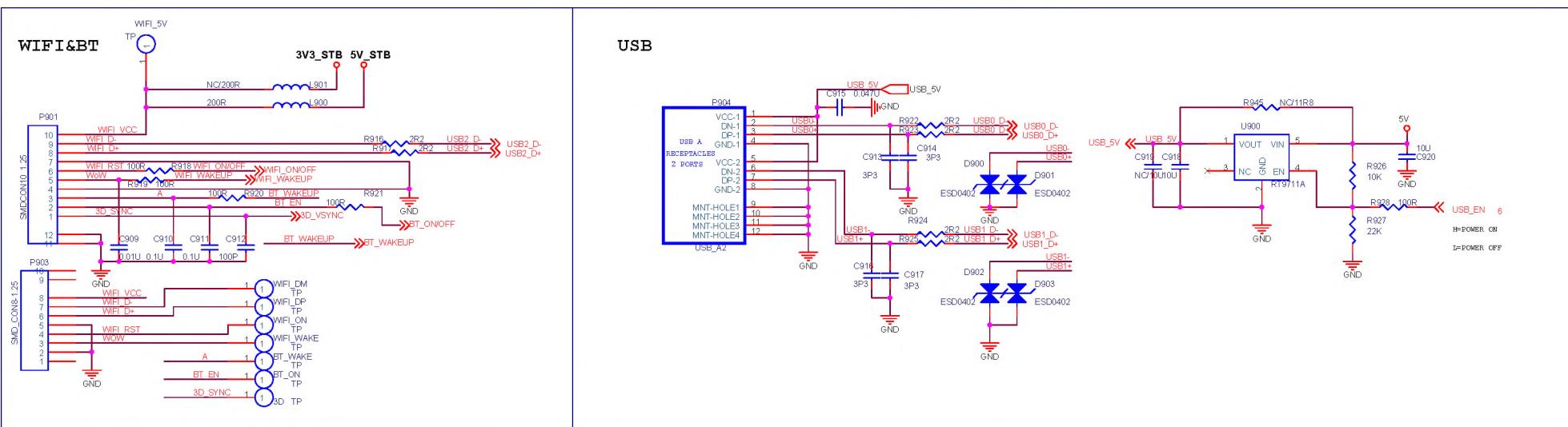
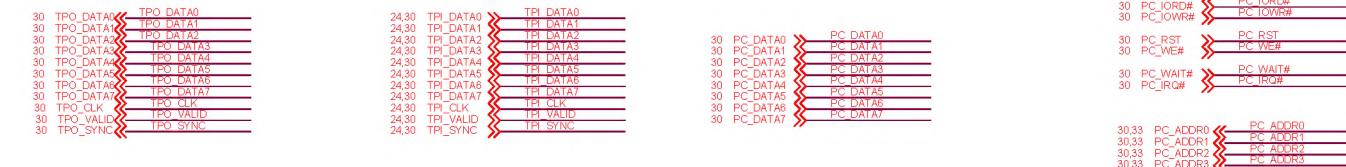
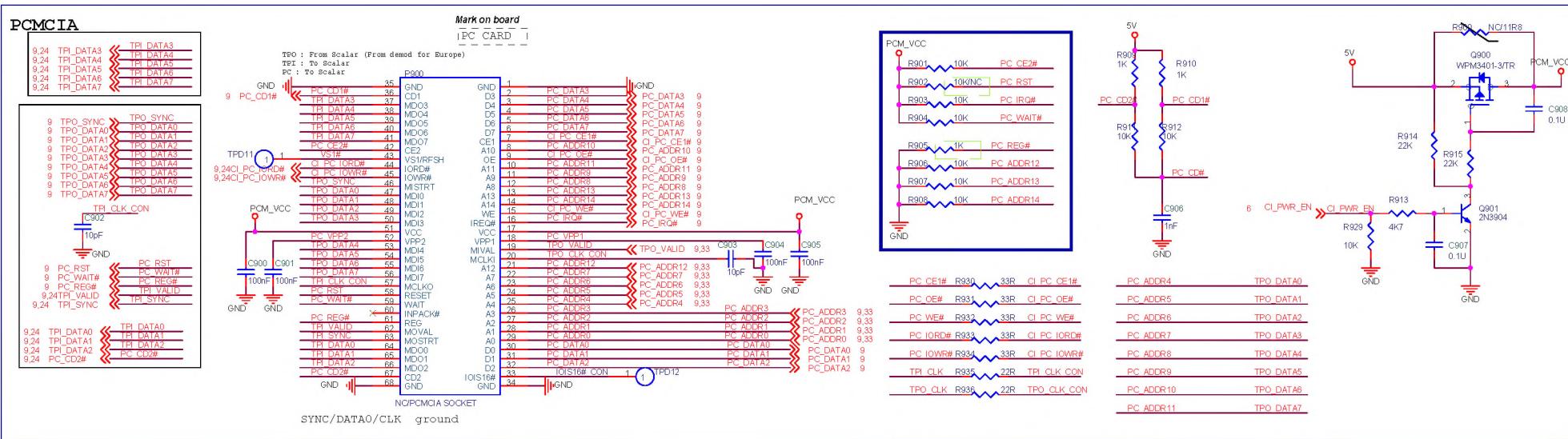


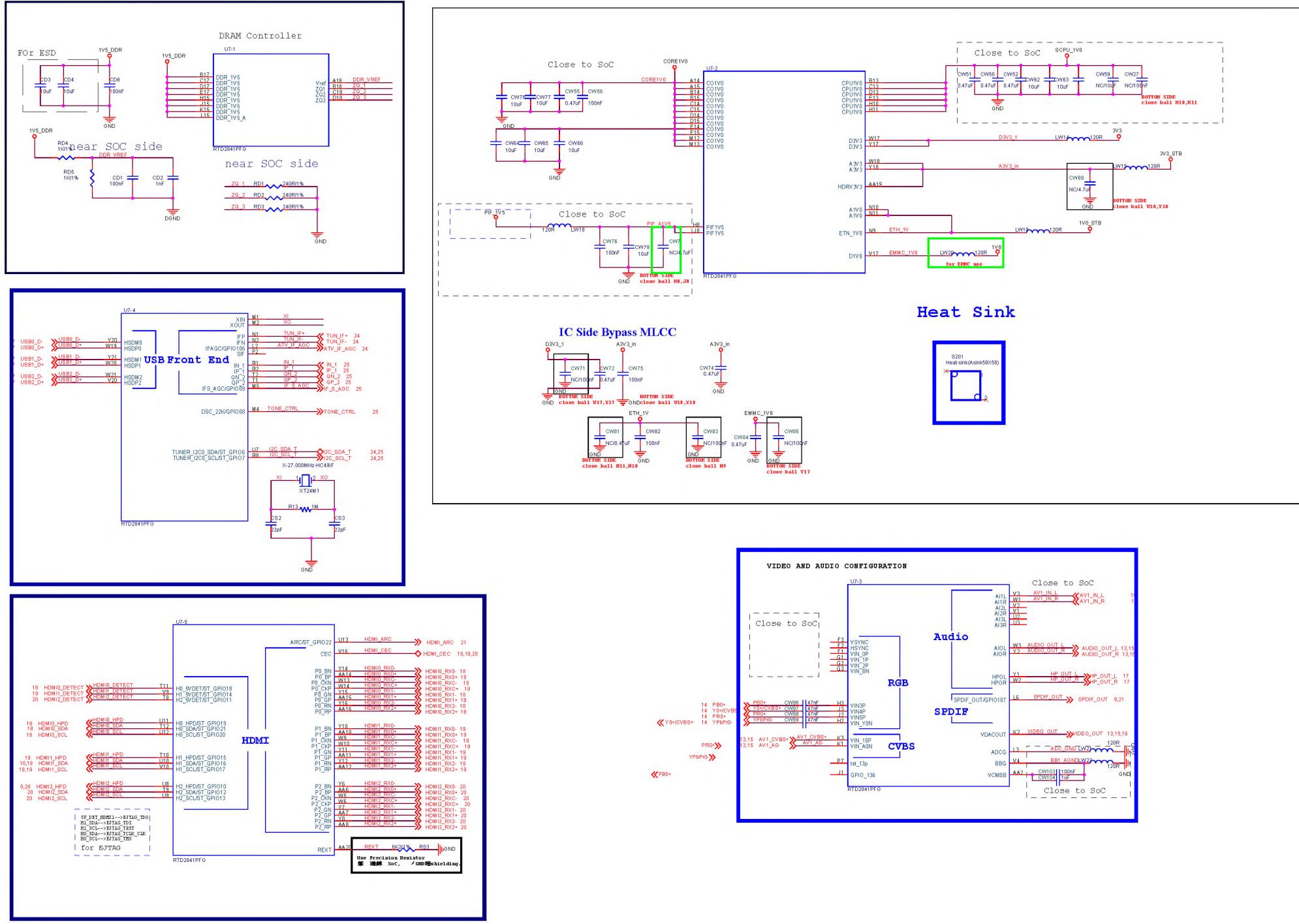


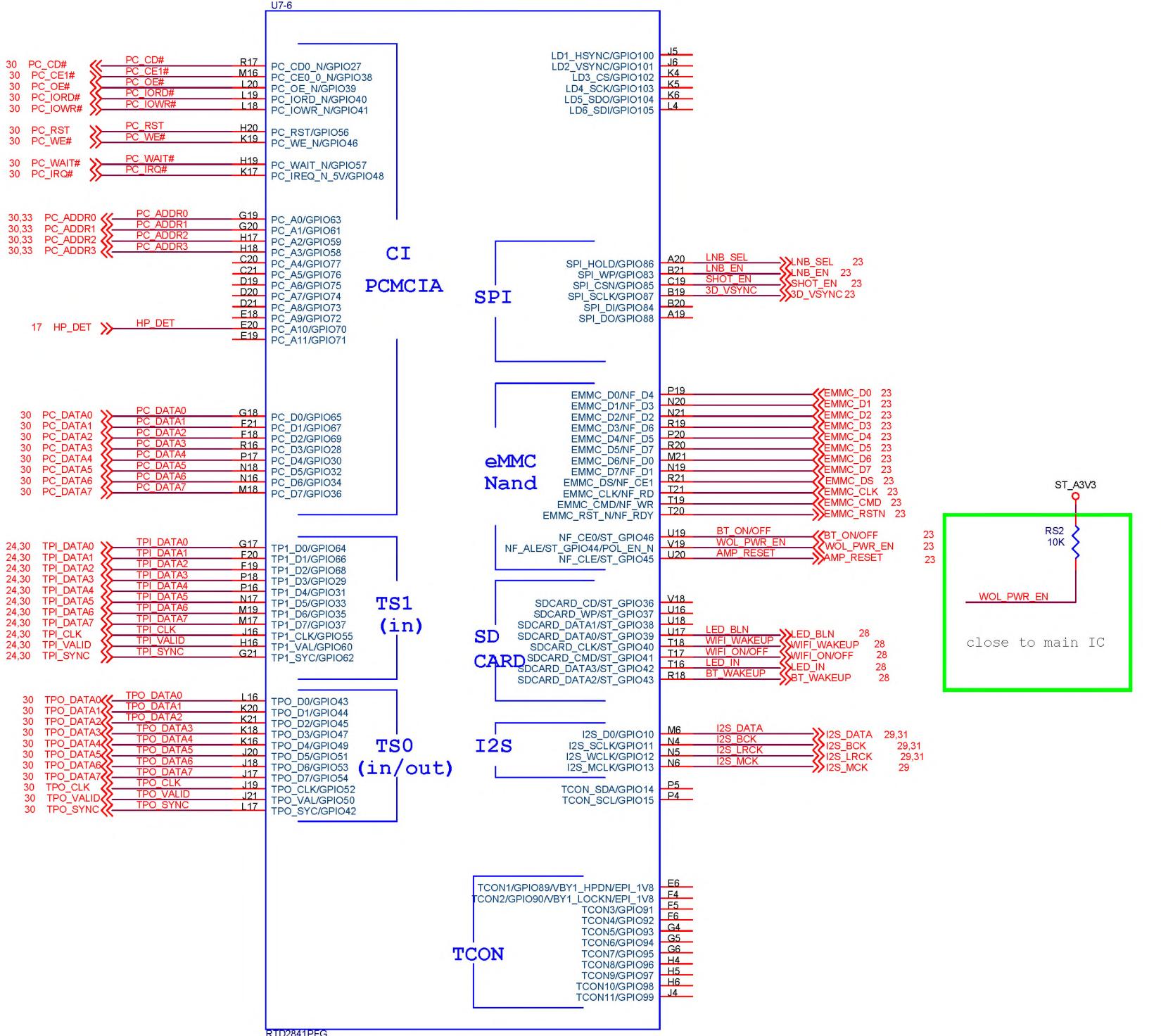
	Headphone	AV Out	Line Out
P1	HP_GND	VIDEO_OUT	NC
P2	HP_DET	AV_GND	AV_GND
P3	HP_R	AV_R_OUT	LINE_OUT_R
P4	HP_L	AV_L_OUT	LINE_OUT_L
R414	0R	NC	NC
R415	NC	0R	0R
R428	0R	NC	NC

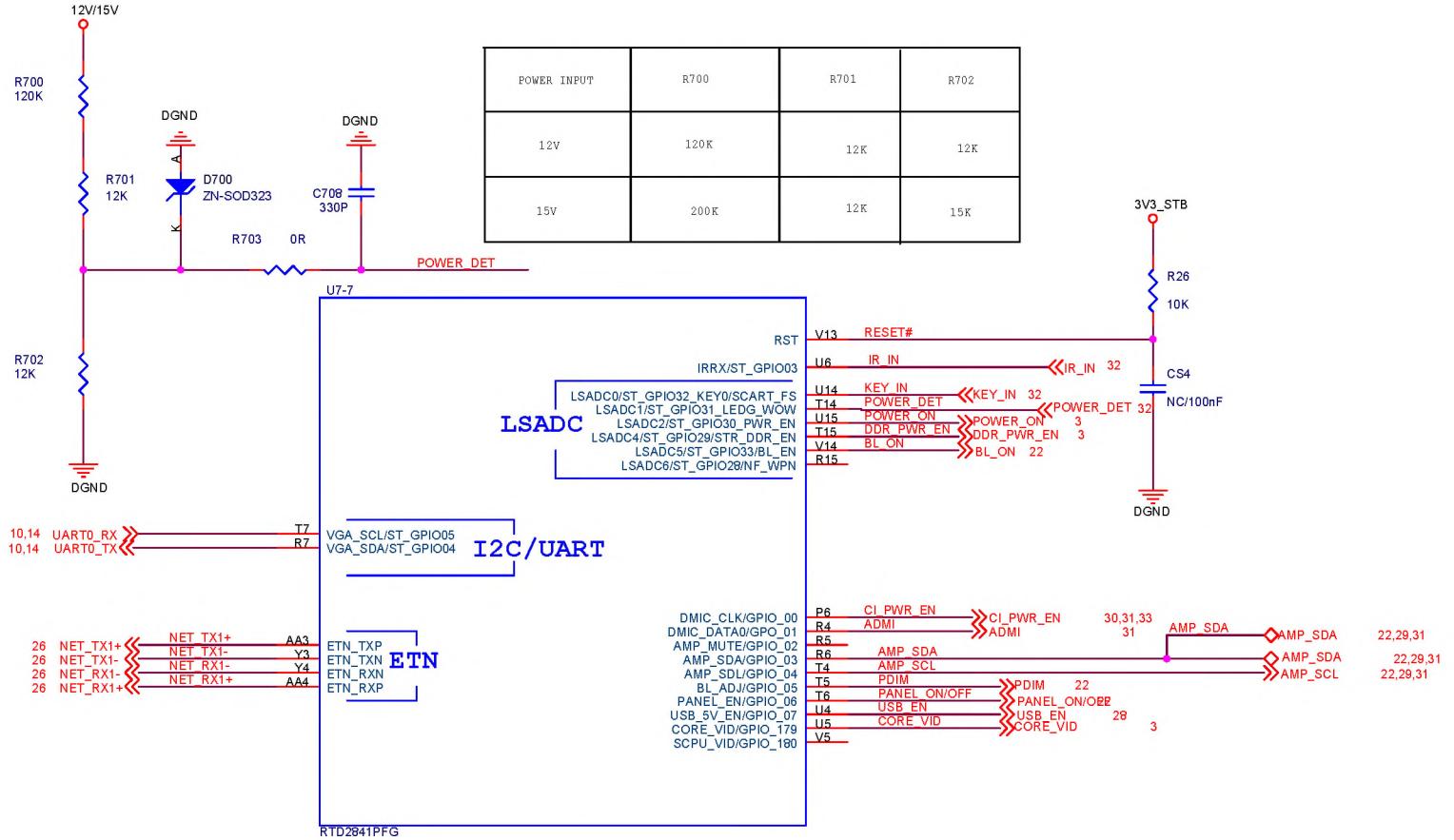


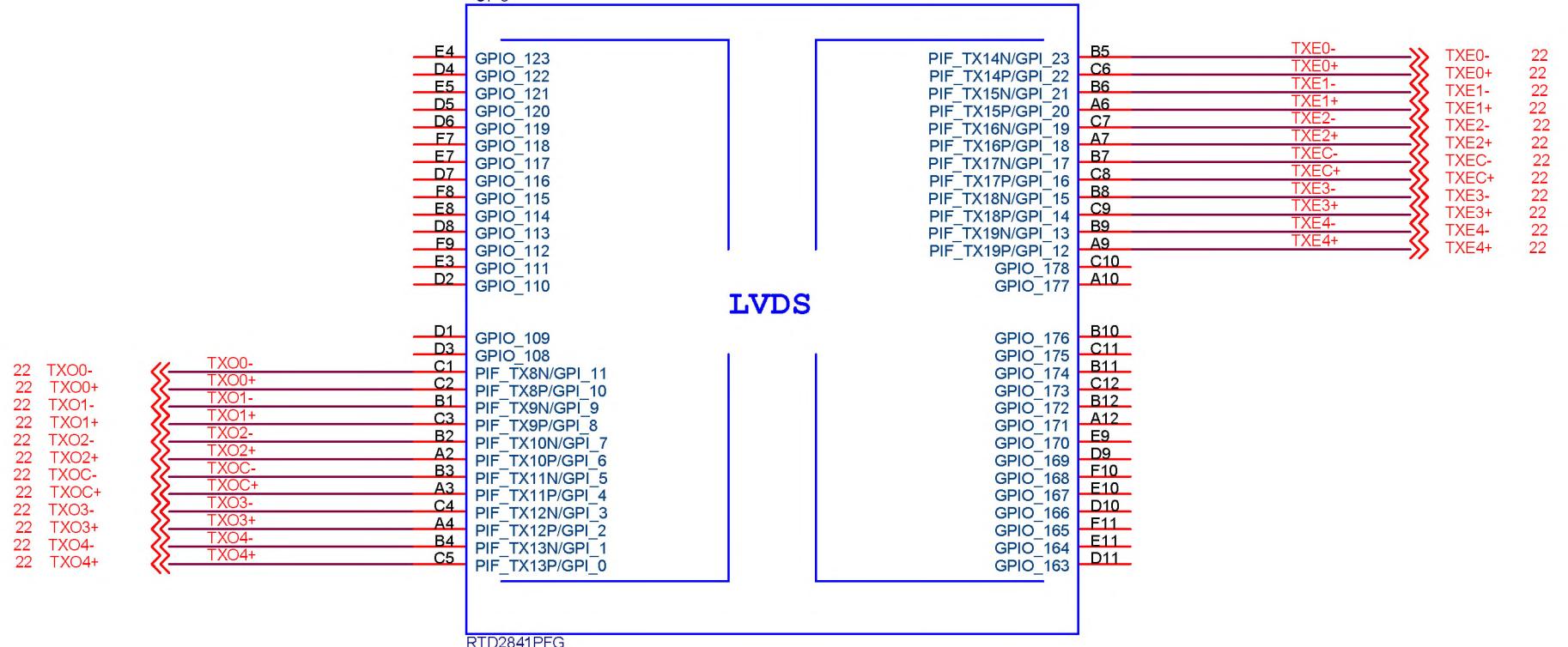


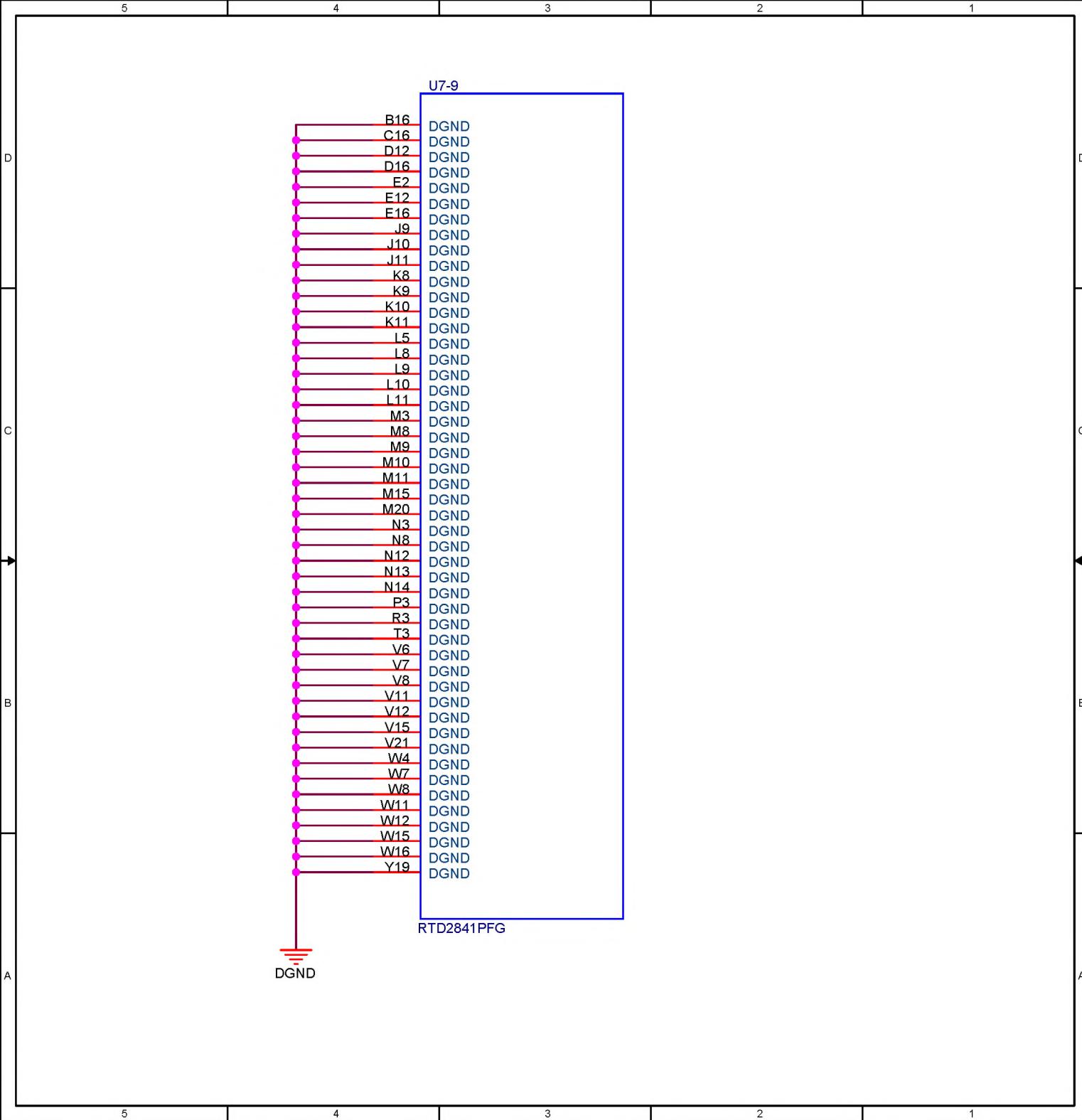


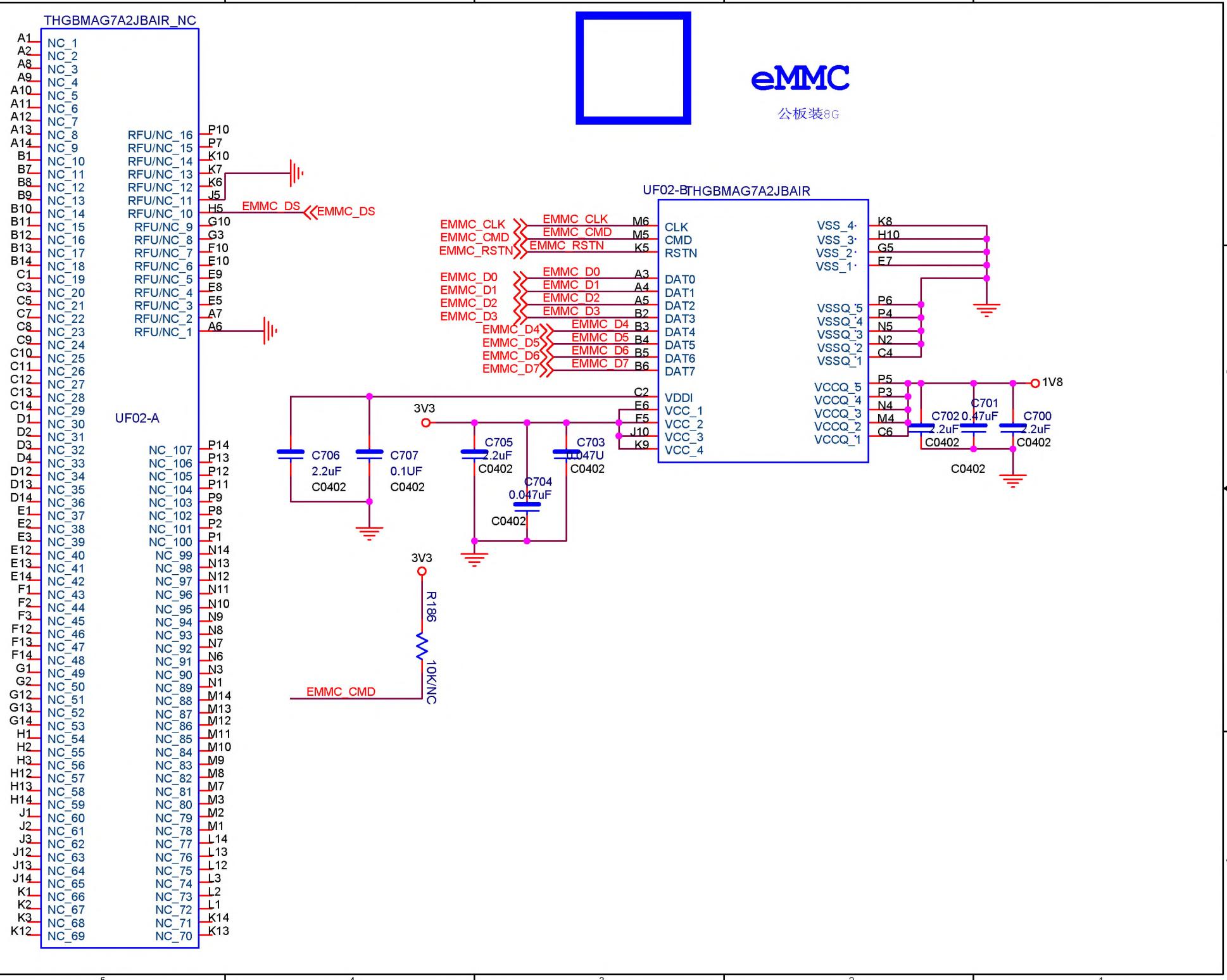


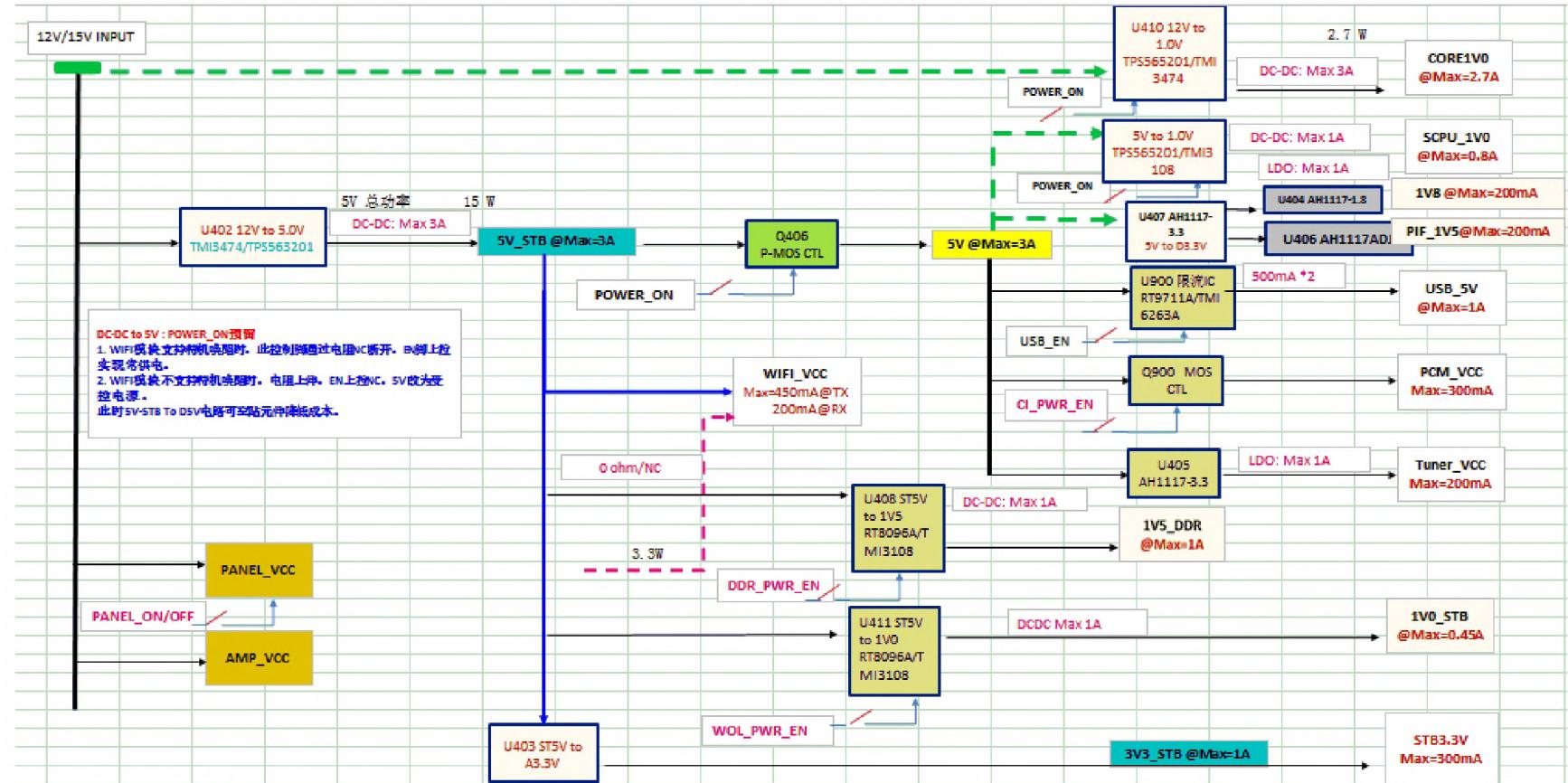


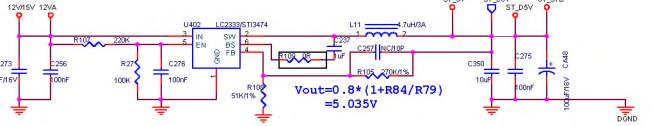




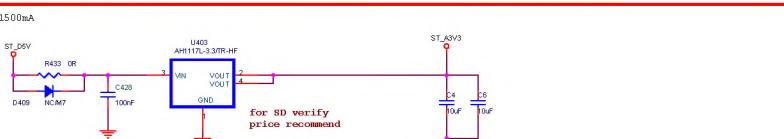




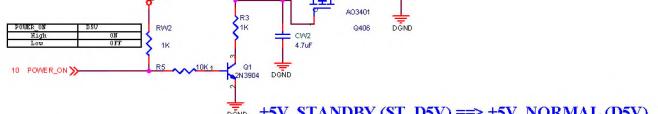




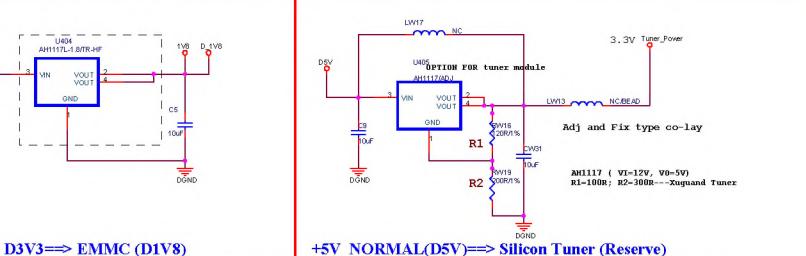
+12V Power for Standby (ST_D5V)



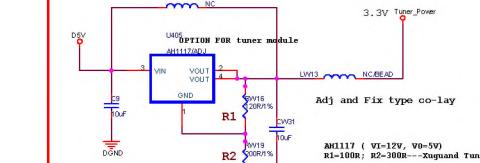
+5V_STANDBY (ST_D5V) \Rightarrow +3.3V_STANDBY (ST_A3V3)



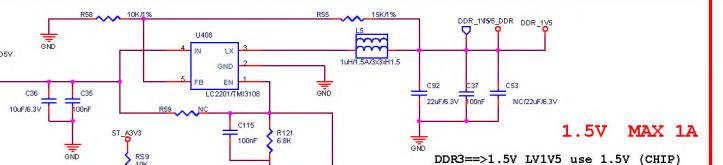
+5V_STANDBY (ST_D5V) \Rightarrow +5V_NORMAL (DSV)



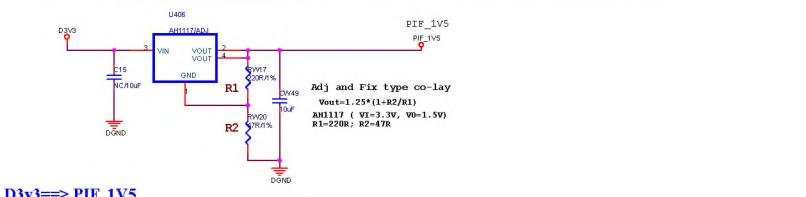
D3V3 \Rightarrow EMMC (D1V8)



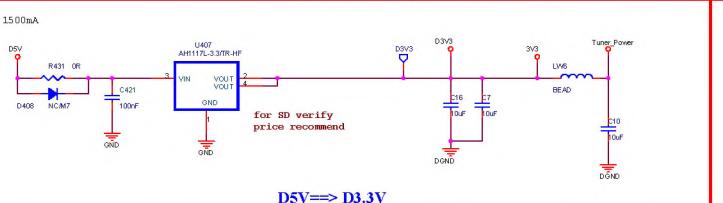
+5V_NORMAL(DSV) \Rightarrow Silicon Tuner (Reserve)



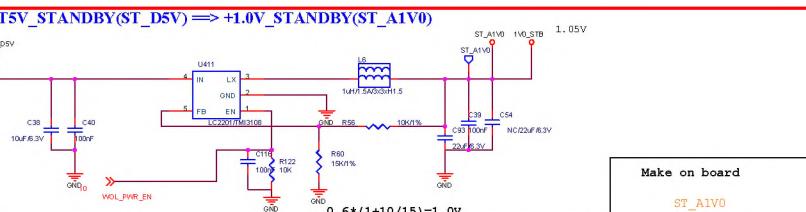
+5V_STANDBY (ST_D5V) \Rightarrow +1.5V (DDR1.5V)



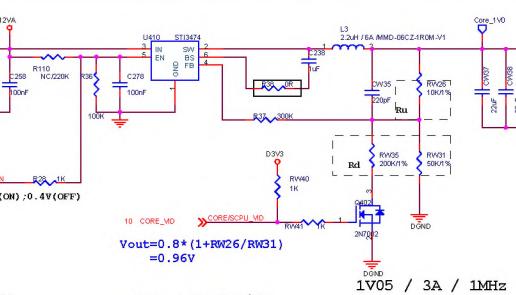
D3v3 \Rightarrow PIF_1V5



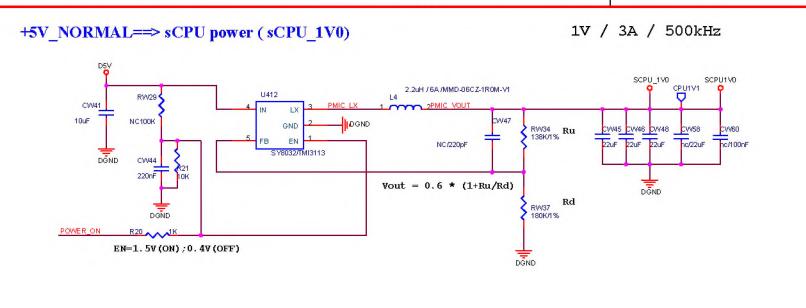
D5V \Rightarrow D3.3V



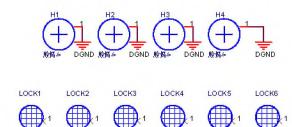
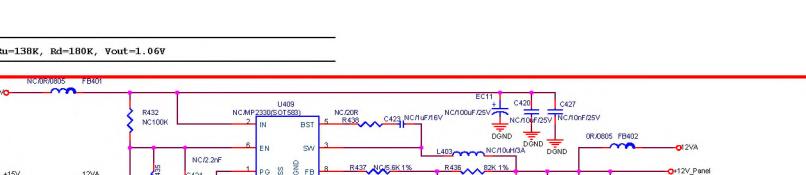
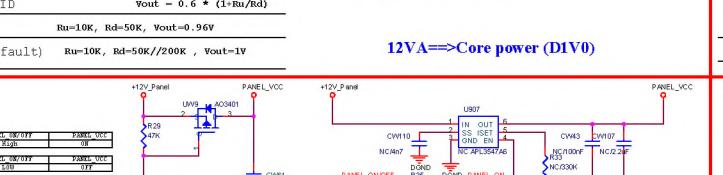
ST5V_STANDBY(ST_D5V) \Rightarrow +1.0V_STANDBY(ST_A1V0)



12VA \Rightarrow Core power (D1V0)

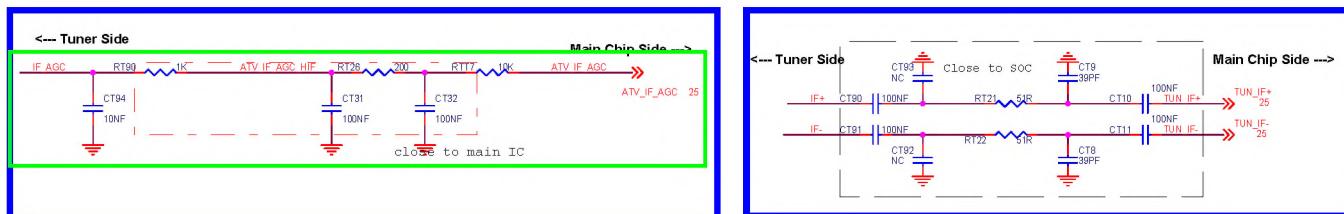
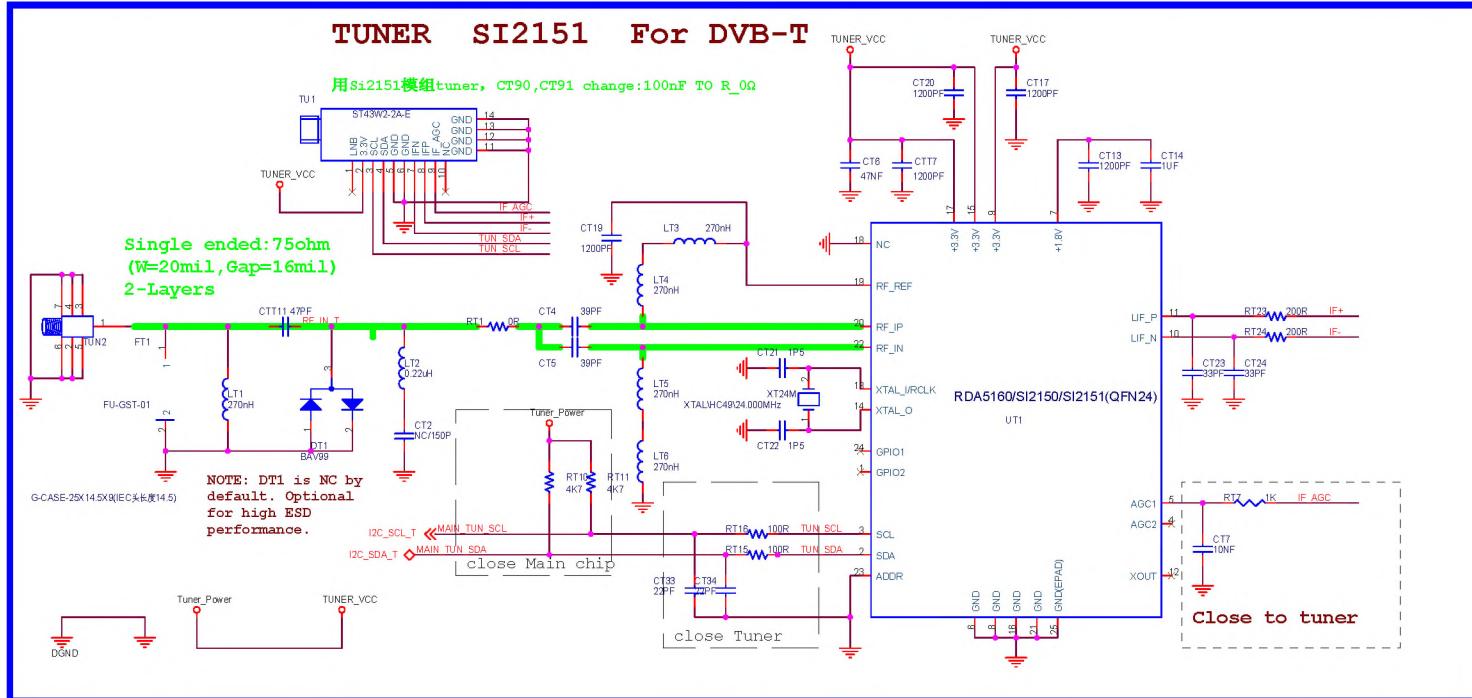


+5V_NORMAL \Rightarrow sCPU power (sCPU_1V0)

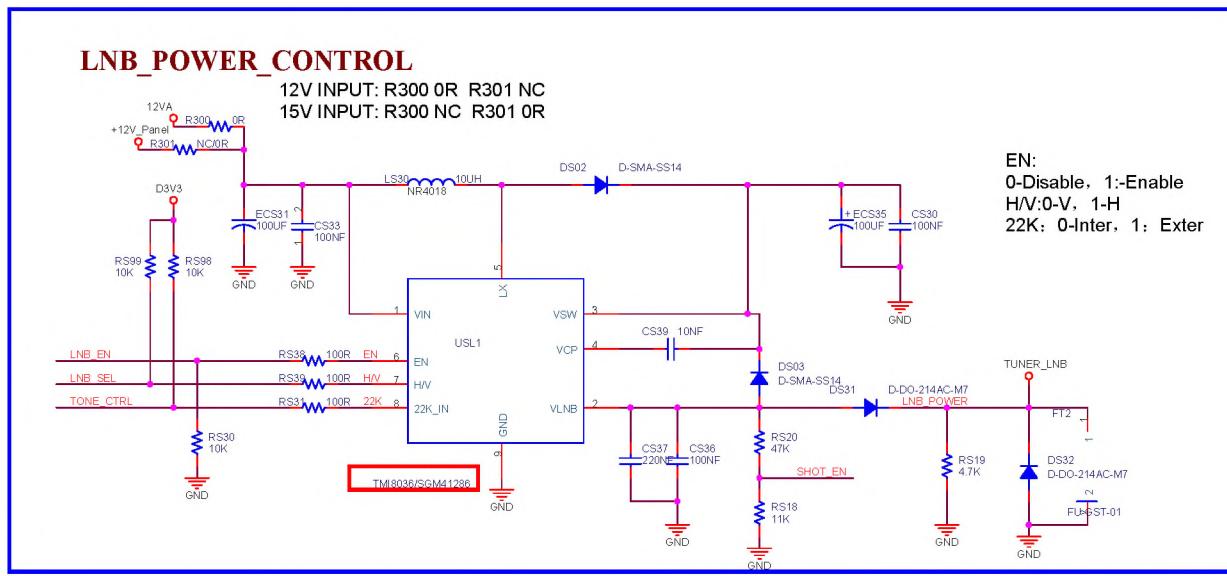
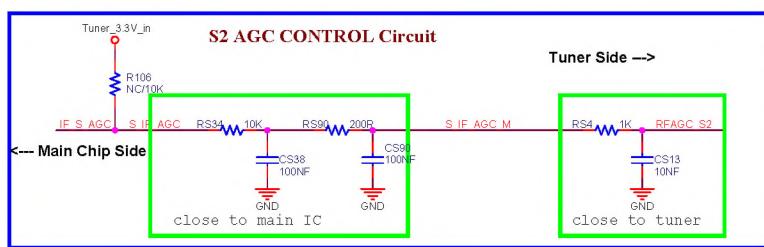
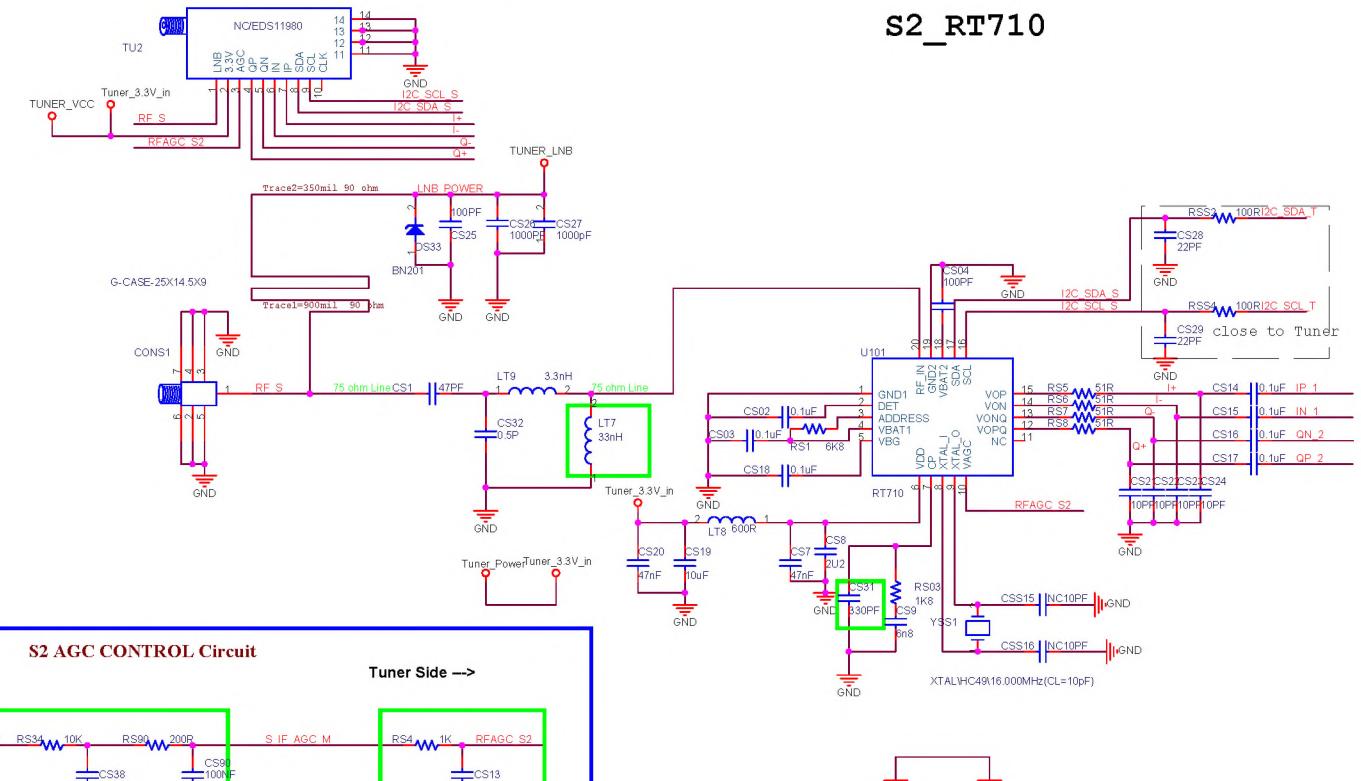
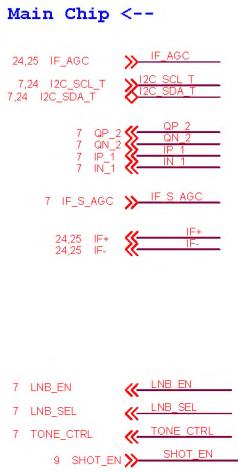


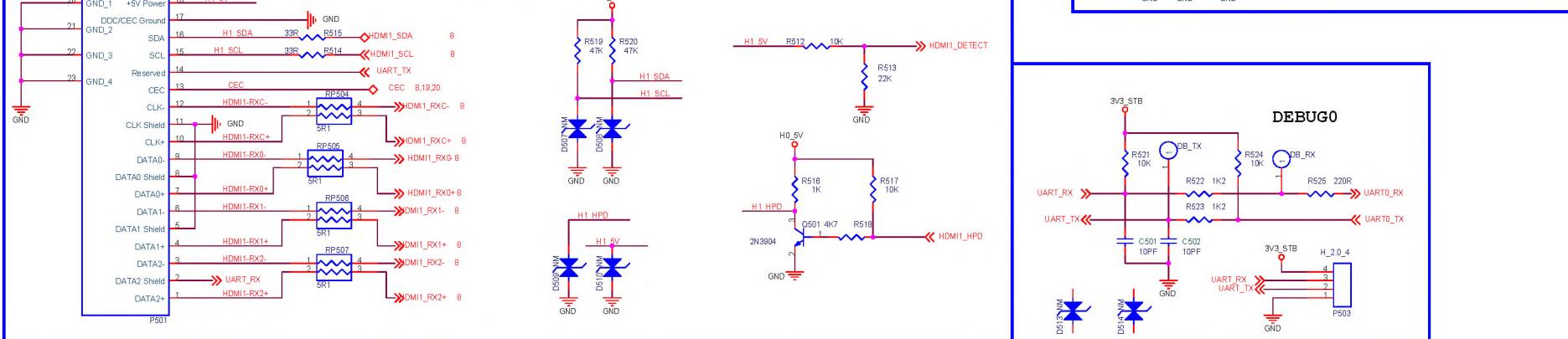
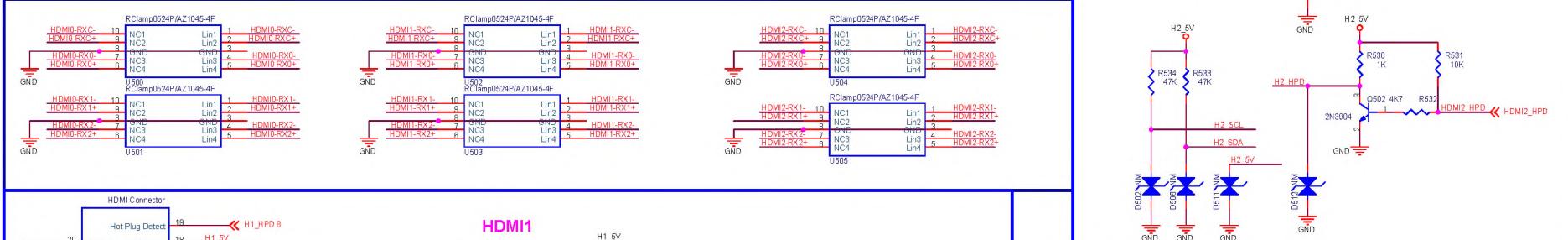
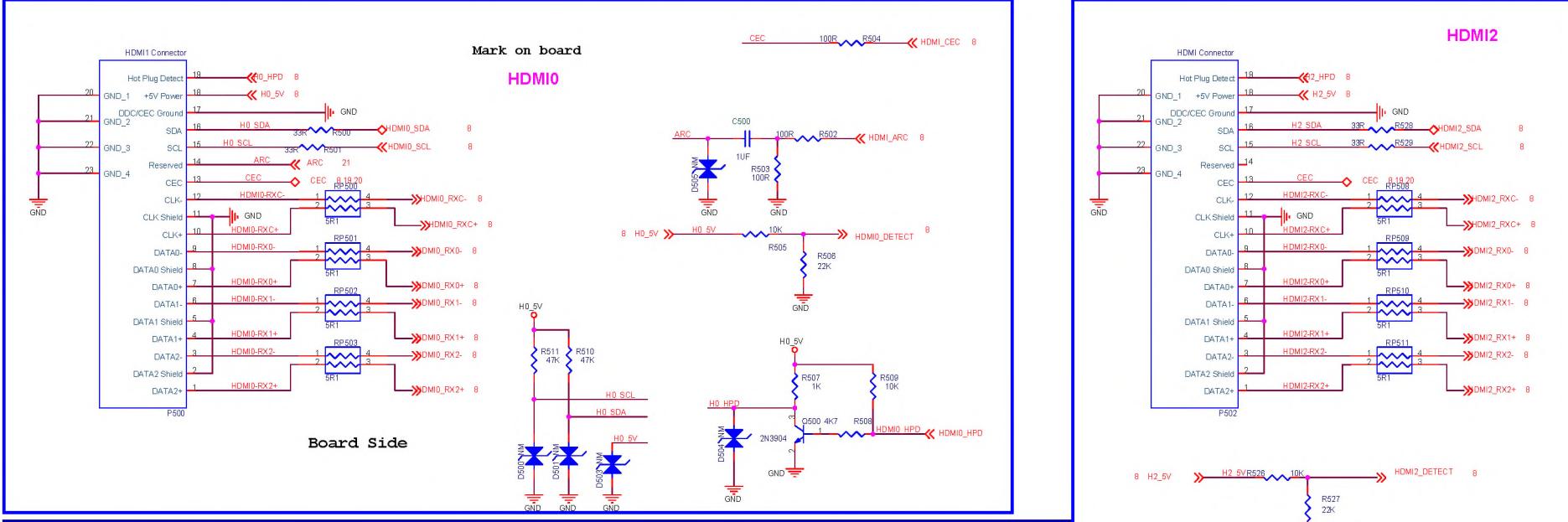
LOCK1
LOCK2
LOCK3
LOCK4
LOCK5
LOCK6

TUNER SI2151 For DVB-T

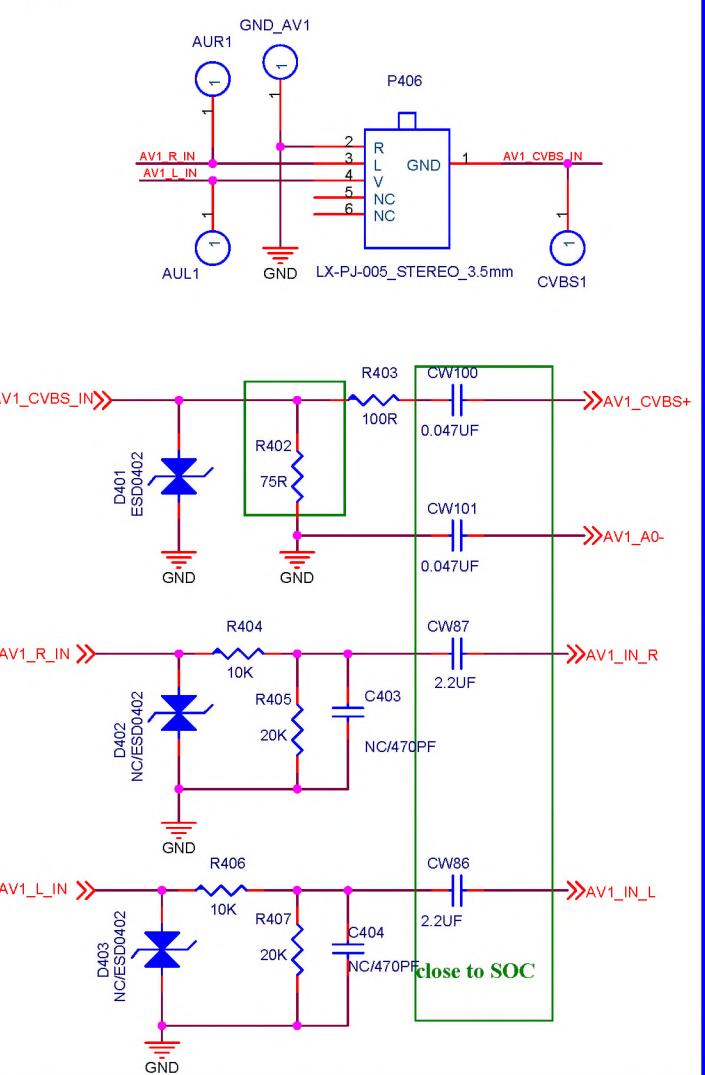


S2_RT710

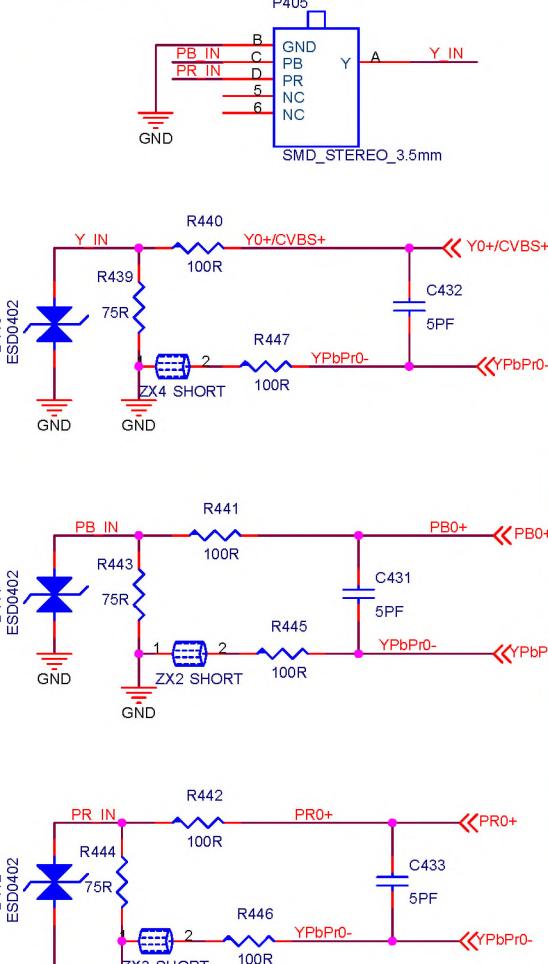




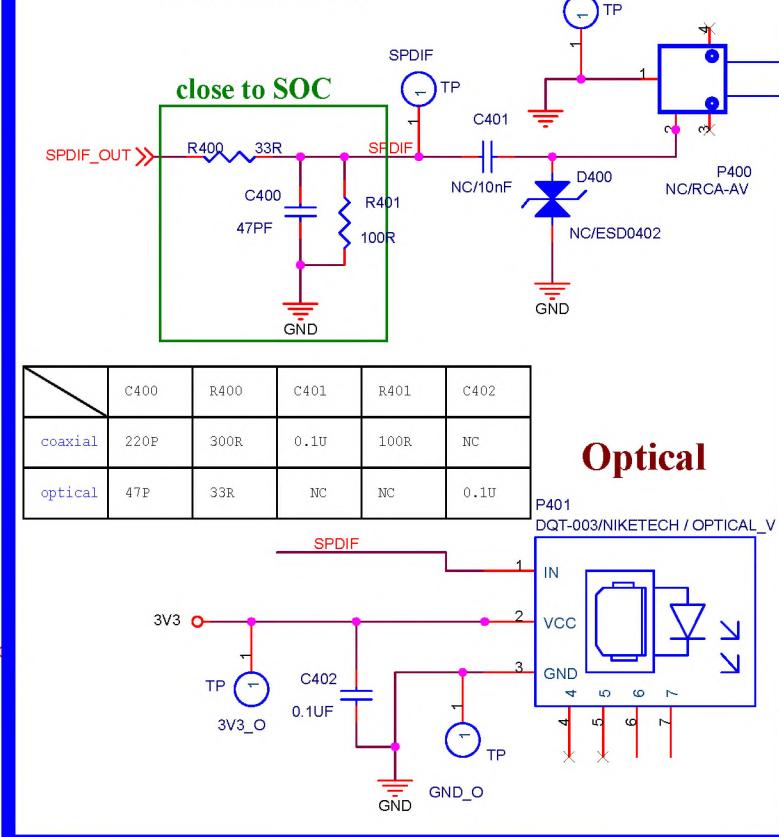
AV IN



YUV IN



Coaxial / NC



Optical

