Artemis Veizi

aveizi@alumni.princeton.edu | (703) 462-0980 | linkedin.com/in/artemisveizi

EDUCATION

Princeton University Class of 2023
B.S.E. Electrical Engineering: Computer Systems GPA: 3.81/4.0
Honors & Awards: G. David Forney Jr. Prize, awarded for outstanding accomplishments in communication science, systems and signals. Graduated magna cum laude.

Thomas Jefferson High School for Science and Technology Class of 2018
Senior Thesis: Neuroscience & CS Research Lab GPA: 4.53/4.0
Honors & Awards: National Merit Scholar, Congressional Award Gold Medal, Harvard Junior Book Award, Presidential Scholar Candidate

SKILLS

Languages & Frameworks: C++, C, Objective-C, Python, PyTorch, Docker, Matlab

Relevant Coursework: Information Theory, Theory of Computation, Advanced Cryptography, Computer Architecture and Organization, Philosophical Foundations of Probability, Stochastic Processes & Differential Equations, Concrete Mathematics, Artificial Intelligence

EXPERIENCE

Apple, Inc. Silicon Engineering Group Cupertino, CA
Silicon Validation Engineer August 2023–Present
- Built on work from prior internships to generate useful visualizations and identify anomalies in test data, applying novel statistical methods to accomplish on– and off–device learning.
- Also contributed to a system-wide redesign to implement our software in C++, while leading team efforts in test-driven development and QCI at Apple.

Silicon Validation Engineering Intern May 2022–August 2022
- Developed a data processing pipeline to visualize SoC performance data, generating plots from hundreds of thousands of data points. Further implemented an isolation forest algorithm and using various statistical techniques to identify anomalies and flag key data.

Silicon Validation Engineering Intern June 2020–May 2021
- Developed an optimization algorithm in C to run in an embedded environment, targeting SOC performance counters to reduce time to failure.
- Built a data processing flow to visualize optimizer performance and run failures.

The Aerospace Corporation Chantilly, VA
Software Engineering Intern May 2019–August 2019
- Developed a graph-based AI system in Python to automate rulebase error detection by detecting cycles & path inconsistencies. I received a Spot Award for my contributions.

National Institutes of Health Bethesda, MD
Cancer Research Fellow May 2016–August 2016
- Worked under Dr. Sriram Subramaniam to achieve atomic resolution structure determination by cryo-electron microscopy. Adapted compute-intensive imaging software by parallelizing workloads, improving efficiency.

EXTRA–CURRICULAR ACTIVITIES

Co-Chair, Early Career Professionals at Apple

Lab Teaching Assistant, Selected, ECE302: Robotic and Autonomous Systems Lab

Writing Center Fellow, Selected, Princeton University Writing Center

Princeton Varsity Women's Lightweight Rowing, D1
IRA All-Academic Team 2022, 2023
Student-Athlete Wellness Leader, Selected, Women's Rowing Team

Technology Chair, Elected, Ivy Club of Princeton (Student Eating Club)

INTERESTS

Albanian (first language), French (fluent), billiards, sourdough, road cycling, trail running.