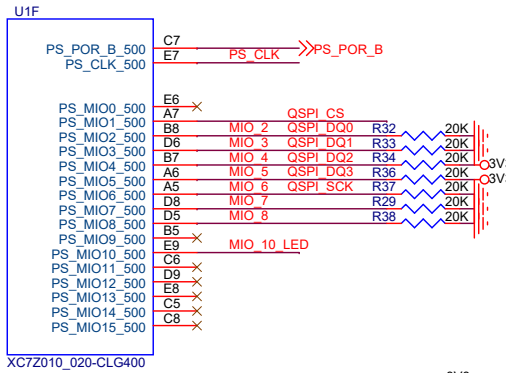
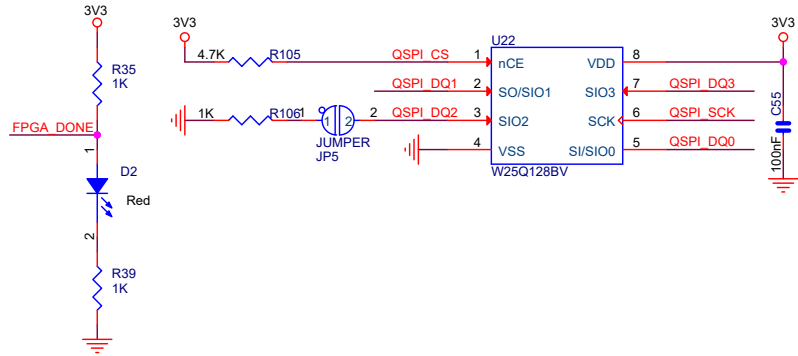
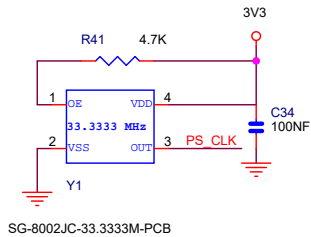
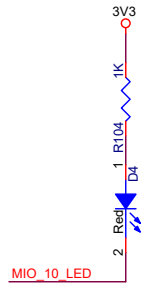
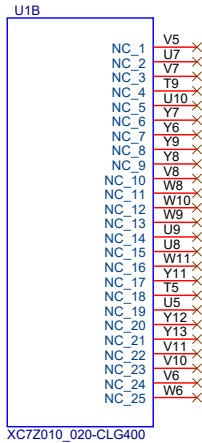
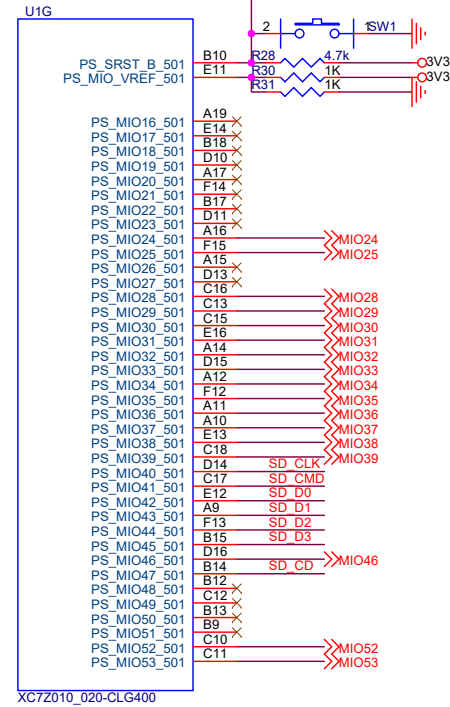


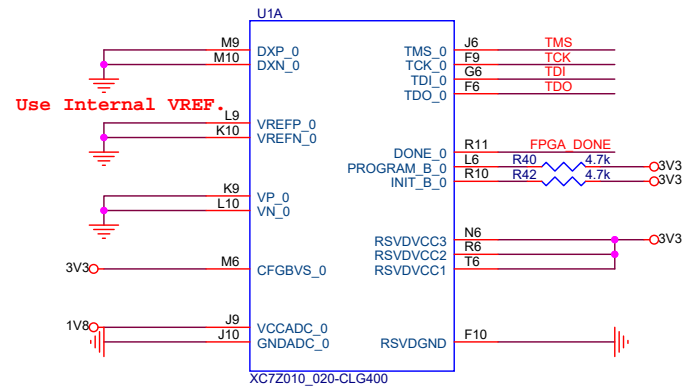
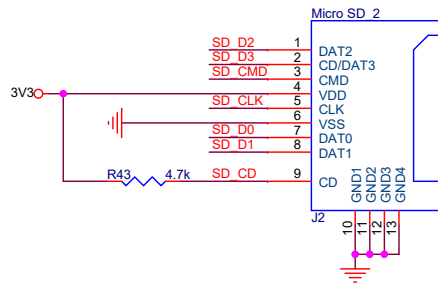
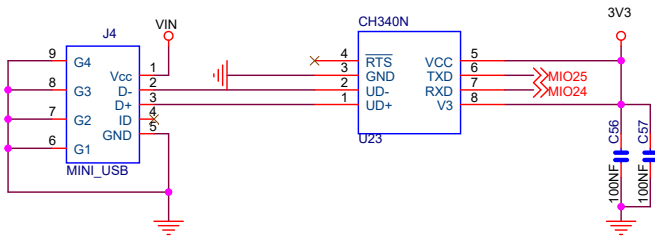
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JTAG Cascade mode  
Default SD CARD Boot  
PLL Enabled  
BANK0/1 voltage is 3.3V



SG-8002JC-33.3333M-PCB



Title		
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