

# Exam 3 Review

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CSE 2301: Principles and Practice of Digital Logic Design

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Coded in L<sup>A</sup>T<sub>E</sub>X

# Flip Flop Question

The operation of the S-R Latch is as follows:

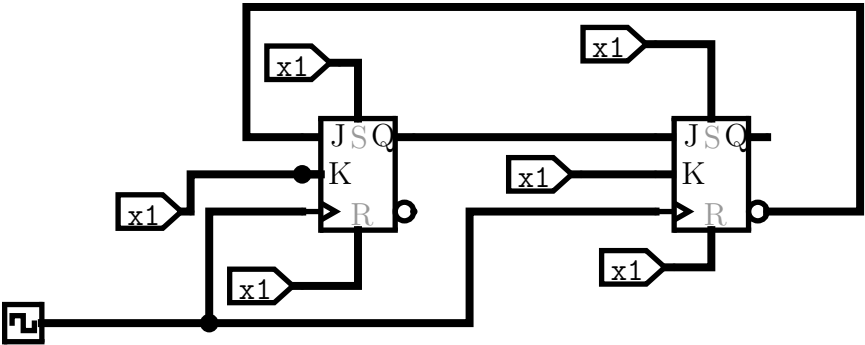


Table 1: Table from Circuit

CURRENT						NEXT	
$Q_b$	$Q_a$	$J_B$	$K_B$	$J_A$	$K_A$	$Q_b$	$Q_a$
0	0	0	0	1	1	0	1
0	1	1	0	1	1	1	0
1	0	0	0	0	1	1	0
1	1	1	0	0	1	1	0

## Discussion

## Practice Questions