

# Lab 01: Logicworks Introduction

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CSE 2301: Principles and Practice of Digital Logic Design  
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## Theory

### ORing Three Signals Using 2-Input OR Gates

To OR three signals  $C$ ,  $B$ , and  $A$  together using only 2-input OR gates:

1. Connect signals B and A to the inputs of the first OR gate.
2. The output of this OR gate is then connected to one input of the second OR gate.
3. The remaining signal ( $C$ ) is connected to the other input of the second OR gate.

This arrangement ensures that the output of the circuit will be the logical OR of all three inputs. The truth table for this 3-way OR gate is given by:

C	B	A	F
0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	1

## Deliverables

For Exercise 1, we were tasked with creating a circuit to acquaint ourselves with Logicworks 5. The circuit produced a truth table with the following inputs and outputs:

C	B	A	X	Y	Z	Hex
0	0	0	0	0	0	0x0
0	0	1	0	0	1	0x1
0	1	0	0	1	0	0x2
0	1	1	0	1	1	0x3
1	0	0	0	1	1	0x3
1	0	1	1	0	0	0x4
1	1	0	1	0	1	0x5
1	1	1	1	1	0	0x6

The hex output effectively converts a 3-bit binary input into 8 different possible combinations through the use of a Programmable Logic Array that weights the input values  $C$ ,  $B$ , and  $A$  to 3, 2, and 1 respectively. Each output line represents a "power of two" such that  $Z$  is  $2^0$ ,  $Y$  is  $2^1$ , and  $X$  is  $2^2$ . These are considered our "binary" output from which the Hex display device's inner logic renders the proper digit in our logicworks program. Hexadecimal is a base 16 radix system, and the conversion is straightforward as learned in class. Since  $C$  is worth a weight of 3, inputs 100 and 001 have the same XYZ and hex output.

## Discussion

This lab assignment had us gain familiarity with Logicworks 5 and our TA's. I have some experience using the Logisim Evolution open-source digital prototyping software, so it was a joy to learn a new tool, although a little bit disappointing that we will not be able to locally install this software on our computers. However, the lab was engaging as I gained familiarity with the class format and the gates' functions. The introduction to the circuit in the second example provided a new definition for me to learn, the 'minterm', which allowed us to create a sum-of-products form to design the circuit in Example 2.

### Practice Questions

#### Voltage Drop Across LED and Resistor in Series

- Given that the voltage drop across the LED is 4V:

$$V_{LED} = 4V$$

$$R = 7000\Omega$$

$$V_{TOT} = 18V$$

$$V_R = V_{TOT} - V_{LED} = 18V - 4V = 14V$$

$$V = IR \Rightarrow I = \frac{V_R}{R} = \frac{14V}{7000\Omega} = 2mA$$

#### TTL Logic Ranges

In TTL (Transistor-Transistor Logic):

- Logic 0: A Voltage between 0.8V and 2V is considered a logical '0'.
- Logic 1: A voltage between 2V and 5V is considered a logical '1'.

This ensures the circuit can clearly distinguish between low (0) and high (1) signals. As a side effect, this makes TTL logic compatible with CMOS Logic as long as the 5V HIGH does not damage the receiver input.