

# **Artosyn JIANGWAN2**

## **dwc\_ddr4\_multiply\_tsmc28hpc18**

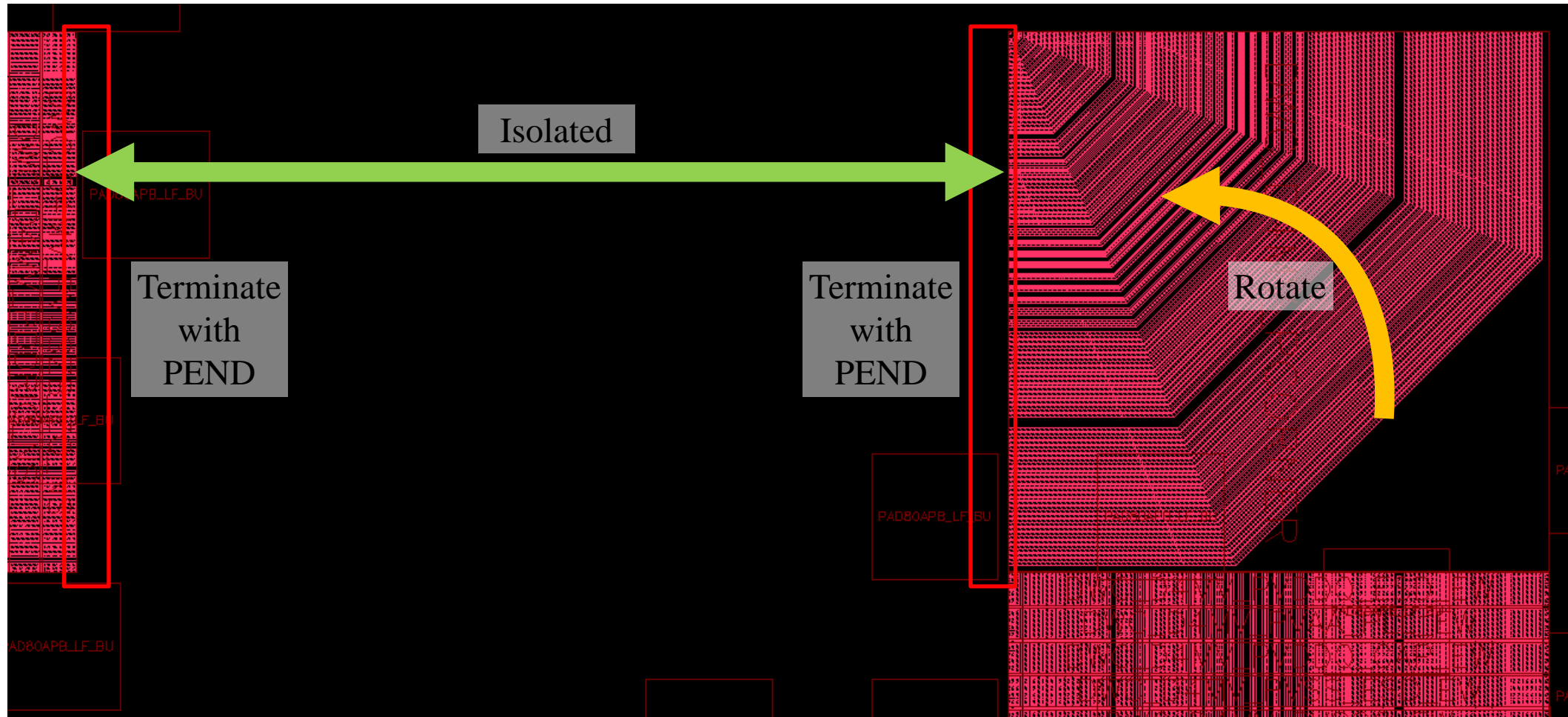
### **GDS review feedback (Case 8001033752)**

Synopsys DDR PHY Support  
1/August, 2017

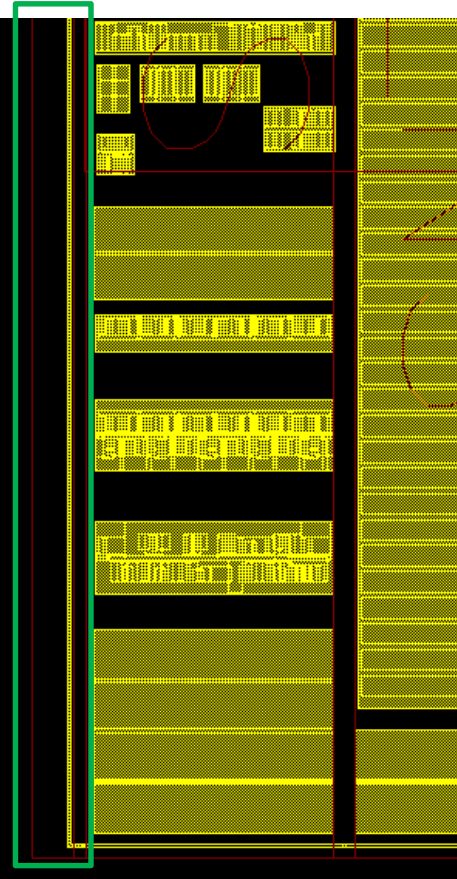


## #2 Abutment – Isolation, Corner

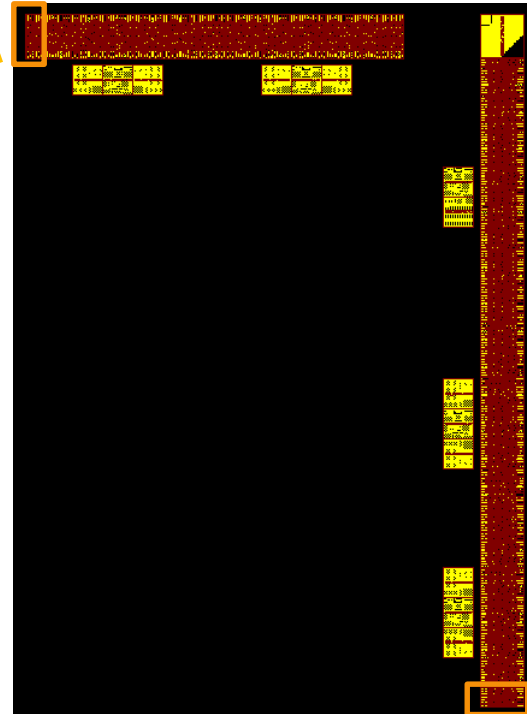
M5



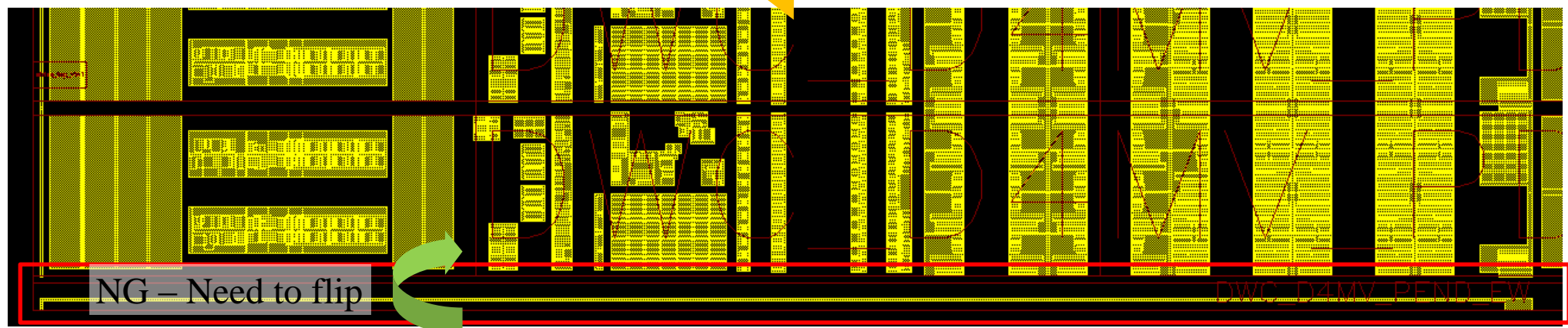
# #3 PEND flipping



Good



NW

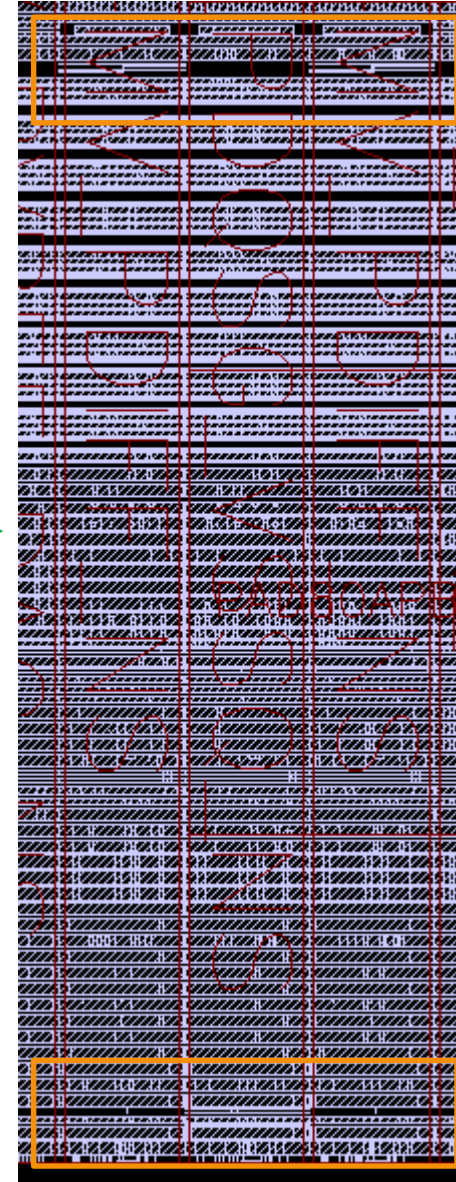
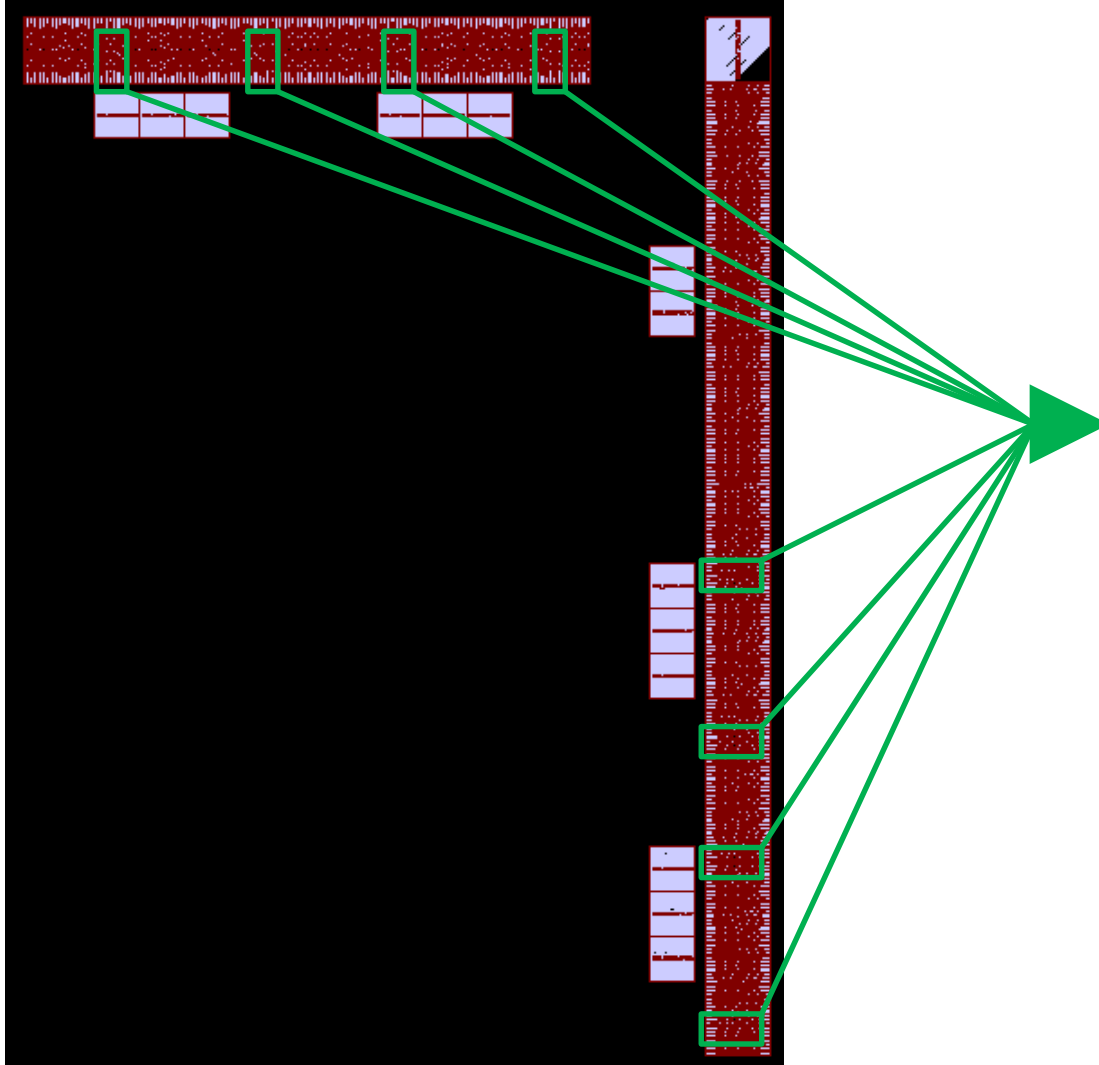


NG – Need to flip

DWC\_D4MV\_PEND\_FW



# #5 DQS connections

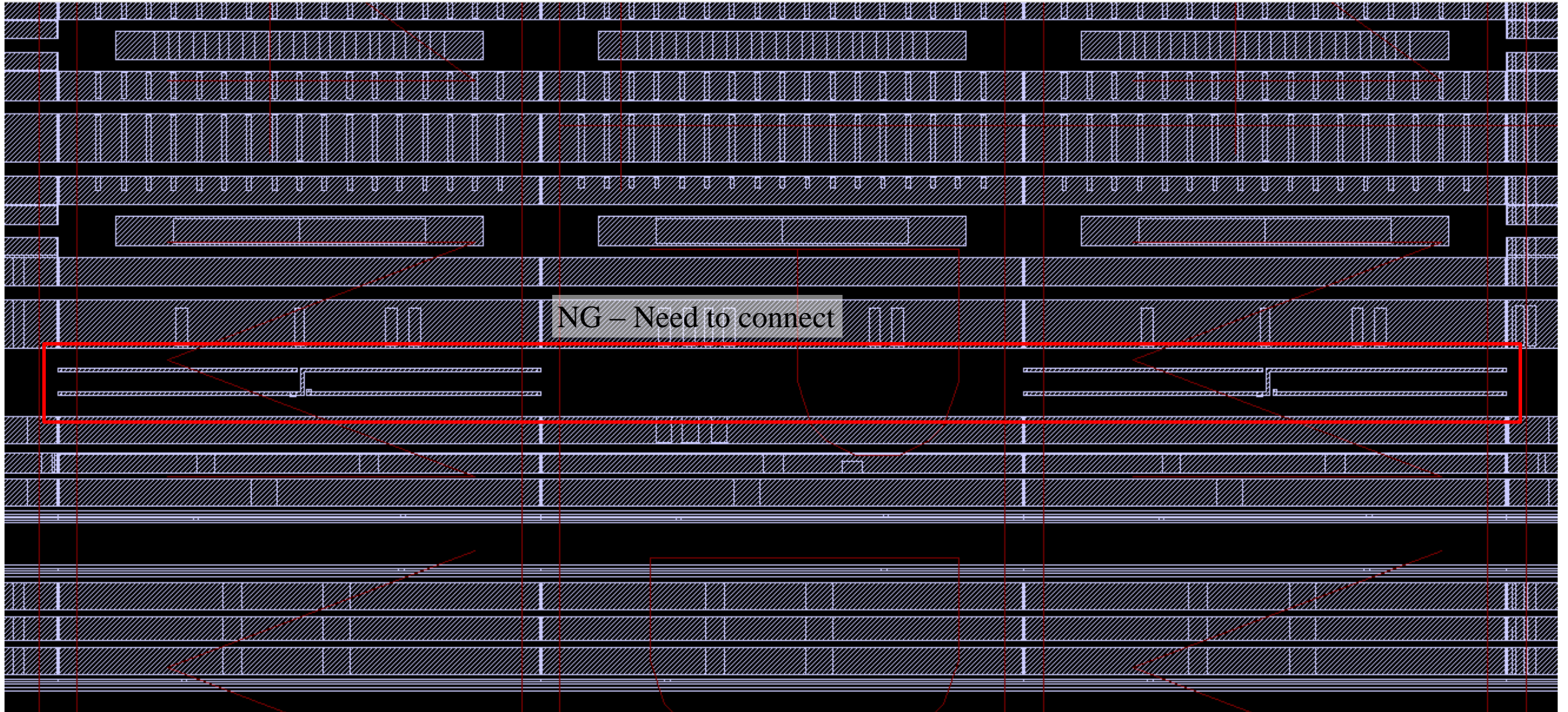


See following slides  
(#5 DQS DFI to  
DFO)

See following slides  
#5 DQS DISE to  
DQSSE/DQSBSE)

# #5 DQS DFI to DFO

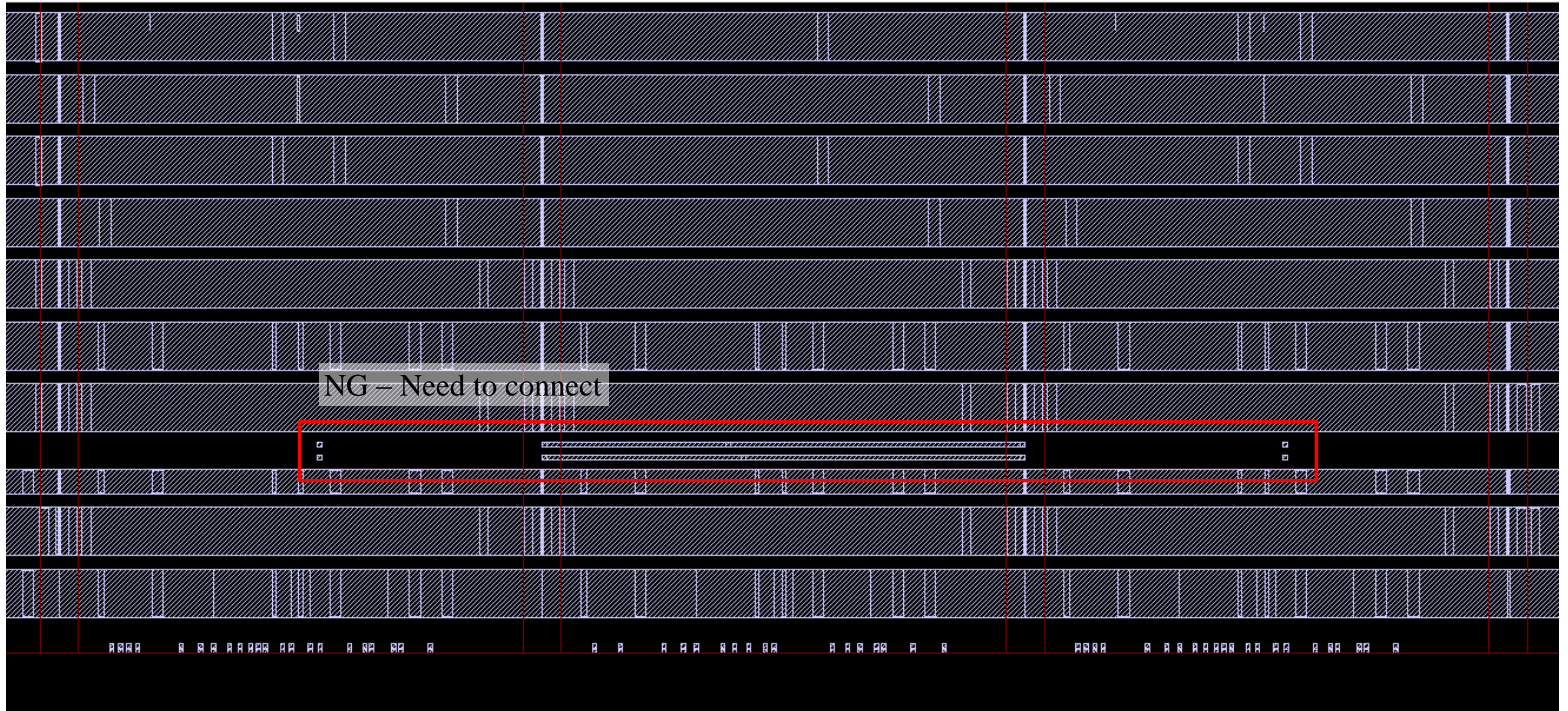
M4





# #5 DQS DISE to DQSSE/DQSBSE

M4



# Thank You

