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| Project Name | **Sirius** | ECO # | SIR-234 |
| Version No. | **Rev. A** |  |  |
| Requesting Engineer | HUIBO ZHONG | Request Date | 20170823 |
| Approve Manager | Song Pan | Request Date |  |
| Problem to be fixed | 1. **Bug/Issue description:**   Async fifo full error in vif2isp data path   1. **Impact if this ECO is not implemented:**   Vif2isp data path will error | | |
| Affected Parts | 1. **Affected IP(s)**   Video\_IF   1. **Affected Partition(s)**   SUB\_1   1. **Current status of affected Partitions(s)  (Placement, Pre-CTS, Post-CTS, Post-Route, STA, etc)**   **STA** | | |
| Fix Solution | **Describe briefly how to solve the problem.**   1. .ADD NEW LOGIC TO GENERATE THE FULL SIGNAL | | |
| Change Description | 1. **Change method (manual ECO fix on netlist or partial Re-synthesis):**   Manual ECO fix @SUB 1   1. **Change effort :** 2. **Connection change or Logic fix:** 3. **How many DFFs or combinational cells will be added or affected in this ECO?**   ADD 28 DFF, 97 LOGIC GATE     1. **Does it have interface change on IP top level? If yes, please describe it in detail**      1. **Change Schematics:**      1. **Others (for analog macros):** | | |
| IP interface change | **No** | | |
| File to be changed | ADD NEW FILE VIF\_ASYNC\_FIFO\_FULL.V | | |
| Change Impacts | 1. **Impacts on scan chain :**   NO.   1. **Impacts on timing critical path:**   NO.   1. **Impacts on IP size and/or floorplan:**   NO. | | |
| Verification status | Done. | | |

Dff connection



INDEX number

|  |  |
| --- | --- |
|  | Path0\_fifo |
| 0 | Eco\_hbzhong\_vif\_cell\_raddr\_gray\_0\_ |
| 1 | Eco\_hbzhong\_vif\_cell\_raddr\_gray\_1\_ |
| 2 | Eco\_hbzhong\_vif\_cell\_raddr\_gray\_2\_ |
| 3 | Eco\_hbzhong\_vif\_cell\_raddr\_gray\_3\_ |
| 4 | Eco\_hbzhong\_vif\_cell\_rptr1\_0\_ |
| 5 | Eco\_hbzhong\_vif\_cell\_rptr1\_1\_ |
| 6 | Eco\_hbzhong\_vif\_cell\_rptr1\_2\_ |
| 7 | Eco\_hbzhong\_vif\_cell\_rptr1\_3\_ |
| 8 | Eco\_hbzhong\_vif\_cell\_rptr2\_0\_ |
| 9 | Eco\_hbzhong\_vif\_cell\_rptr2\_1\_ |
| 10 | Eco\_hbzhong\_vif\_cell\_rptr2\_2\_ |
| 11 | Eco\_hbzhong\_vif\_cell\_rptr2\_3\_ |
| 12 | Eco\_hbzhong\_vif\_cell\_full\_reg |
|  | Path1\_fifo |
| 14 | Eco\_hbzhong\_vif\_cell\_raddr\_gray\_0\_ |
| 15 | Eco\_hbzhong\_vif\_cell\_raddr\_gray\_1\_ |
| 16 | Eco\_hbzhong\_vif\_cell\_raddr\_gray\_2\_ |
| 17 | Eco\_hbzhong\_vif\_cell\_raddr\_gray\_3\_ |
| 18 | Eco\_hbzhong\_vif\_cell\_rptr1\_0\_ |
| 19 | Eco\_hbzhong\_vif\_cell\_rptr1\_1\_ |
| 20 | Eco\_hbzhong\_vif\_cell\_rptr1\_2\_ |
| 21 | Eco\_hbzhong\_vif\_cell\_rptr1\_3\_ |
| 22 | Eco\_hbzhong\_vif\_cell\_rptr2\_0\_ |
| 23 | Eco\_hbzhong\_vif\_cell\_rptr2\_1\_ |
| 24 | Eco\_hbzhong\_vif\_cell\_rptr2\_2\_ |
| 25 | Eco\_hbzhong\_vif\_cell\_rptr2\_3\_ |
| 26 | Eco\_hbzhong\_vif\_cell\_full\_reg |
|  | Isp\_rd\_ctrl |
| 13 | Eco\_hbzhogn\_vif\_cell\_reg\_path0\_isp\_fifo\_wr\_hold\_reg |
| 27 | Eco\_hbzhogn\_vif\_cell\_reg\_path1\_isp\_fifo\_wr\_hold\_reg |