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| Project Name | **Sirius** | ECO # | SIR-242 |
| Version No. | **Rev. A** |  |  |
| Requesting Engineer | Lei Yang | Request Date | 25-8-2018 |
| Approve Manager | Song Pan | Request Date | 25-8-2018 |
| Problem to be fixed | 1. **Bug/Issue description:**   **SUB\_TOP: emmc disable sec/non-sec access sram/ddr, only support non-sec mode**   1. **Impact if this ECO is not implemented:**   CPU, DDR | | |
| Affected Parts | 1. **Affected IP(s)**   **EMMC**   1. **Affected Partition(s)**   **SUB\_TOP**   1. **Current status of affected Partitions(s)  (Placement, Pre-CTS, Post-CTS, Post-Route, STA, etc)** | | |
| Fix Solution | **Describe briefly how to solve the problem.**  **a.change the register default value of awprot[1] and arprot[1] from 0 to 1** | | |
| Change Description | 1. **Change method (manual ECO fix on netlist or partial Re-synthesis):**   Manual ECO fix **@SUB\_TOP**   1. **Change effort :** 2. **Connection change or Logic fix:**   **Substitute DFF type of arprot[1] and awprot[1] from SVP\_FSDPRBQ\_1 to SVP\_FSDPSBQO\_1**   1. **How many DFFs or combinational cells will be added or affected in this ECO?**     **Substitute 2 DFF**   1. **Does it have interface change on IP top level? If yes, please describe it in detail**     No   1. **Change Schematics:** 2. **Others (for analog macros):** | | |
| IP interface change | **NO** | | |
| File to be changed | **xxxxxxxxxxxxxxxx** | | |
| Change Impacts | 1. **Impacts on scan chain :**   **NO**   1. **Impacts on timing critical path:**   **NO**   1. **Impacts on IP size and/or floorplan:**   NO | | |
| Verification status | Done | | |