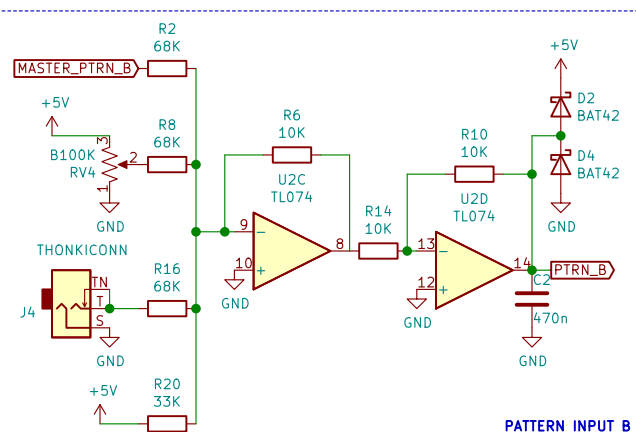
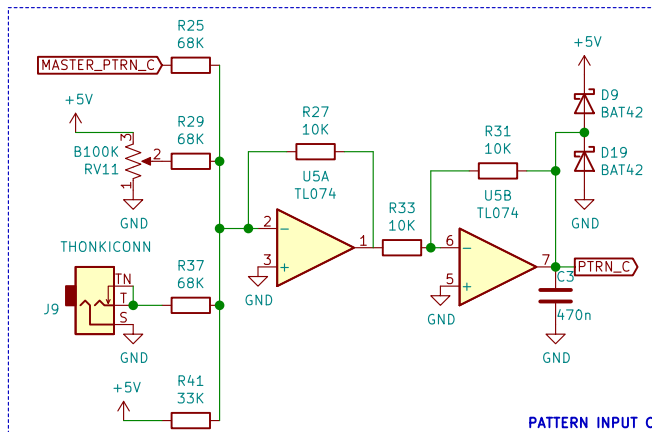


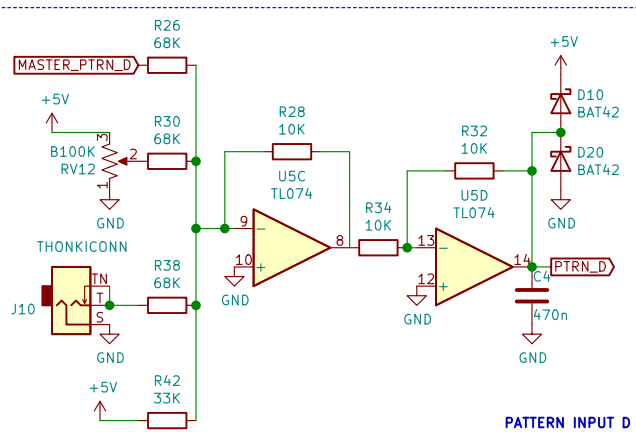
PATTERN INPUT A



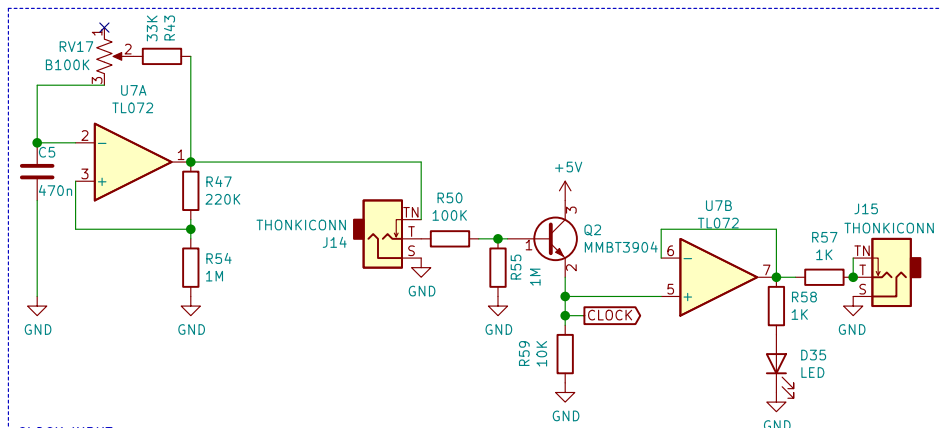
PATTERN INPUT 8



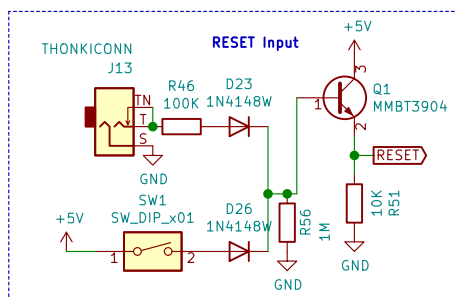
PATTERN INPUT C



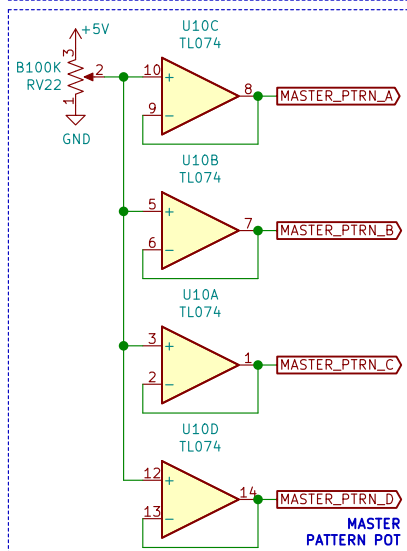
PATTERN INPUT D



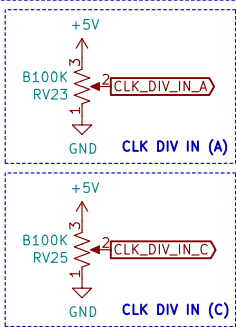
CLOCK INPUT



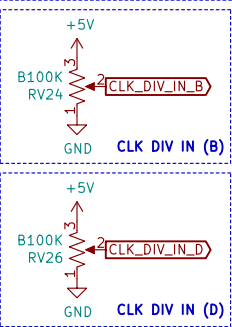
RESET Input



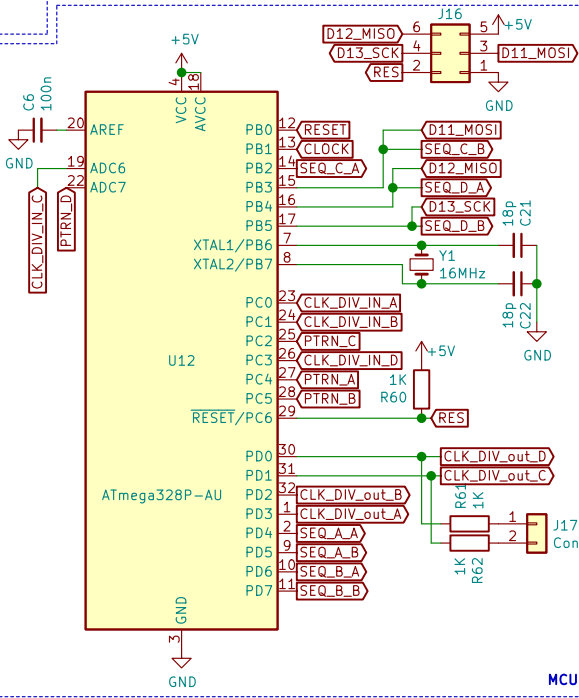
MASTER



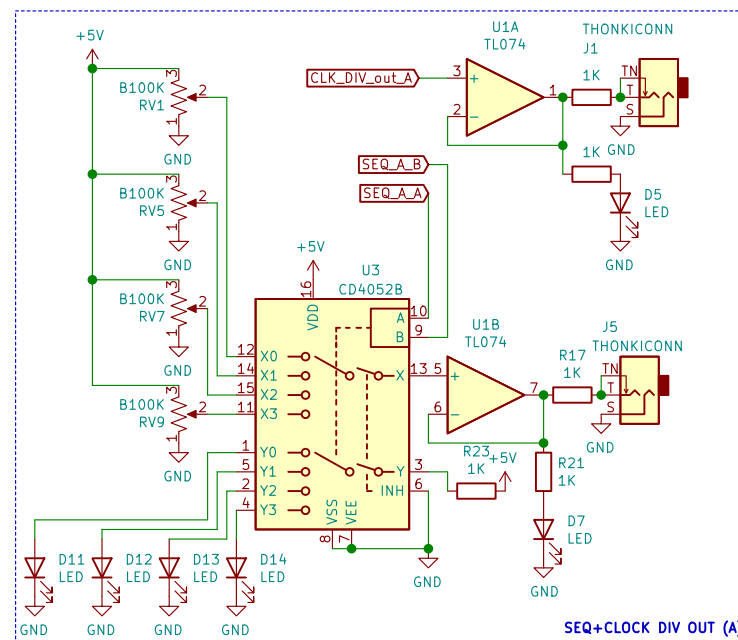
CLK DI



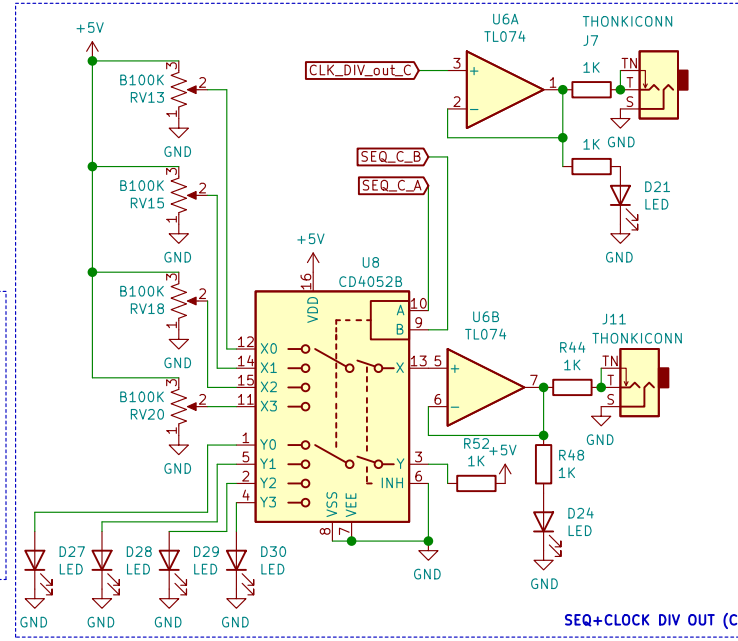
CLK



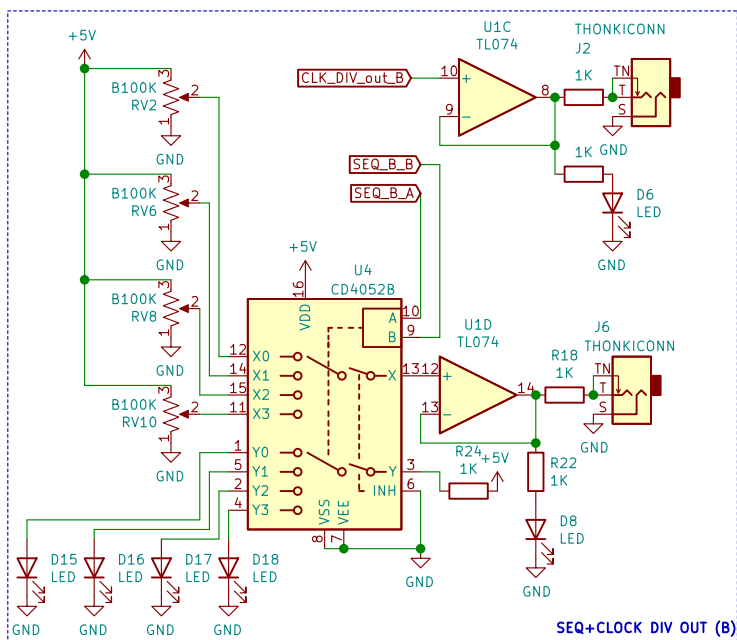
MCU



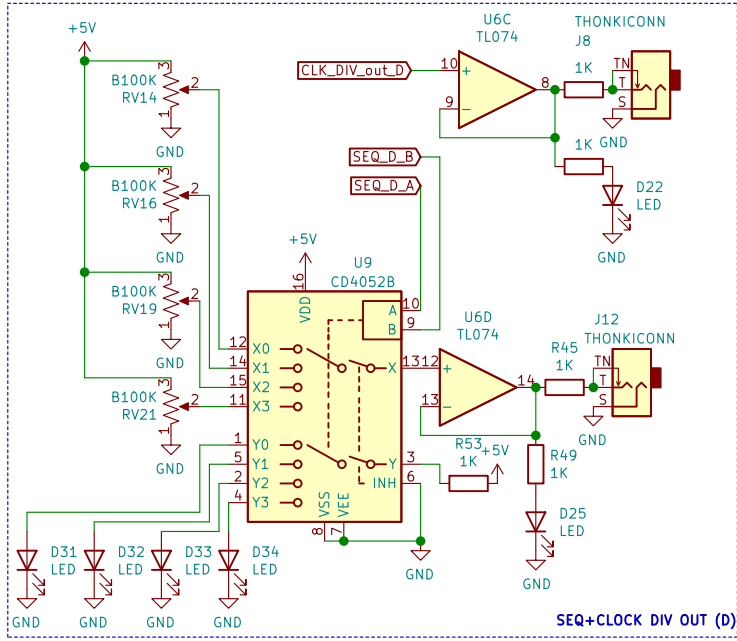
SEQ+CLOCK DIV OUT (A)



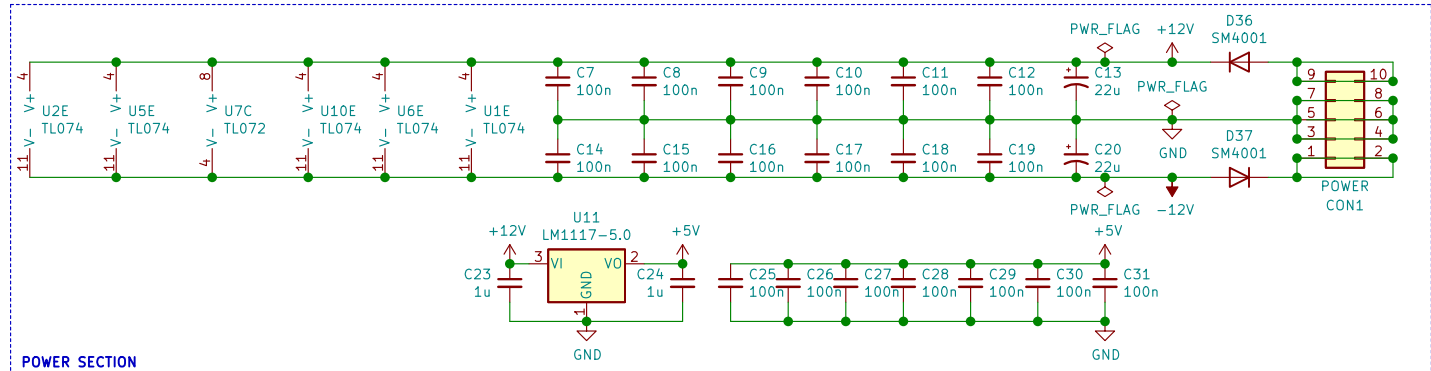
SEQ+CLOCK DIV OUT (C



SEQ+CLOCK DIV OUT (B)



SEQ+CLOCK DIV OUT (D)



POWER SECTION