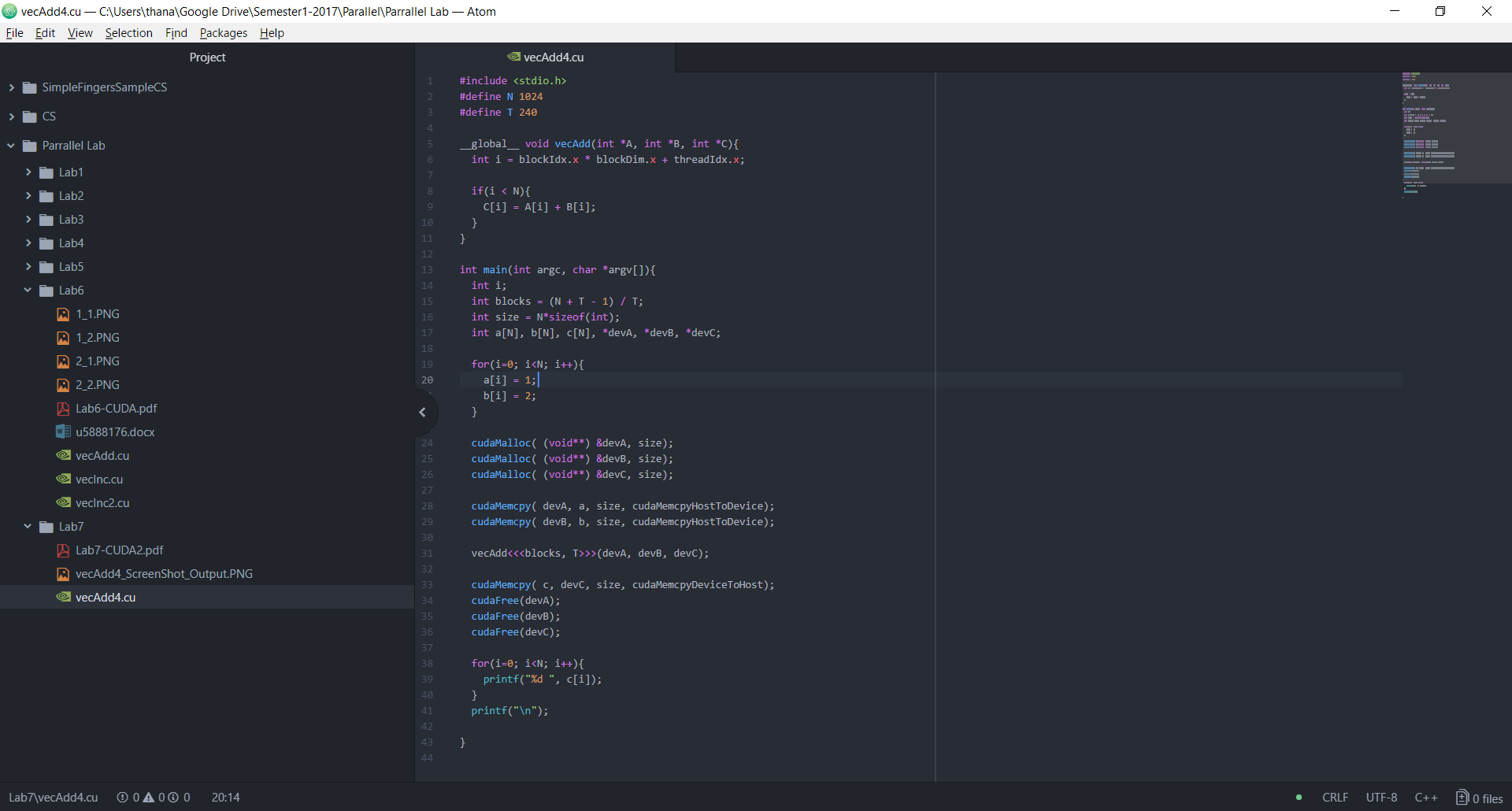
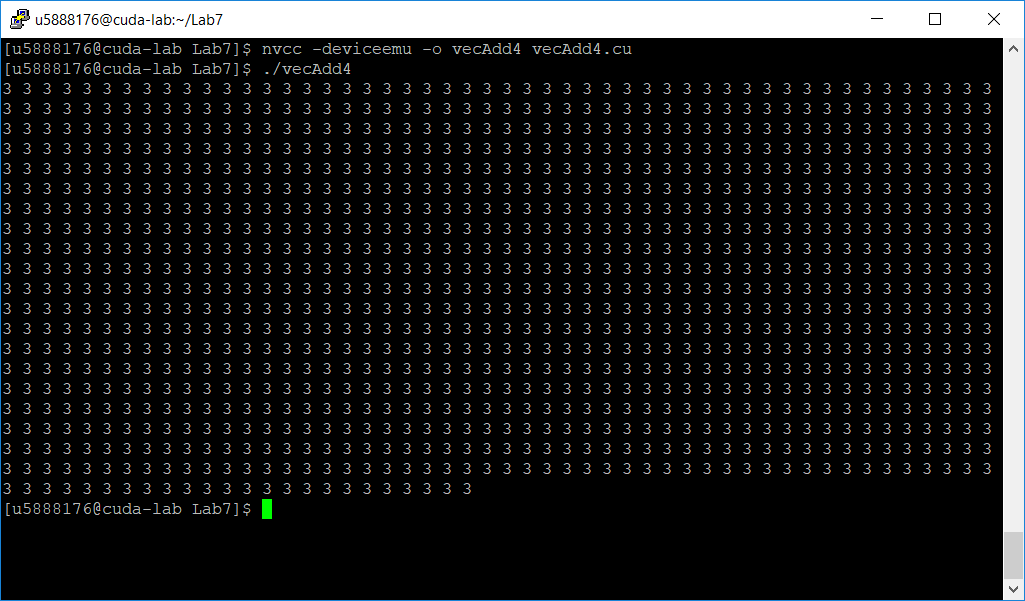
**ITCS443 Parallel and Distributed System**

**Lab7: CUDA Threads**

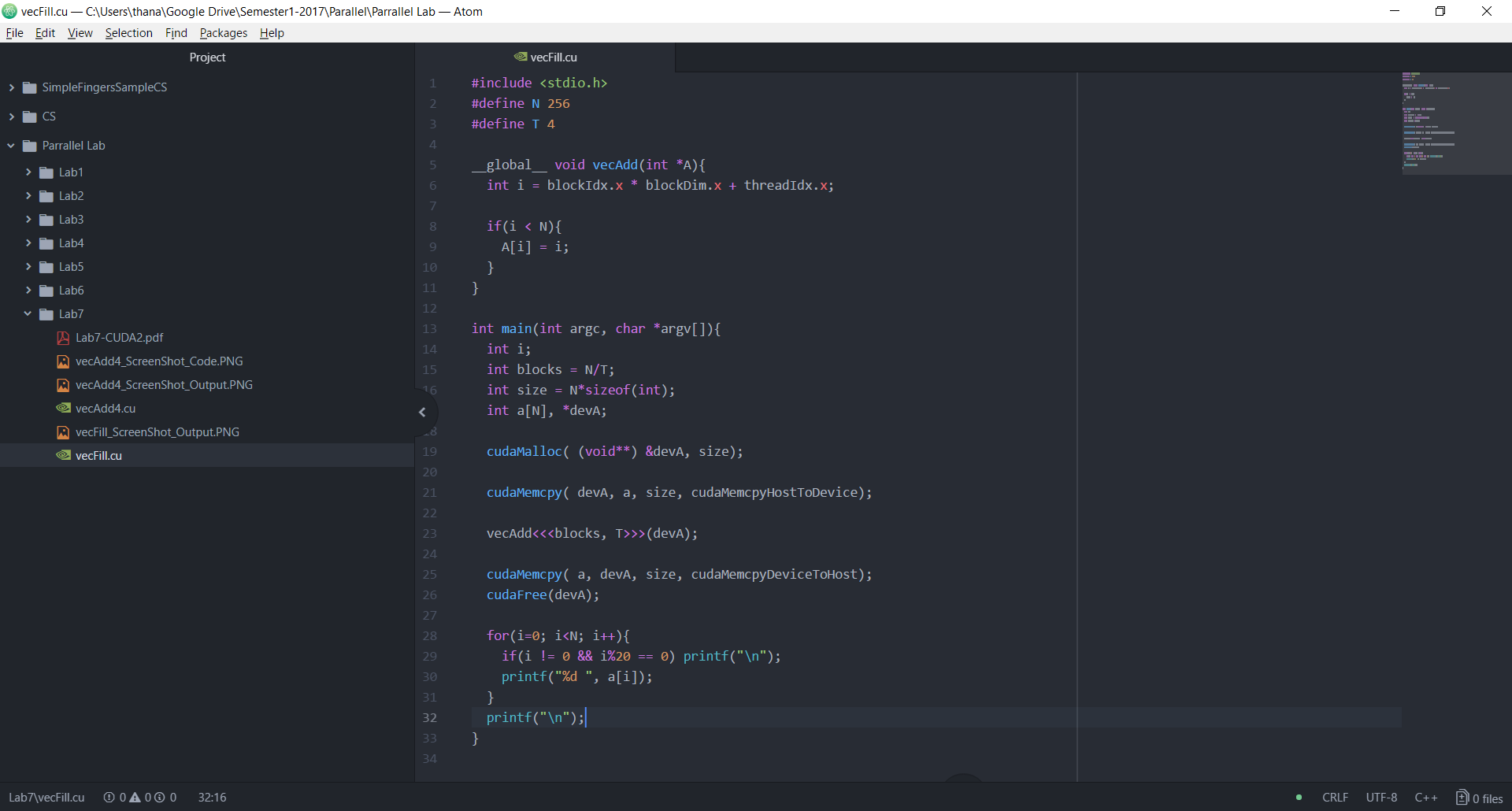
1. vecAdd4.cu
   * Source code



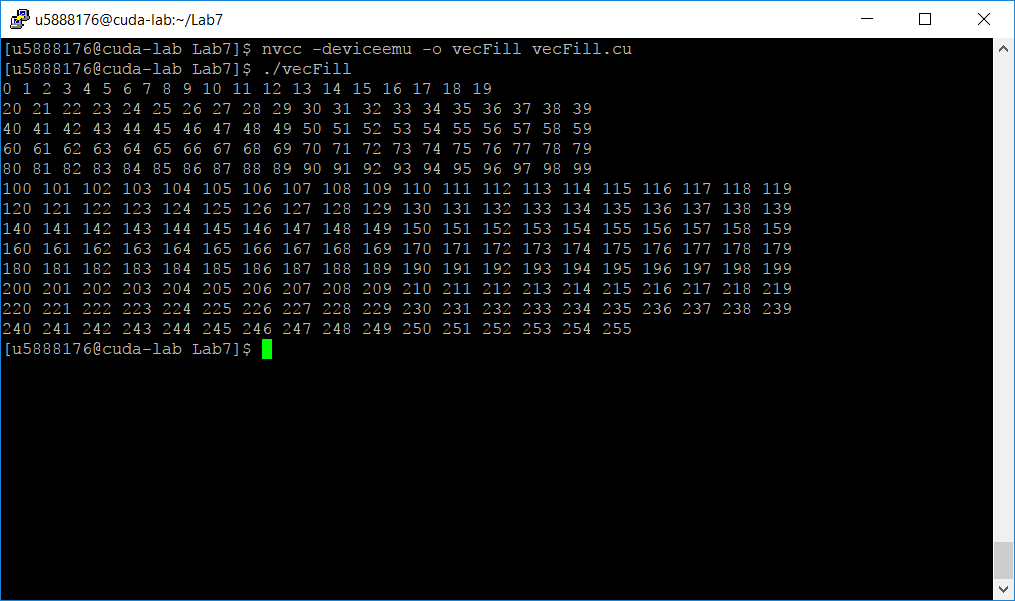
* Output



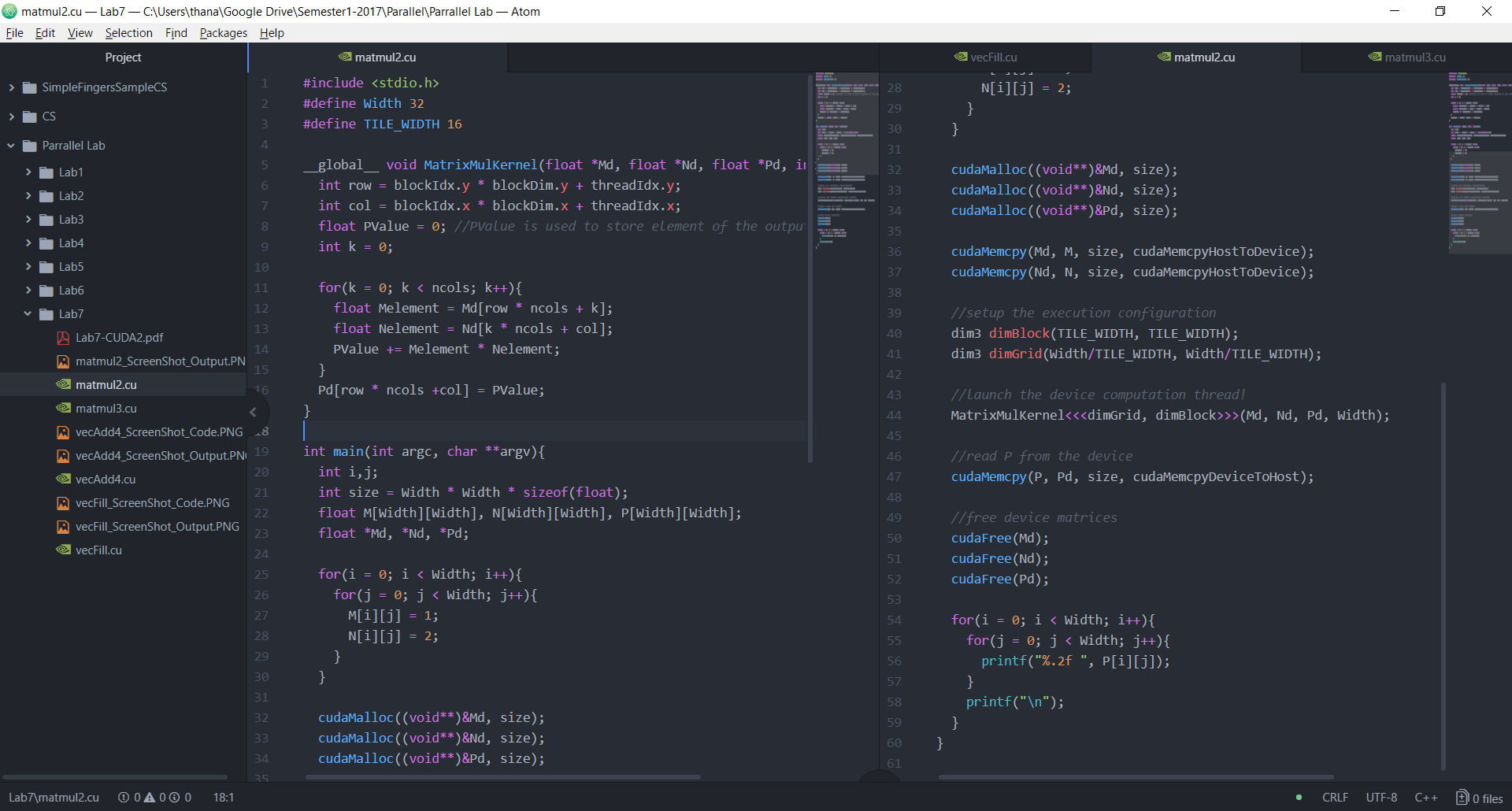
1. vecFill.cu
   * Source code



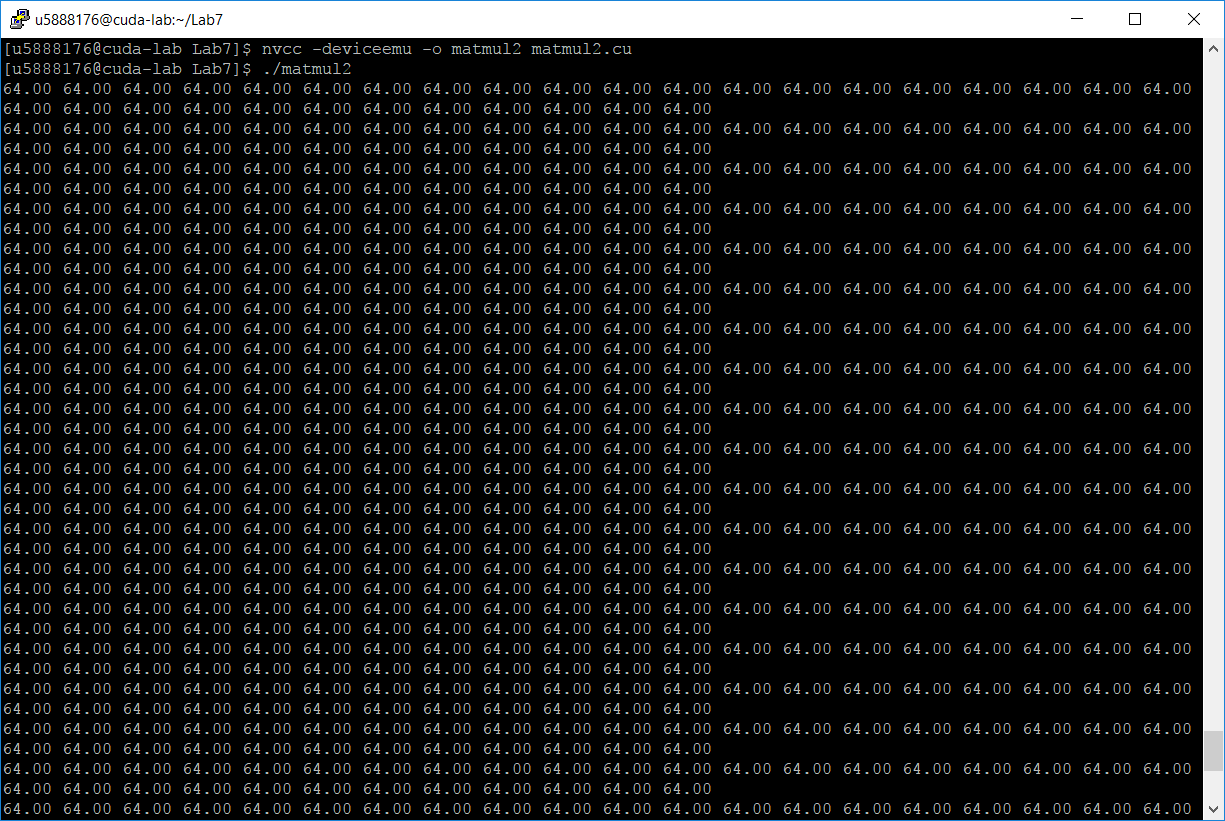
* Output



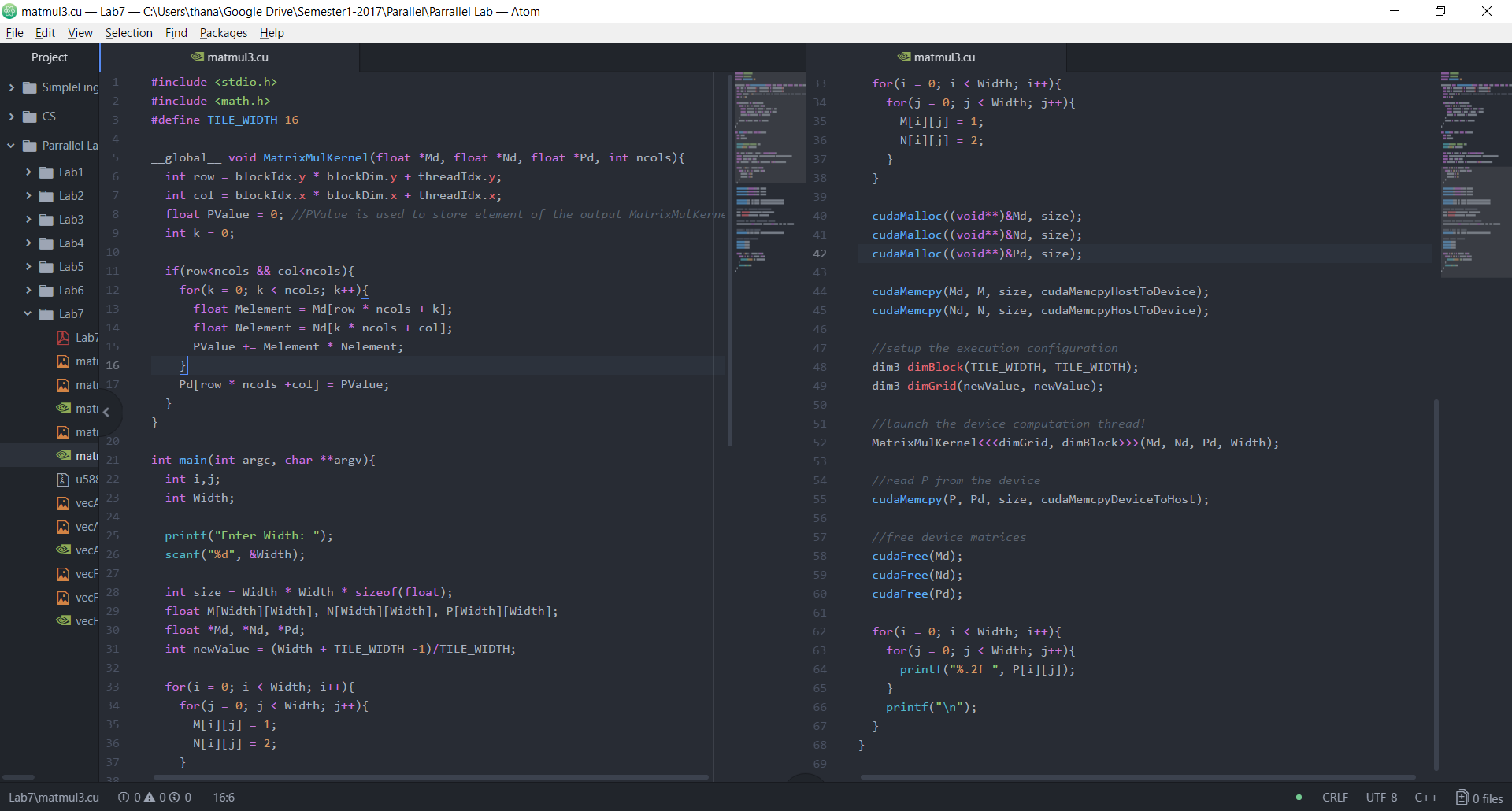
1. matmul2.cu
   * Source code



* Output



1. matmul3.cu
   * Source code



* + Output

