

Electrical Rules Check Report

Class	Document	Message
Error	5v_Reg_biggr.SchDoc	5Vd contains Output Pin and Power Pin objects (Pin U28-2, Pin U16-3, Pin U23-8).
Error	-5v_Reg_biggr.SchDoc	-5Vd contains Output Pin and Power Pin objects (Pin U29-3, Pin U23-4).
Error	7419_SUPPLY.SchDoc	GND contains Output Pin and Power Pin objects (Pin U26-13, Pin U26-13, Pin U1-16, Pin U1-31, Pin U1-38, Pin U1-51, Pin U1-61, Pin U1-83, Pin U1-94, Pin U1-107, Pin U1-120, Pin U1-130, Pin U2-6, Pin U2-12, Pin U2-14, Pin U2-26, Pin U2-30, Pin U2-53, Pin U2-56, Pin U2-57, Pin U2-58, Pin U2-59, Pin U2-60, Pin U2-61, Pin U2-62, Pin U2-63, Pin U2-64, Pin U2-65, Pin U2-66, Pin U2-67, Pin U10-6, Pin U10-6, Pin U10-10, Pin U10-10, Pin U10-12, Pin U10-12, Pin U10-16, Pin U10-16, Pin U10-18, Pin U10-18, Pin U10-20, Pin U10-20, Pin U10-25, Pin U10-25, Pin U10-44, Pin U10-44, Pin U10-56, Pin U10-56, Pin U10-57, Pin U10-57, Pin U16-1, Pin U25-2, Pin U25-3, Pin U25-7, Pin U25-9, Pin U26-6, Pin U26-6, Pin U26-16, Pin U26-16, Pin U28-3, Pin U29-1)
Error	mcu.SchDoc	Missing Negative Net for differential pair [USBDM], positive net [USBDM]
Error	mcu.SchDoc	Missing Negative Net for differential pair [USBDP], positive net [USBDP]
Error	top_level.SchDoc	Multiple top level documents: top_level.SchDoc has been used
Error	mcu.SchDoc	Net ALCC has only one pin (Pin U1-135)
Error	mcu.SchDoc	Net CHRG has only one pin (Pin U1-8)
Error	mcu.SchDoc	Net FAULT has only one pin (Pin U1-9)
Error	7419_SUPPLY.SchDoc	Net GND contains multiple Output Pins (Pin U26-13, Pin U26-13).
Error	7419_SUPPLY.SchDoc	Net NetC130_Negative DC-DC1_1 contains multiple Output Pins (Pin U26-1, Pin U26-2, Pin U26-10, Pin U26-26, Pin U26-27, Pin U26-28).
Error	7419_SUPPLY.SchDoc	Net NetC130_Negative DC-DC2_1 contains multiple Output Pins (Pin U26-1, Pin U26-2, Pin U26-10, Pin U26-26, Pin U26-27, Pin U26-28).
Error	mcu.SchDoc	Net NetU1_70 has only one pin (Pin U1-70)
Error	mcu.SchDoc	Net NetU1_134 has only one pin (Pin U1-134)
Error	high_pass.SchDoc	Net NetU5_HP2_1 has only one pin (Pin U5-1)
Error	high_pass.SchDoc	Net NetU5_HP_1 has only one pin (Pin U5-1)
Error	mcu.SchDoc	Net R1B has only one pin (Pin U1-36)
Error	mcu.SchDoc	Net R2B has only one pin (Pin U1-37)
Error	mcu.SchDoc	Net RUN has only one pin (Pin U1-7)
Error	mcu.SchDoc	Net SCL has only one pin (Pin U1-136)
Error	mcu.SchDoc	Net SDA has only one pin (Pin U1-140)
Error	voltage source.SchDoc	NetC71_OPA5481_2 contains Power Pin and Input Port objects (Pin U19_-4, Port V-).
Error	voltage source.SchDoc	NetC71_OPA5482_2 contains Power Pin and Input Port objects (Pin U19_-4, Port V-).
Error	1v8_Regulator.SchDoc	NetC91_1 contains Power Pin and Input Port objects (Pin U16-1, Port IN-, Port OUT-).
Error	1v8_Regulator.SchDoc	NetC91_2 contains Power Pin and Input Port objects (Pin U16-3, Port IN+).
Error	1v8_Regulator.SchDoc	NetC92_2 contains Power Pin and Input Port objects (Pin U16-2, Port OUT+).
Error	5v_Regulator.SchDoc	NetC94_2 contains Power Pin and Input Port objects (Pin U17-1, Port OUT-).
Error	-5v_Regulator.SchDoc	NetC95_1 contains Power Pin and Input Port objects (Pin U20-3, Port OUT-).
Error	7419_SUPPLY.SchDoc	NetC130_Negative DC-DC1_1 contains Output Pin and Power Pin objects (Pin U26-1, Pin U26-2, Pin U26-10, Pin U26-26, Pin U26-27, Pin U26-28, Pin U26-29).
Error	7419_SUPPLY.SchDoc	NetC130_Negative DC-DC2_1 contains Output Pin and Power Pin objects (Pin U26-1, Pin U26-2, Pin U26-10, Pin U26-26, Pin U26-27, Pin U26-28, Pin U26-29).
Error	24V_Regulator.SchDoc	NetC145_1 contains Power Pin and Input Port objects (Pin U27-2, Port OUT).
Error	24V_Regulator.SchDoc	NetC146_1 contains Power Pin and Input Port objects (Pin U27-3, Port IN).
Error	5v_Reg_biggr.SchDoc	NetC147_1 contains Output Pin and Input Port objects (Pin U28-2, Port OUT).
Error	-5v_Reg_biggr.SchDoc	NetC150_2 contains Output Pin and Input Port objects (Pin U29-3, Port OUT).
Error	top_level.SchDoc	Sheet-Entry IN+ not matched to Port at 9200mil,4000mil
Error	top_level.SchDoc	Sheet-Entry IN- not matched to Port at 9200mil,3600mil
Warning	buffer.SchDoc	Component U8 LT6202CS8#PBF has unused sub-part(s) (2).
Warning	low_pass.SchDoc	Duplicate pins in component Pin U6-4
Warning	low_pass.SchDoc	Duplicate pins in component Pin U6-8
Warning	5kHz_LO_LPF.SchDoc	Duplicate pins in component Pin U7_LO1_HPF-4
Warning	5kHz_LO_LPF.SchDoc	Duplicate pins in component Pin U7_LO1_HPF-8
Warning	5kHz_LO_LPF.SchDoc	Duplicate pins in component Pin U7_LO1_LPF-4
Warning	5kHz_LO_LPF.SchDoc	Duplicate pins in component Pin U7_LO1_LPF-8
Warning	5kHz_LO_LPF.SchDoc	Duplicate pins in component Pin U7_LO2_HPF-4
Warning	5kHz_LO_LPF.SchDoc	Duplicate pins in component Pin U7_LO2_HPF-8
Warning	600Hz_LO_LPF.SchDoc	Duplicate pins in component Pin U19-4
Warning	600Hz_LO_LPF.SchDoc	Duplicate pins in component Pin U19-8
Warning	5kHz_GEN_LPF.SchDoc	Duplicate pins in component Pin U23-4
Warning	5kHz_GEN_LPF.SchDoc	Duplicate pins in component Pin U23-8

Class	Document	Message
Object GND, Power		
Object GND, Sheet		
Entry1v8_Regulator-IN -(Input), Sheet Entry1v 8_Regulator-OUT-(Inp ut), Sheet Entry3v3_R egulator-IN-(Input), Sheet Entry		
3v3_Regulator-OUT (Input), Sheet EntryBat t_charger-IN-(Input), Sheet EntryPower_Input ut-OUT-(Input))		
Warning	top_level.SchDoc	Nets Wire IO_UPDATE_LO1 has multiple names (Net Label IO_UPDATE_LO1, Net Label IO_UPDATE_LO1, Sheet Entry LO1-LO_BUS.IO_UPDATE(Passive), Sheet Entry MCU-LO_BUS_B.IO_UPDATE(Passive))
Warning	top_level.SchDoc	Nets Wire IO_UPDATE_LO2 has multiple names (Net Label IO_UPDATE_LO2, Net Label IO_UPDATE_LO2, Sheet Entry LO2-LO_BUS.IO_UPDATE(Passive), Sheet Entry MCU-LO_BUS_A.IO_UPDATE(Passive))
Warning	top_level.SchDoc	Nets Wire NetC28_HP2_1 has multiple names (Sheet Entry HP2-IN(Input), Sheet Entry MIXER1-OUT(Output))
Warning	top_level.SchDoc	Nets Wire NetC28_HP_1 has multiple names (Sheet Entry HP-IN(Input), Sheet Entry INA-OUT(Output))
Warning	top_level.SchDoc	Nets Wire NetC38_2 has multiple names (Sheet Entry AMP1-IN(Input), Sheet Entry LP-OUT(Output))
Warning	top_level.SchDoc	Nets Wire NetC93_1 has multiple names (Sheet Entry 5v_Regulator-IN+(Input), Sheet Entry Small Relay 1-THRU-(Input))
Warning	top_level.SchDoc	Nets Wire NetC93_2 has multiple names (Sheet Entry 5v_Regulator-IN-(Input), Sheet Entry -5v_Regulator-IN+(Input))
Warning	top_level.SchDoc	Nets Wire NetC96_1 has multiple names (Sheet Entry -5v_Regulator-IN-(Input), Sheet Entry Small Relay 2-THRU+(Input))
Warning	top_level.SchDoc	Nets Wire NetC99_2 has multiple names (Sheet Entry BUFF-IN(Input), Sheet Entry LP2-OUT(Output))
Warning	top_level.SchDoc	Nets Wire NetC105_2 has multiple names (Sheet Entry LP50Driver-OUT(Output), Sheet Entry OPA5481-IN(Input))
Warning	top_level.SchDoc	Nets Wire NetC111_2 has multiple names (Sheet Entry LPHFDriver-OUT(Output), Sheet Entry OPA5482-IN(Input))
Warning	top_level.SchDoc	Nets Wire NetC117_LO1_HPF_2 has multiple names (Sheet Entry LO1_HPF-OUT(Output), Sheet Entry LO1_LPF-IN(Input))
Warning	top_level.SchDoc	Nets Wire NetC117_LO1_LPF_2 has multiple names (Sheet Entry LO1_LPF-OUT(Output), Sheet Entry MIXER1-REF(Input))
Warning	top_level.SchDoc	Nets Wire NetC117_LO2_HPF_2 has multiple names (Sheet Entry LO2_HPF-OUT(Output), Sheet Entry LO2_LPF-IN(Input))
Warning	top_level.SchDoc	Nets Wire NetC123_2 has multiple names (Sheet Entry LO2_LPF-OUT(Output), Sheet Entry MIXER2-REF(Input))
Warning	top_level.SchDoc	Nets Wire NetC130_Negative DC-DC1_2 has multiple names (Sheet Entry +24 Linear-IN(Input), Sheet Entry Negative DC-DC-IN(Input), Sheet Entry Negative DC-DC-IN(Input), Sheet Entry Power_Input-OUT+(Input))
Warning	top_level.SchDoc	Nets Wire NetJ5_07 has multiple names (Sheet Entry LO1_INA1-OUT(Output), Sheet Entry ZIF_Bridge-REF_D_2(Input))
Warning	top_level.SchDoc	Nets Wire NetJ5_08 has multiple names (Sheet Entry LO2_INA1-OUT(Output), Sheet Entry ZIF_Bridge-REF_D_1(Input))
Warning	top_level.SchDoc	Nets Wire NetJ5_09 has multiple names (Sheet Entry MCU-R4(Input), Sheet Entry ZIF_Bridge-Relay2_D(Input))
Warning	top_level.SchDoc	Nets Wire NetJ5_10 has multiple names (Sheet Entry MCU-R5(Input), Sheet Entry ZIF_Bridge-Relay1_D(Input))
Warning	top_level.SchDoc	Nets Wire NetJ9_01 has multiple names (Sheet Entry Small Relay 1-CTRL(Input), Sheet Entry ZIF_Bridge-Relay1(Input))
Warning	top_level.SchDoc	Nets Wire NetJ9_02 has multiple names (Sheet Entry Small Relay 2-CTRL(Input), Sheet Entry ZIF_Bridge-Relay2(Input))

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Warning	top_level.SchDoc	Nets Wire NetJ9_03 has multiple names (Sheet Entry LO2_HPF-IN(Input), Sheet Entry ZIF_Bridge-REF_1(Output))
Warning	top_level.SchDoc	Nets Wire NetJ9_04 has multiple names (Sheet Entry LO1_HPF-IN(Input), Sheet Entry ZIF_Bridge-REF_2(Output))
Warning	top_level.SchDoc	Nets Wire NetJ9_05 has multiple names (Sheet Entry ADC-BUSIY(Output), Sheet Entry ZIF_Bridge-SPI_IN_2.BUSIY(Passive))
Warning	top_level.SchDoc	Nets Wire NetJ9_06 has multiple names (Sheet Entry ADC-SCK(Output), Sheet Entry ZIF_Bridge-SPI_IN_2.SCK(Passive))
Warning	top_level.SchDoc	Nets Wire NetJ9_07 has multiple names (Sheet Entry ADC-SDO(Output), Sheet Entry ZIF_Bridge-SPI_IN_2.SDO(Passive))
Warning	top_level.SchDoc	Nets Wire NetJ9_08 has multiple names (Sheet Entry ADC-CONV(Output), Sheet Entry ZIF_Bridge-SPI_IN_2.CONV(Passive))
Warning	top_level.SchDoc	Nets Wire NetK1_Large Relay 1_2 has multiple names (Sheet Entry Large Relay 1-CTRL(Input), Sheet Entry MCU-R1(Input))
Warning	top_level.SchDoc	Nets Wire NetK1_Large Relay 2_2 has multiple names (Sheet Entry Large Relay 2-CTRL(Input), Sheet Entry MCU-R2(Input))
Warning	top_level.SchDoc	Nets Wire NetK1_Large Relay 3_2 has multiple names (Sheet Entry Large Relay 3-CTRL(Input), Sheet Entry MCU-R3(Input))
Warning	top_level.SchDoc	Nets Wire NetR1_1 has multiple names (Sheet Entry AMP2-OUT(Output), Sheet Entry LP2-IN(Input))
Warning	top_level.SchDoc	Nets Wire NetR2_1 has multiple names (Sheet Entry AMP1-OUT(Output), Sheet Entry MIXER2-IN(Input))
Warning	top_level.SchDoc	Nets Wire NetR2_2 has multiple names (Sheet Entry DC1-IN(Input), Sheet Entry MIXER2-OUT(Output))
Warning	top_level.SchDoc	Nets Wire NetR3_2 has multiple names (Sheet Entry AMP2-IN(Input), Sheet Entry DC1-OUT(Output))
Warning	top_level.SchDoc	Nets Wire NetR10_HP2_2 has multiple names (Sheet Entry HP2-OUT(Output), Sheet Entry LP-IN(Input))
Warning	top_level.SchDoc	Nets Wire NetR10_HP_2 has multiple names (Sheet Entry HP-OUT(Output), Sheet Entry MIXER1-IN(Input))
Warning	top_level.SchDoc	Nets Wire NetR17_1 has multiple names (Sheet Entry dcshift1-OUT(Output), Sheet Entry LP50Driver-IN(Input))
Warning	top_level.SchDoc	Nets Wire NetR17_2 has multiple names (Sheet Entry dcshift1-IN(Input), Sheet Entry LO2_INA2-OUT(Output))
Warning	top_level.SchDoc	Nets Wire NetR18_1 has multiple names (Sheet Entry dcshift2-OUT(Output), Sheet Entry LPHFDriver-IN(Input))
Warning	top_level.SchDoc	Nets Wire NetR18_2 has multiple names (Sheet Entry dcshift2-IN(Input), Sheet Entry LO1_INA2-OUT(Output))
Warning	top_level.SchDoc	Nets Wire NetR19_LO1_1 has multiple names (Sheet Entry LO1-CH0_p(Output), Sheet Entry LO1_INA2-IN+(Input))
Warning	top_level.SchDoc	Nets Wire NetR19_LO2_1 has multiple names (Sheet Entry LO2-CH0_p(Output), Sheet Entry LO2_INA2-IN+(Input))
Warning	top_level.SchDoc	Nets Wire NetR27_LO1_1 has multiple names (Sheet Entry LO1-CH0_n(Output), Sheet Entry LO1_INA2-IN-(Input))
Warning	top_level.SchDoc	Nets Wire NetR27_LO2_1 has multiple names (Sheet Entry LO2-CH0_n(Output), Sheet Entry LO2_INA2-IN-(Input))
Warning	top_level.SchDoc	Nets Wire NetR28_LO1_1 has multiple names (Sheet Entry LO1-CH1_p(Output), Sheet Entry LO1_INA1-IN+(Input))
Warning	top_level.SchDoc	Nets Wire NetR28_LO2_1 has multiple names (Sheet Entry LO2-CH1_p(Output), Sheet Entry LO2_INA1-IN+(Input))
Warning	top_level.SchDoc	Nets Wire NetR29_LO1_1 has multiple names (Sheet Entry LO1-CH1_n(Output), Sheet Entry LO1_INA1-IN-(Input))
Warning	top_level.SchDoc	Nets Wire NetR29_LO2_1 has multiple names (Sheet Entry LO2-CH1_n(Output), Sheet Entry LO2_INA1-IN-(Input))
Warning	top_level.SchDoc	Nets Wire NetR32_1 has multiple names (Sheet Entry Batt_charger-Bus.ALCC(Passive), Sheet Entry MCU-BattBus.ALCC(Passive))
Warning	top_level.SchDoc	Nets Wire NetR33_1 has multiple names (Sheet Entry Batt_charger-Bus.SDA(Passive), Sheet Entry MCU-BattBus.SDA(Passive))
Warning	top_level.SchDoc	Nets Wire NetR40_1 has multiple names (Sheet Entry ADC-IN(Input), Sheet Entry BUFF-OUT(Output))
Warning	top_level.SchDoc	Nets Wire NetR45_1 has multiple names (Sheet Entry Batt_charger-Bus.SCL(Passive), Sheet Entry MCU-BattBus.SCL(Passive))
Warning	top_level.SchDoc	Nets Wire P0_LO1 has multiple names (Net Label P0_LO1, Net Label P0_LO1, Sheet Entry

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Warning	top_level.SchDoc	LO1-LO_BUS.P0(Passive), Sheet Entry MCU-LO_BUS_B.P0(Passive)) Nets Wire P0_LO2 has multiple names (Net Label P0_LO2, Net Label P0_LO2, Sheet Entry LO2-LO_BUS.P0(Passive), Sheet Entry MCU-LO_BUS_A.P0(Passive))
Warning	top_level.SchDoc	Nets Wire P1_LO1 has multiple names (Net Label P1_LO1, Net Label P1_LO1, Sheet Entry LO1-LO_BUS.P1(Passive), Sheet Entry MCU-LO_BUS_B.P1(Passive))
Warning	top_level.SchDoc	Nets Wire P1_LO2 has multiple names (Net Label P1_LO2, Net Label P1_LO2, Sheet Entry LO2-LO_BUS.P1(Passive), Sheet Entry MCU-LO_BUS_A.P1(Passive))
Warning	top_level.SchDoc	Nets Wire P2_LO1 has multiple names (Net Label P2_LO1, Net Label P2_LO1, Sheet Entry LO1-LO_BUS.P2(Passive), Sheet Entry MCU-LO_BUS_B.P2(Passive))
Warning	top_level.SchDoc	Nets Wire P2_LO2 has multiple names (Net Label P2_LO2, Net Label P2_LO2, Sheet Entry LO2-LO_BUS.P2(Passive), Sheet Entry MCU-LO_BUS_A.P2(Passive))
Warning	top_level.SchDoc	Nets Wire P3_LO1 has multiple names (Net Label P3_LO1, Net Label P3_LO1, Sheet Entry LO1-LO_BUS.P3(Passive), Sheet Entry MCU-LO_BUS_B.P3(Passive))
Warning	top_level.SchDoc	Nets Wire P3_LO2 has multiple names (Net Label P3_LO2, Net Label P3_LO2, Sheet Entry LO2-LO_BUS.P3(Passive), Sheet Entry MCU-LO_BUS_A.P3(Passive))
Warning	top_level.SchDoc	Nets Wire RADIO_RX has multiple names (Net Label RADIO_RX, Net Label RADIO_RX, Net Label RADIO_RX, Net Label RADIO_RX, Sheet Entry MCU-RADIO_UART.1(Passive), Sheet Entry uBlox Radio-RADIO_UART.1(Passive))
Warning	top_level.SchDoc	Nets Wire RADIO_TX has multiple names (Net Label RADIO_TX, Net Label RADIO_TX, Net Label RADIO_TX, Net Label RADIO_TX, Sheet Entry MCU-RADIO_UART.2(Passive), Sheet Entry uBlox Radio-RADIO_UART.2(Passive))
Warning	top_level.SchDoc	Nets Wire REF has multiple names (Power Object REF, Power Object REF, Sheet Entry ADC_REF-OUT+(Input))
Warning	top_level.SchDoc	Nets Wire RESET_LO1 has multiple names (Net Label RESET_LO1, Net Label RESET_LO1, Sheet Entry LO1-LO_BUS.RESET(Passive), Sheet Entry MCU-LO_BUS_B.RESET(Passive))
Warning	top_level.SchDoc	Nets Wire RESET_LO2 has multiple names (Net Label RESET_LO2, Net Label RESET_LO2, Sheet Entry LO2-LO_BUS.RESET(Passive), Sheet Entry MCU-LO_BUS_A.RESET(Passive))
Warning	top_level.SchDoc	Nets Wire SCLK has multiple names (Net Label SCLK, Net Label SCLK, Net Label SCLK_LO1, Net Label SCLK_LO1, Net Label SCLK_LO2, Net Label SCLK_LO2, Sheet Entry LO1-LO_SPI.SCLK(Passive), Sheet Entry LO2-LO_SPI.SCLK(Passive), Sheet Entry MCU-LO_SPI.SCLK(Passive))
Warning	top_level.SchDoc	Nets Wire SDIO_0 has multiple names (Net Label SDIO_0, Net Label SDIO_0, Net Label SDIO_0_LO1, Net Label SDIO_0_LO1, Net Label SDIO_0_LO2, Net Label SDIO_0_LO2, Sheet Entry LO1-LO_SPI.SDIO_0(Passive), Sheet Entry LO2-LO_SPI.SDIO_0(Passive), Sheet Entry MCU-LO_SPI.SDIO_0(Passive))
Warning	top_level.SchDoc	Nets Wire SDIO_1_LO1 has multiple names (Net Label SDIO_1_LO1, Net Label SDIO_1_LO1, Sheet Entry LO1-LO_BUS.SDIO_1(Passive), Sheet Entry MCU-LO_BUS_B.SDIO_1(Passive))
Warning	top_level.SchDoc	Nets Wire SDIO_1_LO2 has multiple names (Net Label SDIO_1_LO2, Net Label SDIO_1_LO2, Sheet Entry LO2-LO_BUS.SDIO_1(Passive), Sheet Entry MCU-LO_BUS_A.SDIO_1(Passive))
Warning	top_level.SchDoc	Nets Wire SDIO_2 has multiple names (Net Label SDIO_2, Net Label SDIO_2, Net Label SDIO_2_LO1, Net Label SDIO_2_LO1, Net Label SDIO_2_LO2, Net Label SDIO_2_LO2, Sheet Entry LO1-LO_SPI.SDIO_2(Passive), Sheet Entry LO2-LO_SPI.SDIO_2(Passive), Sheet Entry MCU-LO_SPI.SDIO_2(Passive))
Warning	top_level.SchDoc	Nets Wire SDIO_3_LO1 has multiple names (Net Label SDIO_3_LO1, Net Label SDIO_3_LO1, Sheet Entry LO1-LO_BUS.SDIO_3(Passive), Sheet Entry MCU-LO_BUS_B.SDIO_3(Passive))
Warning	top_level.SchDoc	Nets Wire SDIO_3_LO2 has multiple names (Net Label SDIO_3_LO2, Net Label SDIO_3_LO2, Sheet Entry LO2-LO_BUS.SDIO_3(Passive), Sheet Entry MCU-LO_BUS_A.SDIO_3(Passive))
Warning	top_level.SchDoc	Nets Wire USART1_RX has multiple names (Net Label USART1_RX, Sheet Entry MCU-USART_MCU.RX(Passive))
Warning	top_level.SchDoc	Nets Wire USART1_TX has multiple names (Net Label USART1_TX, Sheet Entry MCU-USART_MCU.TX(Passive))
Warning	top_level.SchDoc	Nets Wire USBDM has multiple names (Net Label USBDM, Sheet Entry MCU-USB_MCU.DM(Passive))
Warning	top_level.SchDoc	Nets Wire USBDP has multiple names (Net Label USBDP, Sheet Entry MCU-USB_MCU.DP(Passive))
Warning	top_level.SchDoc	Nets Wire VBUS has multiple names (Net Label VBUS, Sheet Entry MCU-USB_MCU.VBUS(Passive))

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Warning	top_level.SchDoc	Nets Wire VIN has multiple names (Net Label VIN, Power Object 24V, Sheet Entry 3v3_Regulator-IN+(Input), Sheet Entry 5V Linear Digital-IN(Input), Sheet Entry Batt_charge-IN+(Input), Sheet Entry Large Relay 2-THRU+(Input))
Warning	mcu.SchDoc	NetU1_44 contains IO Pin and Input Port objects (Pin U1-44, Port R1).
Warning	mcu.SchDoc	NetU1_45 contains IO Pin and Input Port objects (Pin U1-45, Port R5).
Warning	mcu.SchDoc	NetU1_96 contains IO Pin and Input Port objects (Pin U1-96, Port R3).
Warning	mcu.SchDoc	NetU1_97 contains IO Pin and Input Port objects (Pin U1-97, Port R4).
Warning	mcu.SchDoc	NetU1_98 contains IO Pin and Input Port objects (Pin U1-98, Port R2).
Warning	mixer.SchDoc	Sheet contains duplicate ports Port REF at 3290mil,3630mil and 3690mil,2230mil
Warning	top_level.SchDoc	Unconnected Sheet Entry MCU-BattBus(Input) at 12550mil,4300mil