

GOVERNMENT COLLEGE OF ENGINEERING KANNUR
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
FIFTH SEMESTER BTECH ELECTRONICS AND COMMUNICATION ENGINEERING (2021 ADMISSION)
S5 BTECH ECE PRACTICAL END SEMESTER EXAMINATION SCHEDULE

S5 ECE A (54 Regular) + (1 Supplementary)

LAB	ECL331 ANALOG INTEGRATED CIRCUITS AND SIMULATION LAB		
DATE & TIME	9.00 AM - 12.00 PM	11.00 AM - 2.00 PM	12.30 PM - 3.30 PM
13-12-2023 (WEDNESDAY)	1-7 (7 Nos)	8-14 (7 Nos)	15-20 (6 Nos)
14-12-2023 (THURSDAY)	21 -27 (7 Nos)	28-34 (7 Nos)	35-39, KNR19EC075 (6 Nos)
15-12-2023 (FRIDAY)			
18-12-2023 (MONDAY)	40-47 (8 Nos)	48 -54 (7 Nos)	

S5 ECE A (54 Regular) + (2 Supplementary)

LAB	ECL333 DIGITAL SIGNAL PROCESSING LAB		
DATE & TIME	9.00 AM - 12.00 PM	12.00 PM - 3.00 PM	
12-12-2023 (TUESDAY)	21-30 (10 Nos)	31-39, KNR19EC075 (10 Nos)	
13-12-2023 (WEDNESDAY)	40-49 (10 Nos)	50 -54, KNR20EC029 (6 Nos)	
14-12-2023 (THURSDAY)			
15-12-2023 (FRIDAY)	1-10 (10 Nos)	11-20 (10 Nos)	