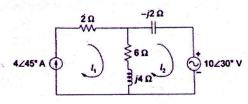


Course Code: ECT205
Course Name: NETWORK THEORY

	Course Name: NET WORK THEORY	
Max.	Marks: 100 Duration	: 3 Hour
	PART A	
<b>&amp;</b>	Answer all questions. Each question carries 3 marks	Marks
1	Explain super mesh analysis	(3)
2	Differentiate ideal and practical voltage sources.	(3)
3	State Reciprocity theorem	(3)
4	What is the significance of Superposition theorem?	(3)
5	State initial value and final value theorem	(3)
6	Find expression for current when an unit impulse is given to a series RC circuit.	(3)
7	Is $\alpha_{12} = \frac{2s^2 + 5s + 1}{s + 7}$ a valid function? Justify.	(3)
8	What do you mean by open circuit natural frequency and short circuit natural frequency?	(3)
9	What are image parameters?	(3)
10	The impedance parameters of a two-port network are $\begin{bmatrix} 6 & 3 \\ 3 & 4 \end{bmatrix}$ . Find its admittance	(3)
	parameters.	
	PART B	
	Answer any one full question from each module. Each question carries 14 marks	
	Module 1	
11	a) Find I in the network shown using nodal analysis	(7)
	4A	
	$\begin{array}{c c} \hline 2\Omega & 2 \\ \hline 2\Omega & 8\Omega & 7A \end{array}$	
	b) Find voltage across 6Ω resistor using mesh analysis	

(7)

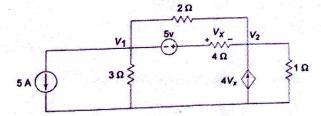
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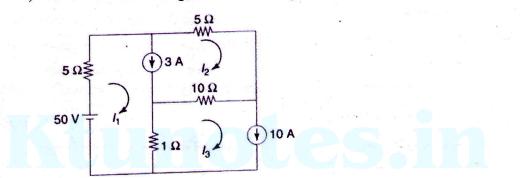
12 a) Find voltage across  $4\Omega$  resistor using nodal analysis



**(7)** 



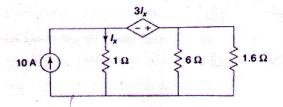
b) Determine current through 100 resistor using mesh analysis



Module 2

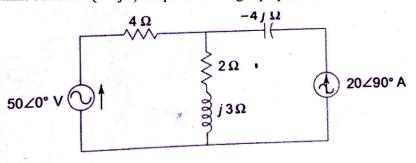
a) Find current through 1.6Ω resistor using Thevenin's Theorem

(7)



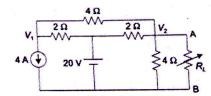
b) Determine current in  $(2 + j3)\Omega$  impedance using superposition theorem

(7)



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a) Find value of R<sub>L</sub> for maximum power transfer. Also find the maximum power transferred. (7)

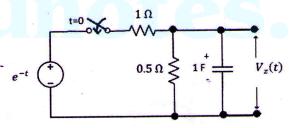


b) Determine current through 40 resistor using superposition theorem.

(7)

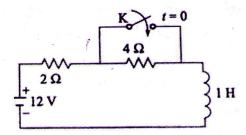
## Module 3

15 a) In the circuit, the switch is closed at t=0, connecting a source  $e^{-t}$  to the RC circuit. At time t=0, it is observed that capacitor voltage has the value  $V_c(0)=0.5V$ . For the element values given, determine  $V_z(t)$  after converting the circuit into transformed domain.



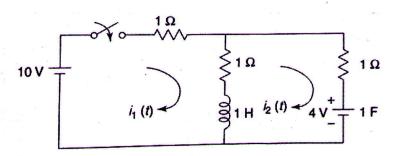
b) Determine current flowing through the circuit shown for  $t \ge 0$ 

(6)



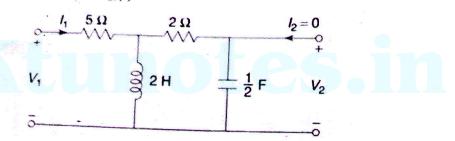
- a) Find the expression for current through a series RL circuit when a pulse input of width T and amplitude A is applied across it
  - b) For the circuit shown switch is closed at t = 0. Find currents  $i_1(t)$  and  $i_2(t)$  if initial current through inductor is zero and initial voltage on capacitor is 4V

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#### Module 4

- a) Obtain the time domain response of the given function using pole zero diagram  $V(s) = \frac{(s+2)(s+6)}{(s+1)(s+5)}$  (8)
  - b) Explain the significance of poles and zeros with reference to driving point (6) functions and transfer functions.
- a) What are the necessary conditions for transfer function? (6)
  - b) Determine driving point impedance Z<sub>11</sub>(s), transfer impedance Z<sub>21</sub>(s) and voltage transfer ratio G<sub>21</sub>(s) for the network shown



#### Module 5

- 19 a) Derive the conditions for reciprocity and symmetry for Z parameters and for (8) ABCD parameters.
  - b) Express g parameters in terms of h parameters and T parameters. (6)
- 20 a) Show that when two 2 port networks are connected in parallel, the resultant Y (6) matrix is the sum of Y matrices of each individual network.
  - b) Obtain short circuit admittance parameters of the circuit shown.

