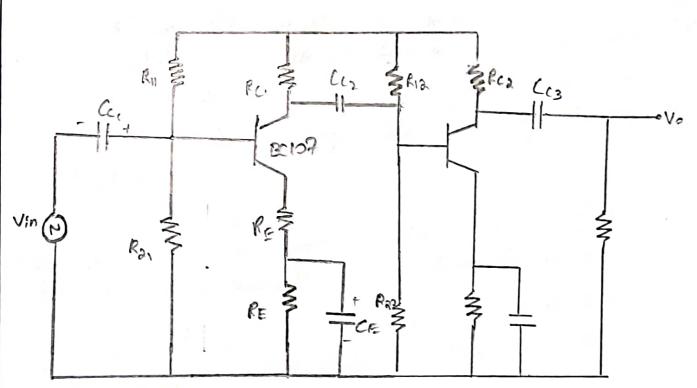
ANALOG CIRCUITS ASSIGNMENT

Submitted By
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ECE S4 -B
Roll No! 50

Ans. Multistage ampliliers over used for carading to emprove parameters: Suchas voltage/current gain, emput/output resistance.

A duo stage amplifier provides an overall voltage gain of An and An which are the of livet and second respectively.

Sence each stage provides a phase reversal. The input and



The Input of second stage Is in parallel with R(, of the livst stage is

. A1 = Rc, 11, R12

output signal will be in phase.

where Ria is the Enput resistance of the second stage

Ria = Ria || Raa || (1+he) re

hle = 100

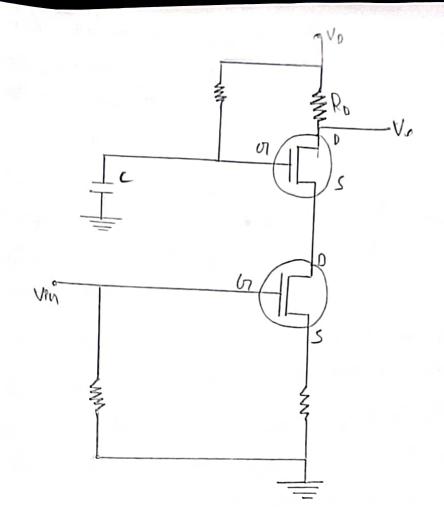
AL = RCallRL

Core must be taken while selecting Ax and A, . If A, In the input ato the second stage will be one too high The gain of the livet stage can be controlled by a negetive leedback 91 series with emitter, this is achieved by un bypassed sesis hor Re.

Ellects of analyse & that, single stage amplifies &s not able to provide enough power and bull bit all the requigrement of an ideal amplifier, so multistage amplifier es segui ired. In multistage amplifier, a number of single amplifiers are Connected in cascade arrangement 1e, output of first stage is connected in cht to signit of second stage, hence quereasing voltage and Current grin.

do With Circuit schematic explain the working of Cascade amplisier. How wide bandwidth is obtained in Carcade amplisier?

A conside amplifier i) a double staged circuit witha buller amplifies that bollows the trans conductance amp. litien



It an amplifier comprises of BOT, then the input stage is C-E configuration that leads to the commen base at which to output is collected. This type of amphilions is knowned cascade amplifiers.

The Emput signal is applied at the terminal gade at initial The Emput signal is applied at the terminal gade at initial Stage. The second stage that is output stage; con liguration, Commthe output of the Endividual stage; con liguration, Common the output is collected from the drain ferminal on davin. The line output is collected from the drain ferminal at which the resistor Ro is connected. The gate of the FET at the second stage is grounded. So the values of the source to voltage of the second stage FET has remained equivalent to the drain voltage of the sirst stage FET.

FET at the Econd stage often a Low resistance path. The gain of the FET as the broad page. is reduced

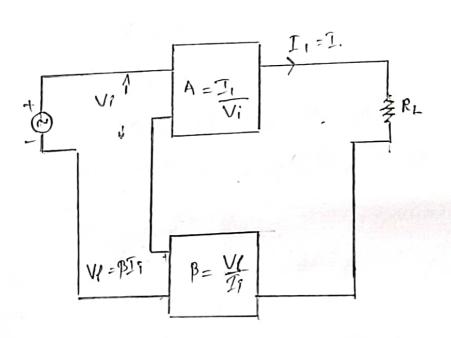
which prodirectly reduces miller effect, Hence Increase band will. The overall gain does not get affected become it is compensated. By the FET in second stage.

birst stage FET, gate and the source terminals of second stage FET is almost constant. Hence there is nothing for lead trade. These leads to Prolation and the condition is called miller effect.

High Bandwidth: A carcade amplifier we 2 mismatched stage of the stage beed. No regative of the stage beed. No regative headback technique is useful at high drequencies. Next become of CB stage the miller capacitance of transistor is minimised by himiting voltage gain of amplifier to 2. Avoiding the miller apacitance because of himiting voltage gain of amplifier to 2. Avoiding the miller apacitance is bountage.

30) Desire expression los veltage again, Expet impedence and output impedance of current series and current shunt amplifies with her-desire.

i) Current series leed back:



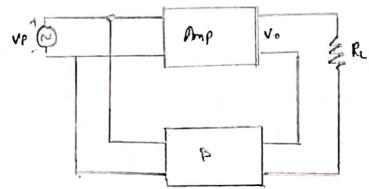
P) Singut impedence of consent series leed back

Rif =
$$\frac{Vs}{T_i}$$

We know $Vs = Vi + Vl$
 $\Rightarrow Ril = \frac{Vi + Vl}{T_i}$
 $Vl = BVo \Rightarrow Ril = \frac{Vi + BVo}{T_i} = \frac{Vi}{T_i} \left(1 + \frac{BVo}{V_i}\right)$
 $\frac{Vo}{Vi} = A \Rightarrow Ril = (\rho A + 1) \frac{Vi}{T_i}$
 $\frac{V1}{I} = R_i \Rightarrow Ril = R_i (1 + |PAA)$
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0 . - (1+ AP) Ro

?) (went shout decabach:



iii) Charend Shunt leed back!

$$= Ri1 = \frac{Vi}{Ii + 13I_o} = \frac{Vi}{Ii \left(1 + \frac{13I_o}{Ii}\right)}$$

Drain =
$$\frac{I_0}{I_1}$$
 = $A =$ Rif = $\frac{Vi}{I_i(1+\beta A)}$

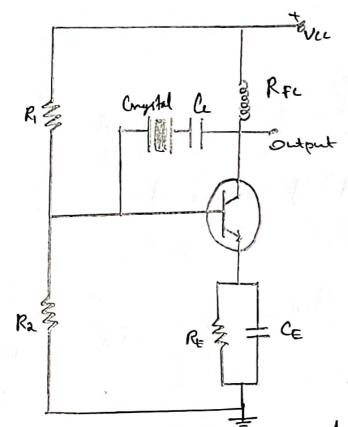
* orain with leed back low oursent shut Al = to/I,

O Delin piezo electric effect, with circuit relevatir explain the working principle of crystal bouillator

piezoelectric ellect: piezo electric ellect i, the ability ob certain material to generate on electric charge in suspon se to applied stren.

When peisselectric crystal is placed under memanion 55 a shelow of positive and negative charge centre in the material takes places, which results in an external bid when newersel, an outer electrical lield elther strechess on compresses the piezo electric material.

Crystal oxillator: --



- Cryfal oscillator 1) basically a tuned circuit using piezo elect Ic crystal on the sesonant tank circuit.

These oscillator are used where as higher largency stability

& required such as in communication transmitters and recieves.

- piezo electric crystal exhibit prezo electric property:

the ability to transform mechanical debormation into eletited charge , and vise versa

- Il a prezo eletre material 13 squeezed it develop es a voltage and il a voltage is applied accross

-Crystal oscillator operate on the principle of soverse piezo electric effect, in which alternating voltage 11 applied causing. It to vibrate at its natural brequence.

- It's there vibration which eventually get converted into oscillation,

- There oscillators are usually make of quartz crystall.