TRANSISTOR EQUIVALENT CIRCUIT MODEL

TOTAL RESPONSE OF AN AMPLIFIER:

DC RESPONSE + AC RESPONSE

AC ANALYSIS

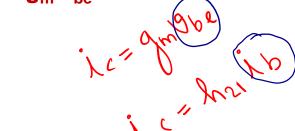
- SMALL SIGNAL ANALYSIS
- LARGE SIGNAL ANALYSIS
- What is an Equivalent Model??
- An equivalent model is the combination of circuit elements properly chosen to represent the actual behavior of the device under specific operating point
- Why Equivalent model?
- Cannot apply network theorems directly on practical devices. So need equivalent model to use this theorems to find out different network parameters.

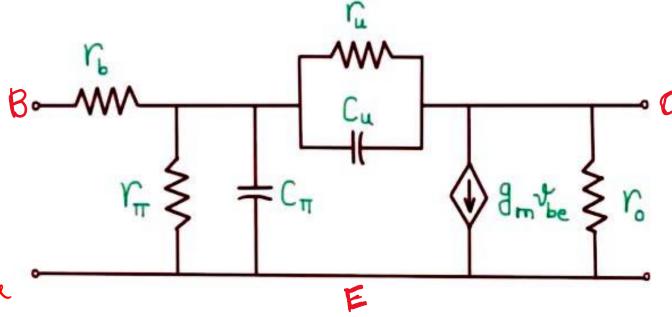
TRANSISTOR MODELS

- Hybrid Model
- re Model
- Hybrid Pi Model

- C_u Transition capacitance between base and collector. Early effect representation
- C_{TT} Diffusion Capacitance. Minority carriers stored in Base region
- r_π small input resistance between base and emitter seen from base
- ru collector base reverse resistance (large value)
- **r**_b base terminal resistance (small value)
- emitter resistance

 compared to the control of the
- **g**_m **v**_{be} current source

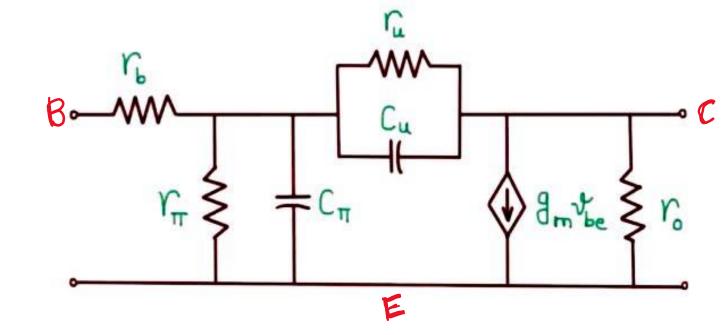




VOLTAGE CONTROLLED CURRENT SOURCE CURRENT CONTROLLED CURRENT SOURCE

DC conditions of a transistor

If an input signal v_{be} also applied to the base



$$e^{\chi} = 1 + \chi + \chi^2$$

$$= 1! + \chi^2$$

$$= 1! + 2!$$

$$= 1 + 2!$$

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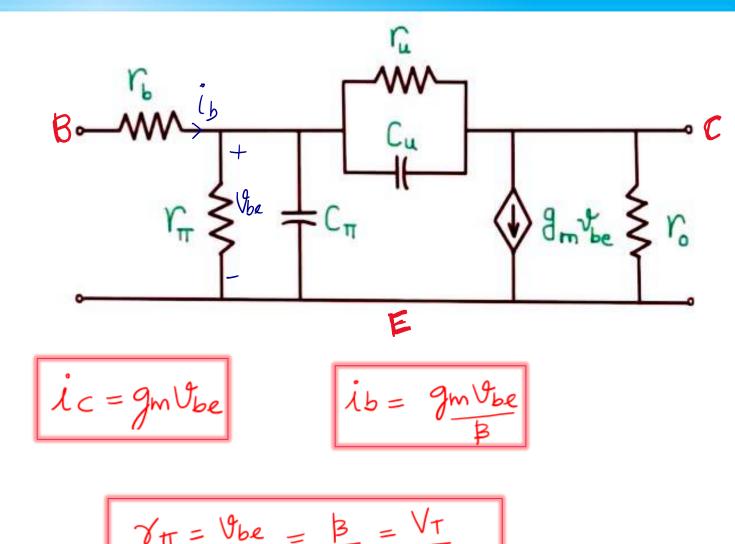
$$= 1 + 2!$$

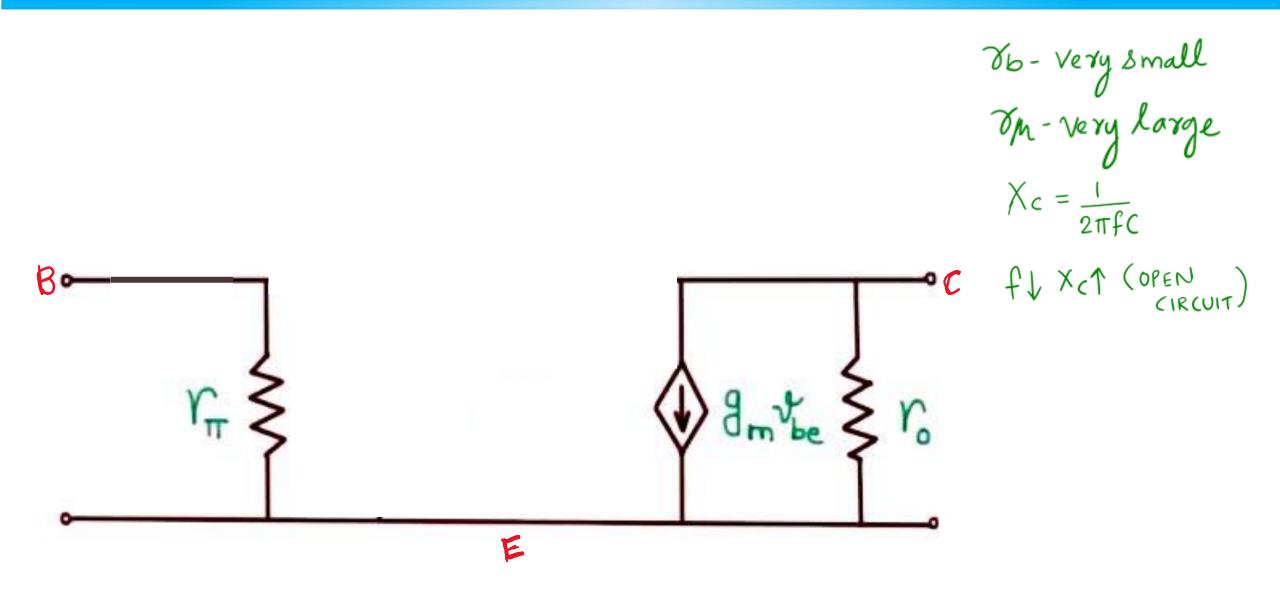
$$= 1 + 2!$$

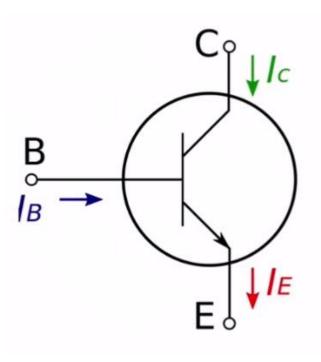
$$= 1 + 2!$$

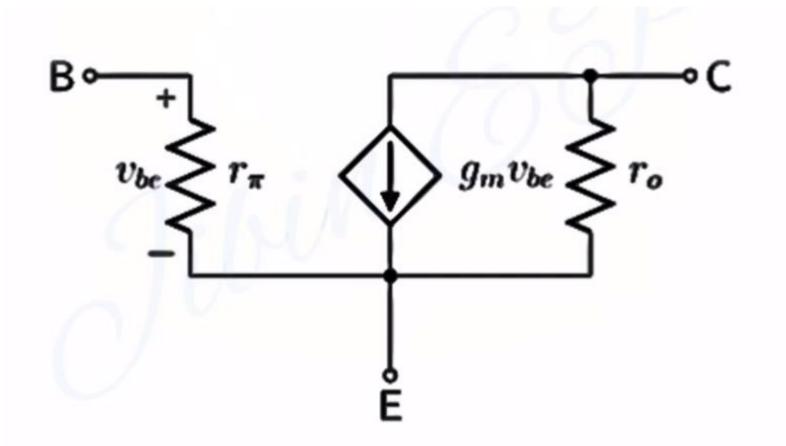
$$ic = Ic \cdot 9be$$
 VT

TRANSCONDUCTANCE, $gm = Ic$
 $ic = gm \cdot 9be$
 $ib = ic \Rightarrow ib = gm \cdot 9be$
 $b = ic \Rightarrow ib = gm \cdot 9be$
 $b = ic \Rightarrow fib = gm$
 $fm = Ic \Rightarrow fib = gm$
 $fm = Ic \Rightarrow fib = gm$







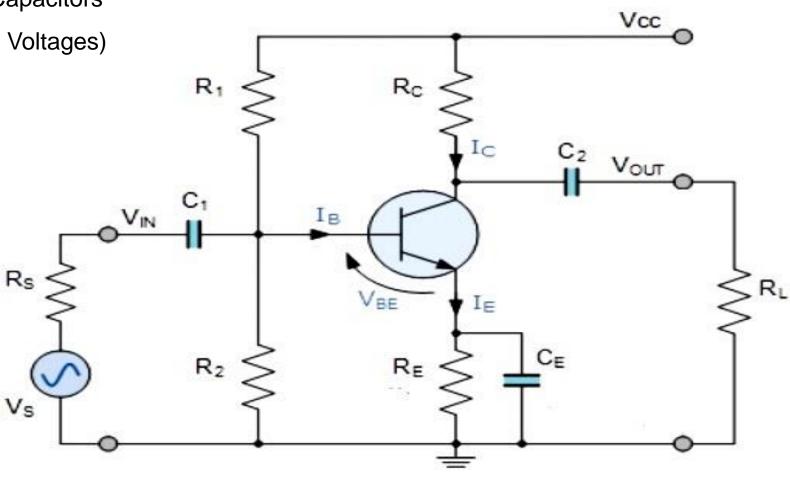


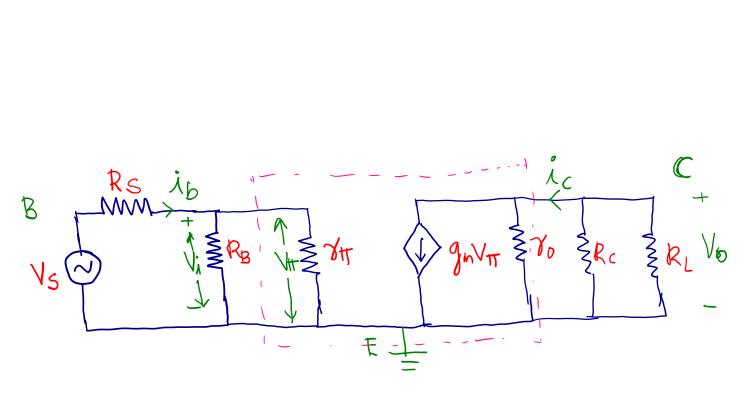
CE AMPLIFIER WITH BYPASS CAPACITOR

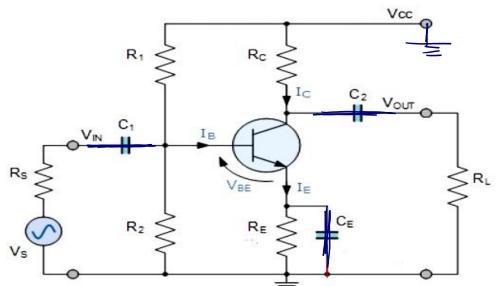
STEPS TO CONVERT TO HYBRID π MODEL

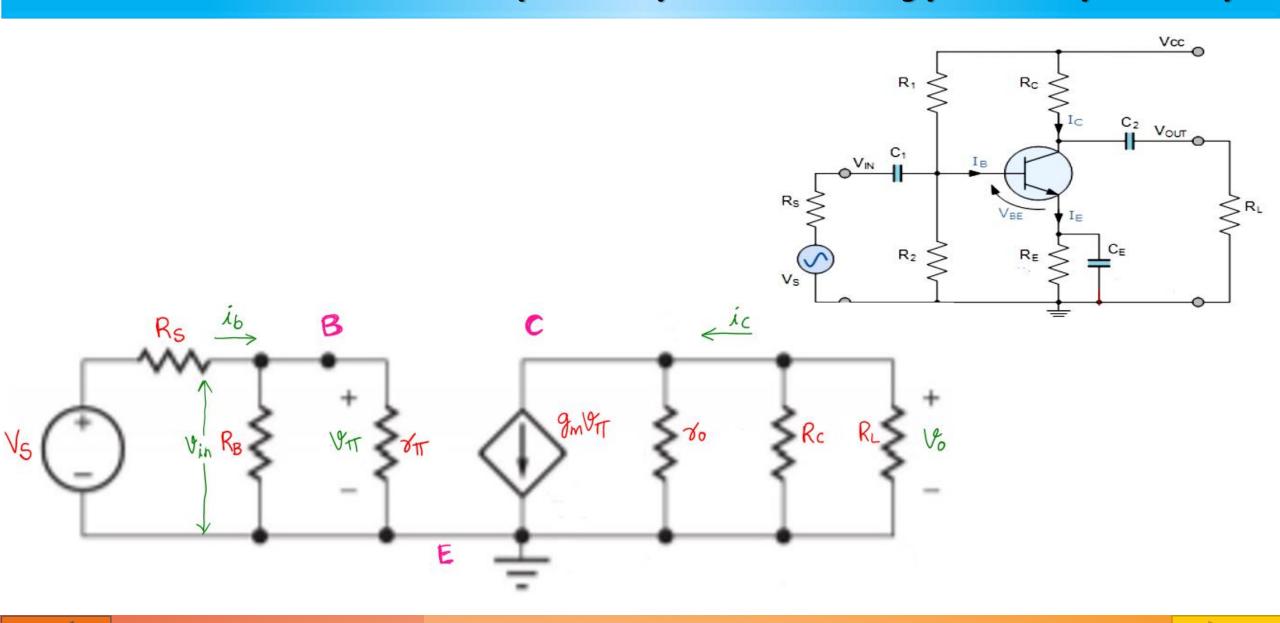
Short circuit all Bypass and Coupling Capacitors

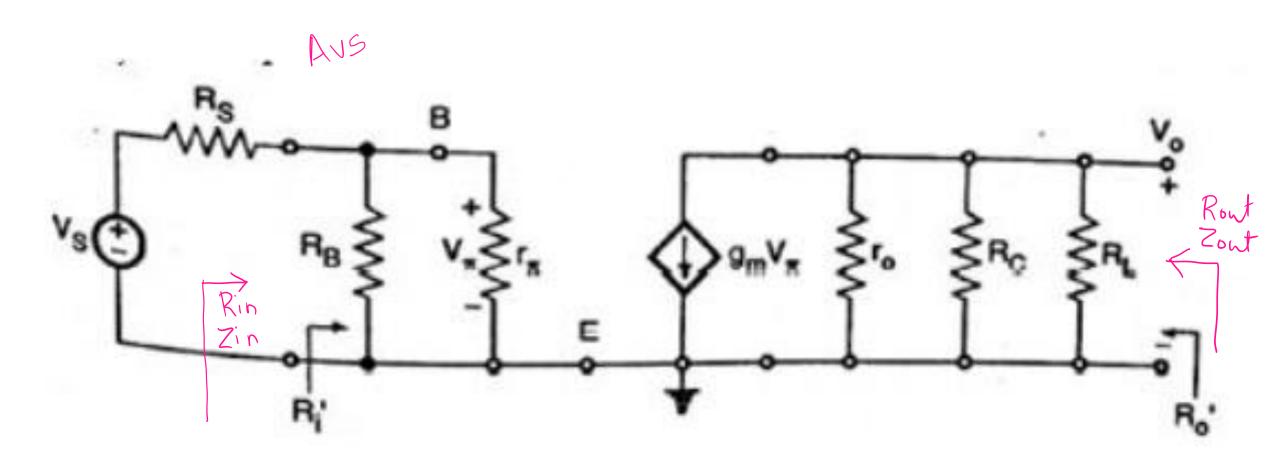
Remove all external power supply (DC Voltages)
 and connect that node to ground







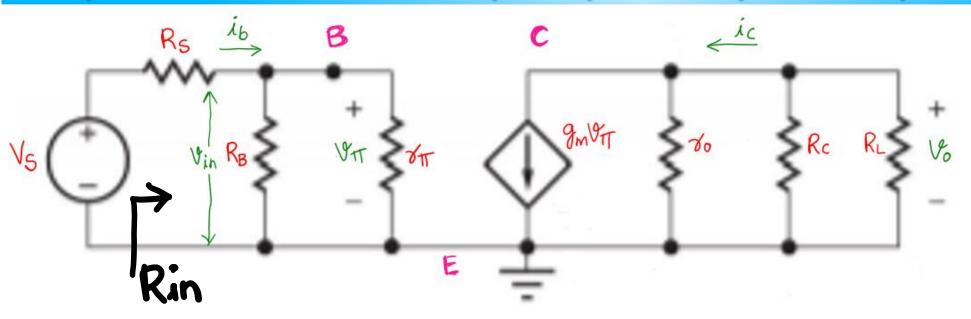




PARAMETERS TO DETERMINE

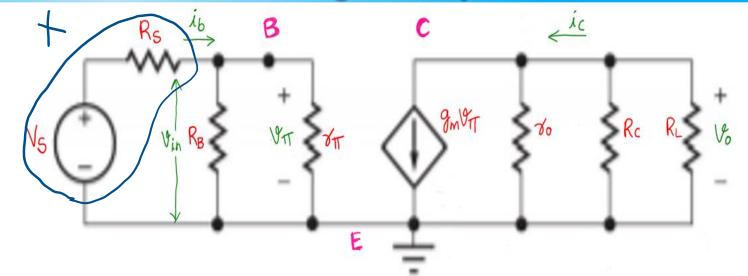
- INPUT RESISTANCE (R_{in})
- OUTPUT RESISTANCE (R_{OUT})
- VOLTAGE GAIN WITHOUT $V_S(A_V)$
- VOLTAGE GAIN WITH V_S (A_{VS})
- CURRENT GAIN (A_i)

Input Resistance (Rin) or Input Impedance (Zin)



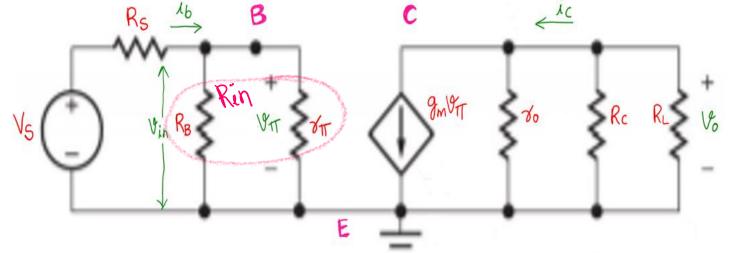
Voltage Gain (without V_s) (A_V)

$$A_V = \frac{Vout}{Vin} = \frac{iout \times Rout}{Vin}$$



Voltage Gain (with V_s) (A_{vs})

$$A_{VS} = \frac{9 \text{ out}}{VS}$$
 $R_B || V_T \cong R_{in}$



APPLYING VOLTAGE DIVIDER RULE AT INPUT SIDE

$$\frac{\text{Vin} = \frac{\text{Vs} \cdot \text{Rin}}{\text{Rin} + \text{Rs}}}{\frac{\text{Vin}}{\text{Rin} + \text{Rs}}} = \frac{\frac{\text{Rin}}{\text{Rin} + \text{Rs}}}{\frac{\text{Vin}}{\text{Rin} + \text{Rs}}}$$

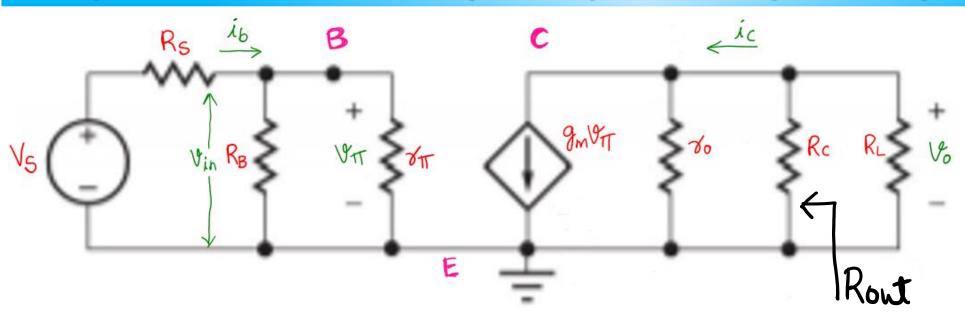
$$\frac{V_{in}}{V_5} = \frac{R_{in}}{R_{in} + R_5}$$

$$Av_5 = Av\left(\frac{Rin}{Rin + Rs}\right)$$

$$AV_{5} = \frac{V_{0}ut}{V_{5}} = \frac{V_{0}ut}{V_{5}} \times \frac{V_{in}}{V_{in}} = \frac{V_{0}ut}{V_{in}} \times \frac{V_{in}}{V_{5}}$$

$$AV_{5} = -gm(Rc||R_{c}||V_{0}) \times \frac{R_{in}}{R_{in}+R_{5}}$$

Output Resistance (Rout) or Output Impedance (Zout)

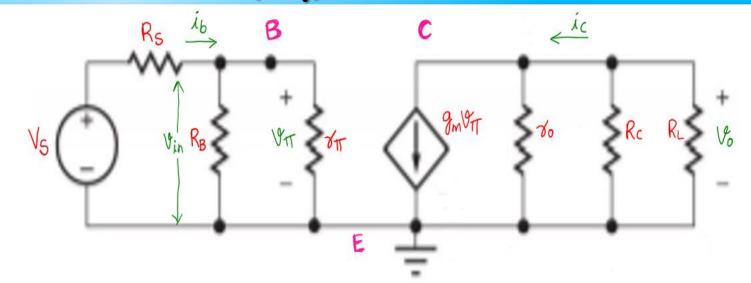


Current Gain (A_i)

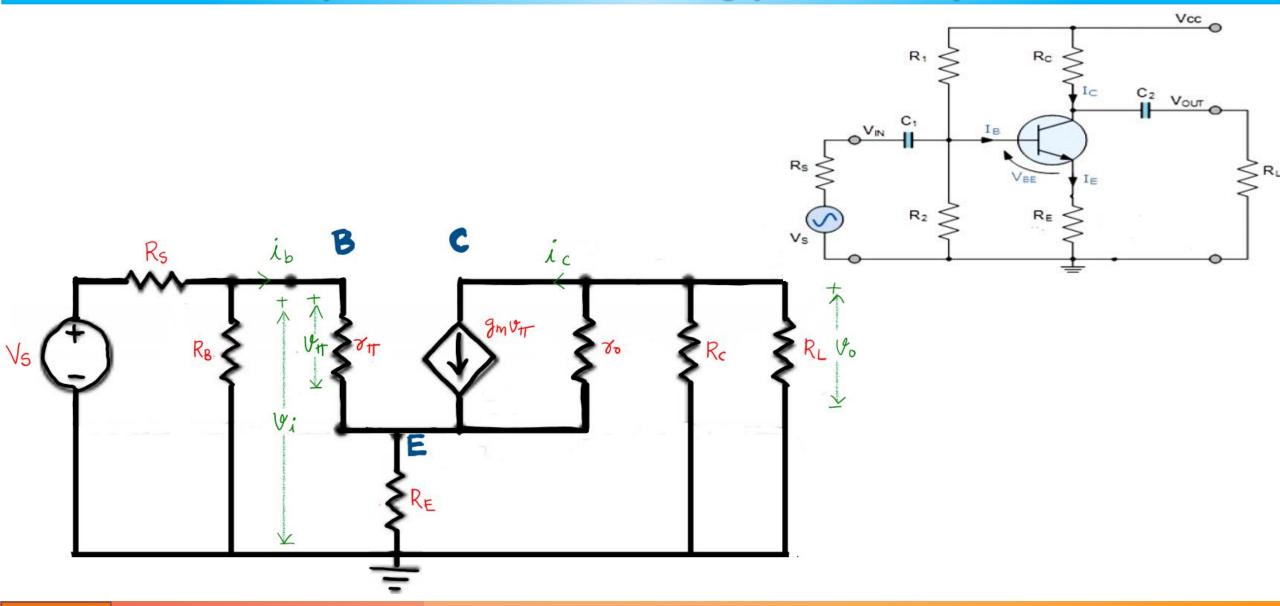
$$Ai = \frac{i\omega t}{iin}$$

 $iout = Vout/RL$
 $Rin \cong RB 117TT$

$$Ai = \frac{iout}{iin} = \frac{Vout/RL}{Vin/Rin} = \frac{Vout}{Vin} \times \frac{Rin}{RL}$$



CE amplifier without bypass capacitor



CE amplifier without bypass capacitor

PARAMETERS TO DETERMINE

- INPUT RESISTANCE (R_{in})
- VOLTAGE GAIN WITHOUT V_S (A_V)
- VOLTAGE GAIN WITH V_S (A_{VS})
- CURRENT GAIN (A_i)

Input Resistance (Rin) or Input Impedance (Zin)

Rin = RBIIRi' Ri' =
$$\frac{y_{in}}{ib}$$

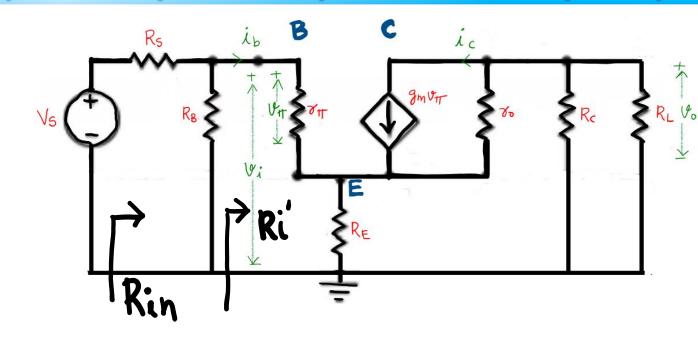
Uin = $ib \pi \pi + (ib + gm 9\pi) RE$

Uin = $ib (\pi \pi + (1 + gm 9\pi) RE)$

Vin = $ib (\pi \pi + (1 + gm 9\pi) RE)$

Ri' = $\frac{y_{in}}{ib} = \frac{y_{in}}{ib} = \frac{y_{in}}$

$$Rin = RBII (9\pi + (1+gm9\pi)RE)$$



Voltage Gain without Vs (Av)

$$A_{V} = \frac{V_{out}}{V_{in}} \qquad V_{out} = i_{out} \times R_{out}$$

$$NEGLECTING r_{o} term$$

$$V_{out} = (-g_{m}V_{TT}) \cdot (R_{c}||R_{L})$$

$$V_{TI} = i_{b}Q_{TT} \qquad V_{in} = R_{i}! \cdot i_{b}$$

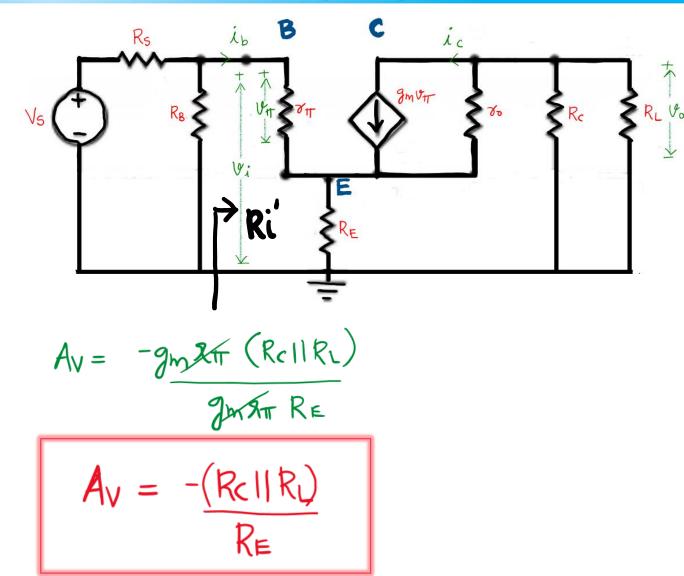
$$A_{V} = (-g_{m}i_{b}Q_{TT}) \cdot (R_{c}||R_{L})$$

$$R_{i}! = R_{E}(1+|3|) + Q_{TI} = R_{E} + |Q_{TE}| + Q_{TE}$$

$$R_{i}! = Q_{m}Q_{TE}$$

$$R_{i}! = Q_{m}Q_{TE}$$

$$R_{i}! = Q_{m}Q_{TE}$$



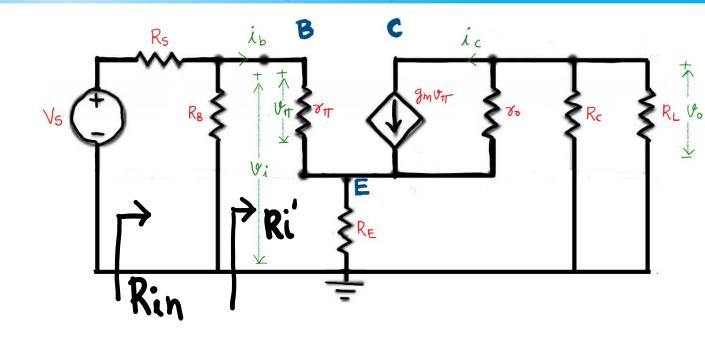
Voltage Gain with Vs (Avs)

$$A_{V_5} = \frac{90 \, \text{ut}}{95}$$

APPLYING VOLTAGE DIVIDER RULE AT INPUT SIDE

$$\frac{y_{in}}{R_s + R_{in}} = \frac{y_{in}}{R_s + R_{in}}$$

$$A_{V_5} = A_V \times \left(\frac{Rin}{Rin + Rs}\right)$$

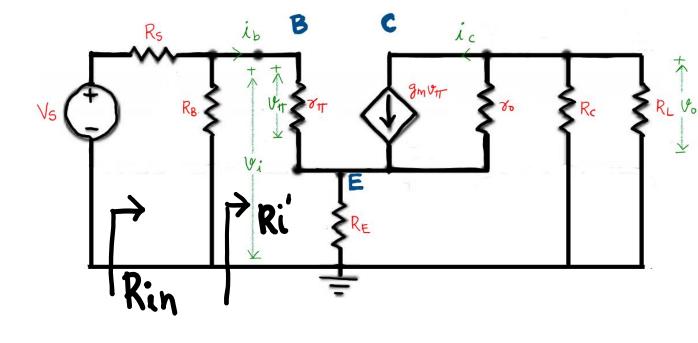


$$AV_5 = \frac{-(R_c IIRL)}{R_E} \left(\frac{Rin}{Rin + R_s} \right)$$

Current Gain (Ai)

$$Ai = \frac{iout}{iin} = \frac{Vout/RL}{Vin/Rin} = \frac{Vout}{Vin} \times \frac{Rin}{RL}$$

$$A_i = A_v \left(\frac{Rin}{RL} \right)$$



$$Ai = \frac{-(RcIIRL)}{RE} \cdot \frac{(Rin)}{RL}$$

