reset_b	•		
i_copro_write_data_wen	<b>-</b>		
i_status_bits_firq_mask_wen	•	o_multiply_done	•
i_status_bits_irq_mask_wen	<b>-</b>		
i_status_bits_mode_wen	•		_
i_status_bits_flags_wen	•	o_status_bits	
i_reg_bank_wen	<b>-</b>		
i_pc_wen	<b>-</b>	o_priviledged	
i_base_address_wen	•	o_pri/meaged	
i_write_data_wen	•		
i_firq_not_user_mode	<b>→</b>	o_exclusive	-
i_user_mode_regs_store_nxt	<b>→</b>		
i_user_mode_regs_load	•		
i_reg_write_sel	•	o_write_enable	
i_status_bits_sel	<b>▶</b>		
i_byte_enable_sel	<b>▶</b>		
i_pc_sel	<b>▶</b>	o_data_access	-
i_address_sel	<b>-</b>		
i_interrupt_vector_sel	<b>-</b>	hada ayalda	<b></b>
i_multiply_function	•	o_byte_enable	
	973 execute		
i_alu_function	- a23_execute		
i_alu_function  i_barrel_shift_function	• a25_cxccutc	o_adex	<b></b>
	*	o_adex	▶
i_barrel_shift_function	*	o_adex	•
i_barrel_shift_function  i_barrel_shift_data_sel	azs_cxccutc	o_adex o_address_nxt	<b>-</b>
i_barrel_shift_function  i_barrel_shift_data_sel  i_barrel_shift_amount_sel	azs_cxccutc		•
i_barrel_shift_function  i_barrel_shift_data_sel  i_barrel_shift_amount_sel  i_rn_sel	azs_cxccutc		
i_barrel_shift_function  i_barrel_shift_data_sel  i_barrel_shift_amount_sel  i_rn_sel  i_rds_sel	azs_cxccutc		
i_barrel_shift_function  i_barrel_shift_data_sel  i_barrel_shift_amount_sel  i_rn_sel  i_rds_sel  i_rm_sel	azs_cxccutc	o_address_nxt	
i_barrel_shift_function  i_barrel_shift_data_sel  i_barrel_shift_amount_sel  i_rn_sel  i_rds_sel  i_rm_sel  i_data_access_exec  i_exclusive_exec  i_condition		o_address_nxt o_address_valid	
i_barrel_shift_function  i_barrel_shift_data_sel  i_barrel_shift_amount_sel  i_rn_sel  i_rds_sel  i_rm_sel  i_data_access_exec  i_exclusive_exec  i_condition  i_shift_imm_zero	azz_cxccuc	o_address_nxt	
i_barrel_shift_function  i_barrel_shift_data_sel  i_barrel_shift_amount_sel  i_rm_sel  i_rds_sel  i_rm_sel  i_cm_sel  i_cm_sel  i_data_access_exec  i_exclusive_exec  i_exclusive_exec  i_shift_imm_zero  i_imm_shift_amount	AZS_CXCCUTC	o_address_nxt o_address_valid	
i_barrel_shift_data_sel  i_barrel_shift_amount_sel  i_rn_sel  i_rds_sel  i_rm_sel  i_data_access_exec  i_exclusive_exec  i_condition  i_shift_imm_zero  i_imm_shift_amount  i_imm32	azz_cxccuc	o_address_nxt o_address_valid	
i_barrel_shift_data_sel  i_barrel_shift_amount_sel  i_rn_sel  i_rds_sel  i_rm_sel  i_data_access_exec  i_exclusive_exec  i_condition  i_shift_imm_zero  i_imm_shift_amount  i_imm32  i_status_bits_firq_mask	azz_cxccutc	o_address_nxt  o_address_valid  o_address	
i_barrel_shift_function  i_barrel_shift_data_sel  i_barrel_shift_amount_sel  i_rn_sel  i_rds_sel  i_rm_sel  i_tadata_access_exec  i_exclusive_exec  i_condition  i_shift_imm_zero  i_imm_shift_amount  i_imm32  i_status_bits_firq_mask  i_status_bits_irq_mask		o_address_nxt  o_address_valid  o_address	
i_barrel_shift_function  i_barrel_shift_data_sel  i_barrel_shift_amount_sel  i_rn_sel  i_rds_sel  i_rm_sel  i_data_access_exec  i_exclusive_exec  i_condition  i_shift_imm_zero  i_imm_shift_amount  i_imm32  i_status_bits_firq_mask  i_status_bits_irq_mask  i_status_bits_mode		o_address_nxt  o_address_valid  o_address	
i_barrel_shift_function  i_barrel_shift_data_sel  i_barrel_shift_amount_sel  i_rn_sel  i_rds_sel  i_rm_sel  i_data_access_exec  i_exclusive_exec  i_exclusive_exec  i_shift_imm_zero  i_imm_shift_amount  i_imm32  i_status_bits_firq_mask  i_status_bits_irq_mask  i_status_bits_mode  i_fetch_stall		o_address_nxt  o_address_valid  o_address  o_address	
i_barrel_shift_function  i_barrel_shift_data_sel  i_barrel_shift_amount_sel  i_rn_sel  i_rds_sel  i_rm_sel  i_data_access_exec  i_exclusive_exec  i_exclusive_exec  i_condition  i_shift_imm_zero  i_imm_shift_amount  i_imm32  i_status_bits_firq_mask  i_status_bits_irq_mask  i_status_bits_irq_mask  i_status_bits_mode  i_fetch_stall  i_copro_read_data		o_address_nxt  o_address_valid  o_address  o_address	
i_barrel_shift_function  i_barrel_shift_data_sel  i_barrel_shift_amount_sel  i_rn_sel  i_rds_sel  i_rm_sel  i_exclusive_exec  i_exclusive_exec  i_condition  i_shift_imm_zero  i_imm_shift_amount  i_imm32  i_status_bits_firq_mask  i_status_bits_irq_mask  i_status_bits_irq_mask  i_status_bits_mode  i_fetch_stall  i_copro_read_data  i_read_data_alignment		o_address_nxt  o_address_valid  o_address  o_address	
i_barrel_shift_function  i_barrel_shift_data_sel  i_barrel_shift_amount_sel  i_rn_sel  i_rds_sel  i_rm_sel  i_data_access_exec  i_exclusive_exec  i_exclusive_exec  i_condition  i_shift_imm_zero  i_imm_shift_amount  i_imm32  i_status_bits_firq_mask  i_status_bits_irq_mask  i_status_bits_irq_mask  i_status_bits_mode  i_fetch_stall  i_copro_read_data		o_address_nxt  o_address_valid  o_address  o_address  o_copro_write_data	