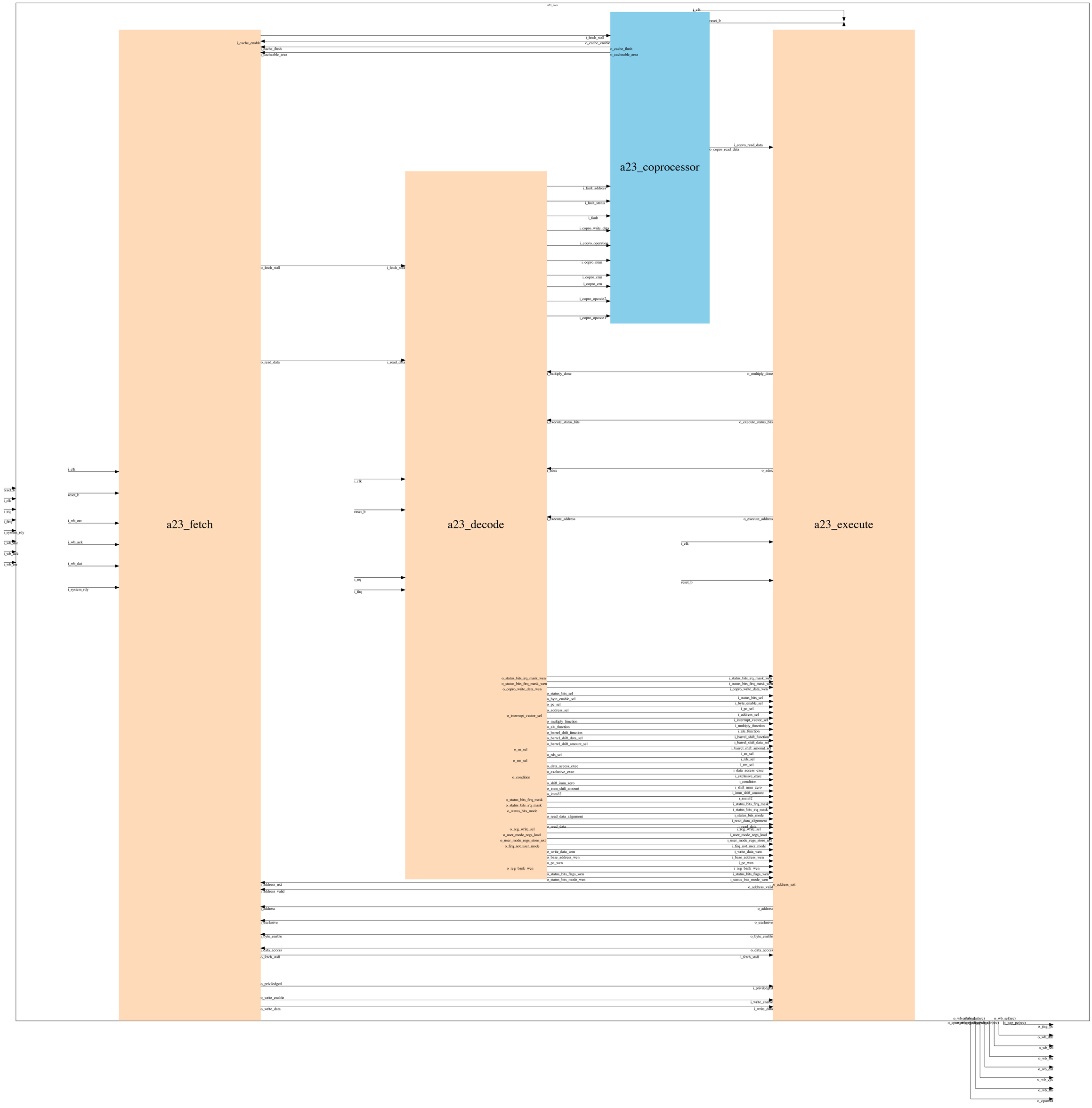
a23_core
a23_coprocessor
a23_fetch
a23_decode
a23_execute
i_clk
reset_b
i_fetch_stall
o_cache_enable
i_cache_enable
o_cache_flush
i_cache_flush
o_cacheable_area
i_cacheable_area
i_copro_read_data
o_copro_read_data
i_fault_address
i_fault_status
i_fault
i_copro_write_data
i_copro_operation
i_copro_num
i_copro_crm
i_copro_crn
i_copro_opcode2
i_copro_opcode1
o_fetch_stall
i_fetch_stall
o_read_data
i_read_data
i_multiply_done
o_multiply_done
i_execute_status_bits
o_execute_status_bits
i_adex
o_adex
i_execute_address
o_execute_address
i_irq
i_firq
i_system_rdy
i_wb_err
i_wb_ack
i_wb_dat
o_status_bits_irq_mask_wen
o_status_bits_firq_mask_wen
o_copro_write_data_wen
o_status_bits_sel
o_byte_enable_sel
o_pc_sel
o_address_sel
o_interrupt_vector_sel
o_multiply_function
o_alu_function
o_barrel_shift_function
o_barrel_shift_data_sel
o_barrel_shift_amount_sel
o_rn_sel
o_rds_sel
o_rm_sel
o_data_access_exec
o_exclusive_exec
o_condition
o_shift_imm_zero
o_imm_shift_amount
o_imm32
o_status_bits_firq_mask
o_status_bits_irq_mask
o_status_bits_mode
o_read_data_alignment
o_reg_write_sel
o_user_mode_regs_load
o_user_mode_regs_store_nxt
o_firq_not_user_mode
o_write_data_wen
o_base_address_wen
o_pc_wen
o_reg_bank_wen
o_status_bits_flags_wen
o_status_bits_mode_wen
o_address_nxt
o_address_valid
o_address
o_exclusive
o_byte_enable
o_data_access
o_fetch_stall
o_privileged
o_write_enable
o_write_data
o_wb_sel(src)
o_tag_pc(src)
o_cpuwait