**Lab 9: Implement a RISC single Cycle Processor**

Task 1: Using blocks (lab 8) and other glue logic, implement single cycle processor RISC which could run add and logic instructions. ( use the mips\_regfile.circ 32 bit reg file and 32 bit ALU from lab 8)

**Submission :**

Submit single doc/pdf file with above answers and \*.circ files . Course work submission through cs322.iitp@gmail.com with subject: YourrollNo\_Lab7. **Due on 12**th October 2018 , 5PM**.**