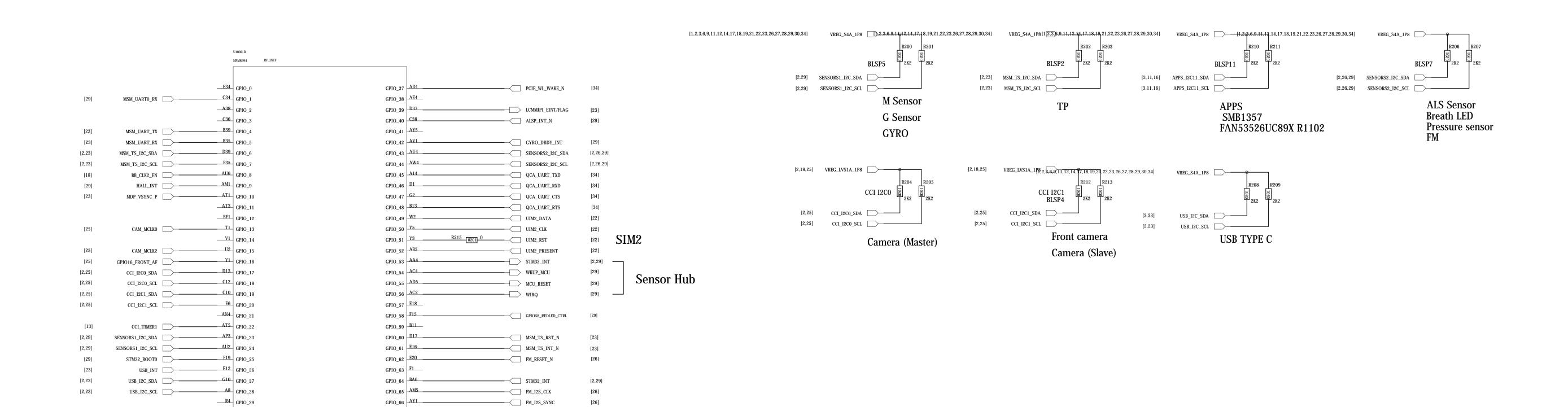


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GPIO_67 AY3 FM_12S_DATA [26]

GPIO_68 AW2 CODEC_RESET_N [28]

GPIO_70 F39 R216 0201 0 SLIMBUS_CDC_CLK [28]

0201 82pF(NC)

SLIMBUS_CDC_DATA [28]

CODEC_INT1 [28]

CODEC_INT2 [28]

GPIO_69 F41

GPIO_71 C40 φ

GPIO_72 E40

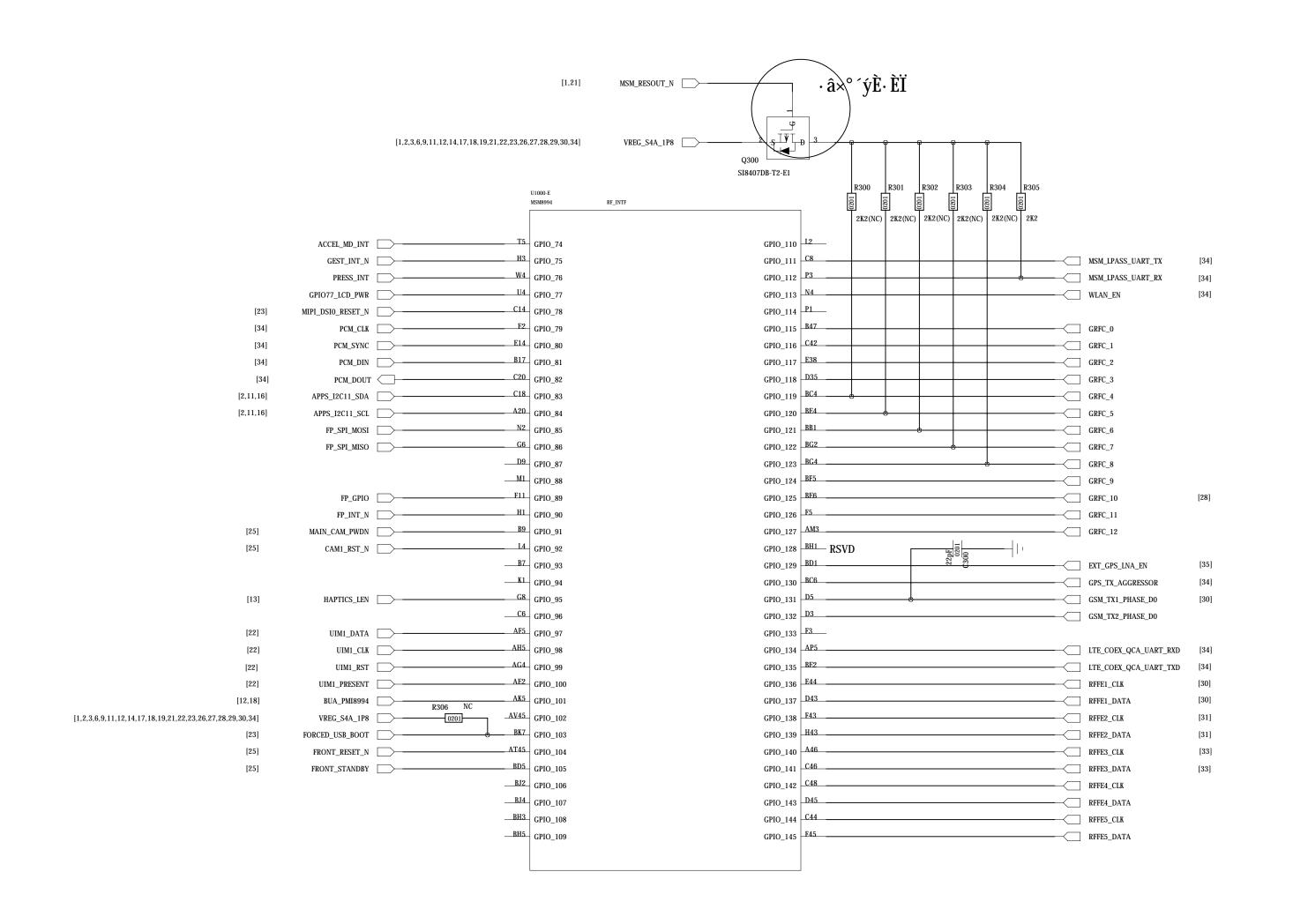
[23] GPIO30_3V3_LDO1_EN ________D7 GPIO_30

[34] PCIE_WL_RST_N AB1 GPIO_35

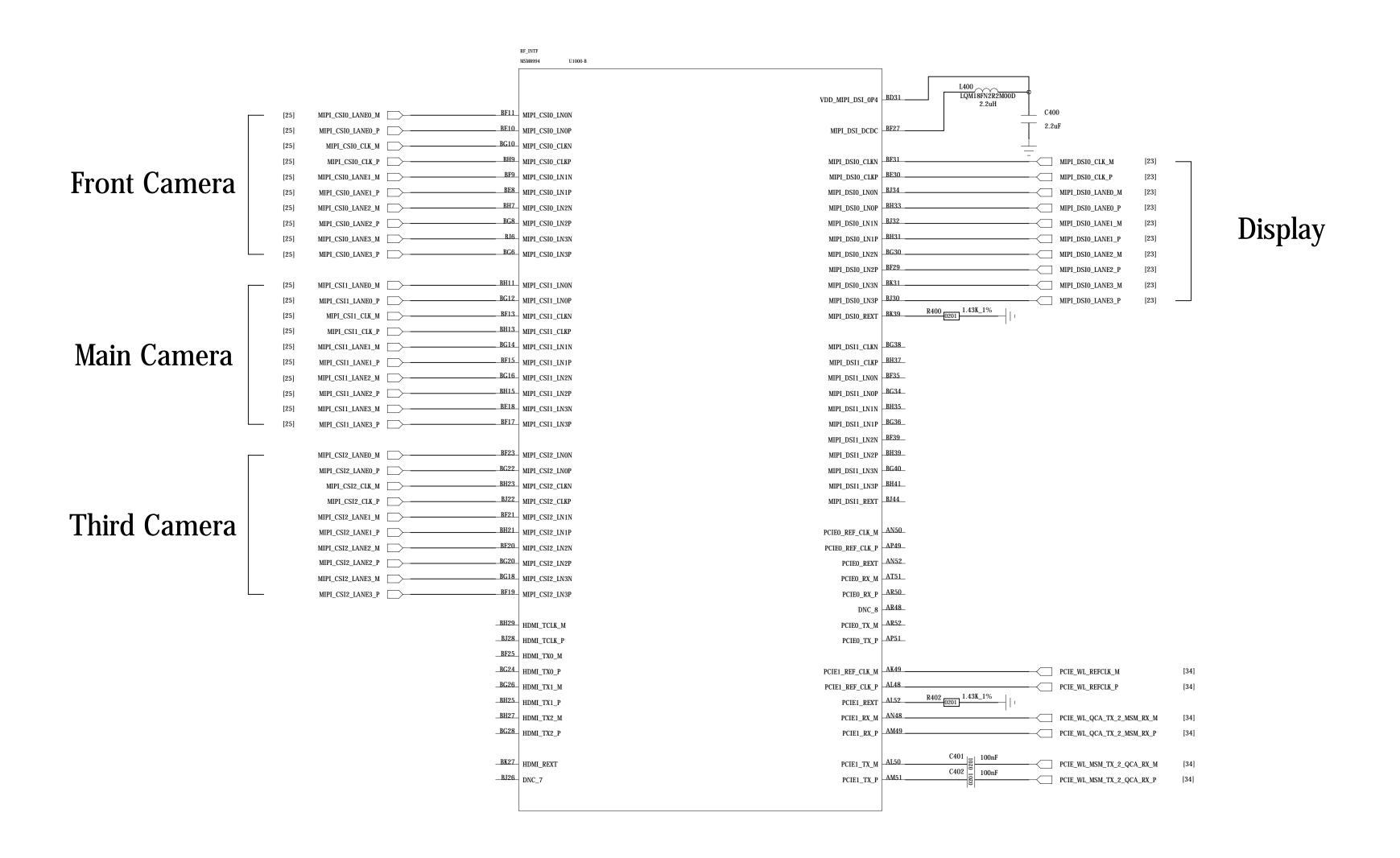
[34] PCIE_WL_CLK_REQ_N _____AD3 GPIO_36

BG42 GPIO_33 _BF41 GPIO_34

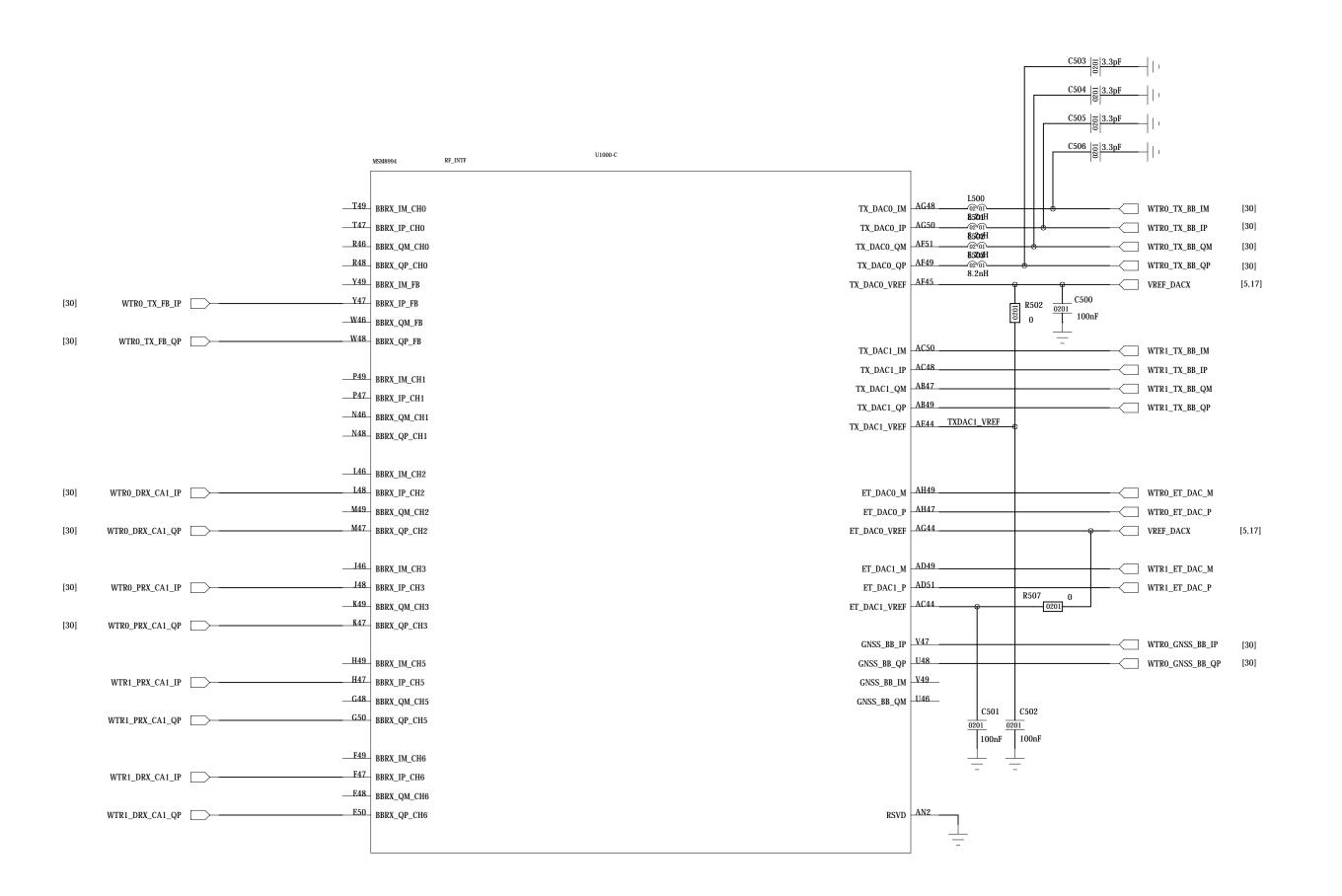
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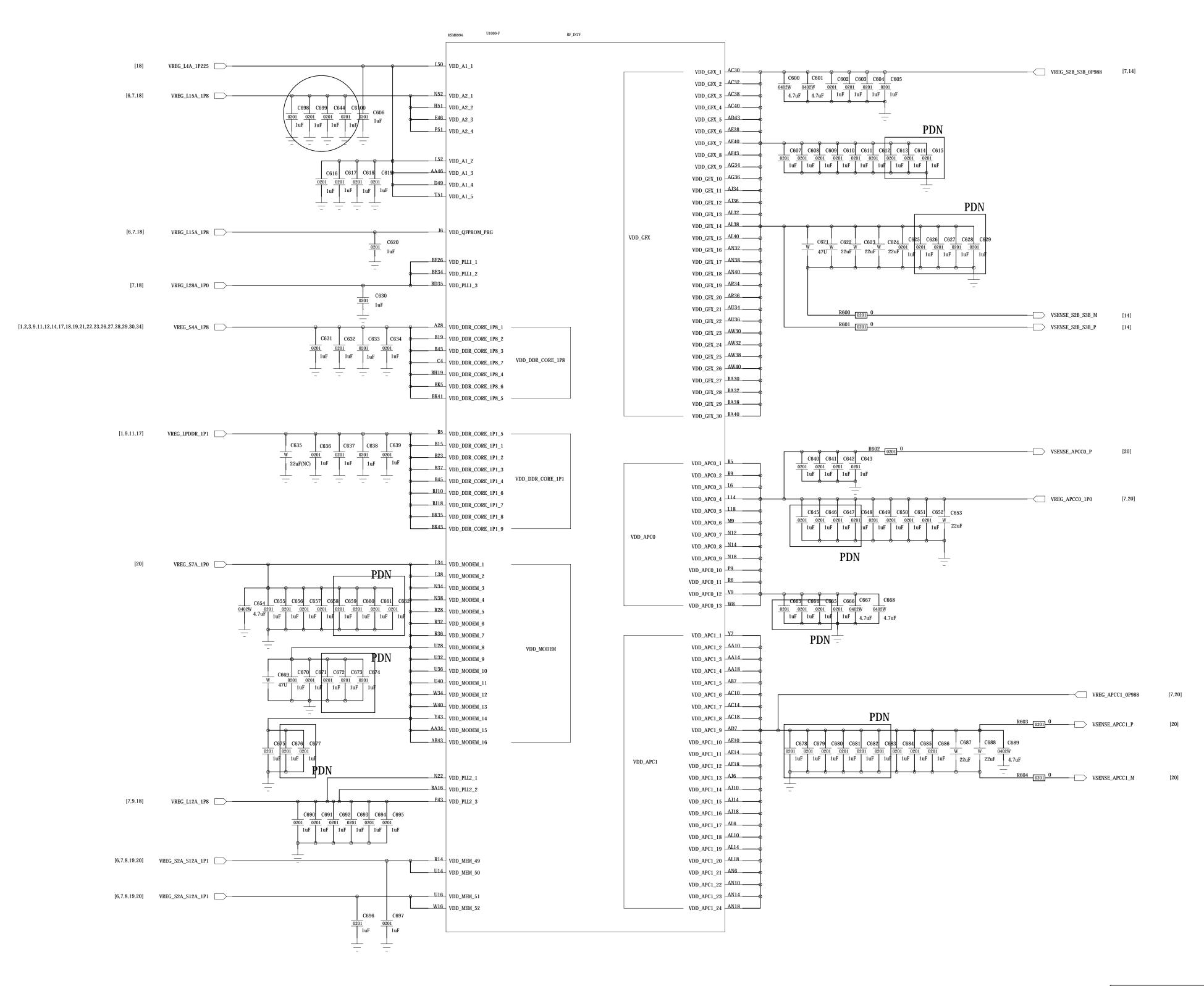
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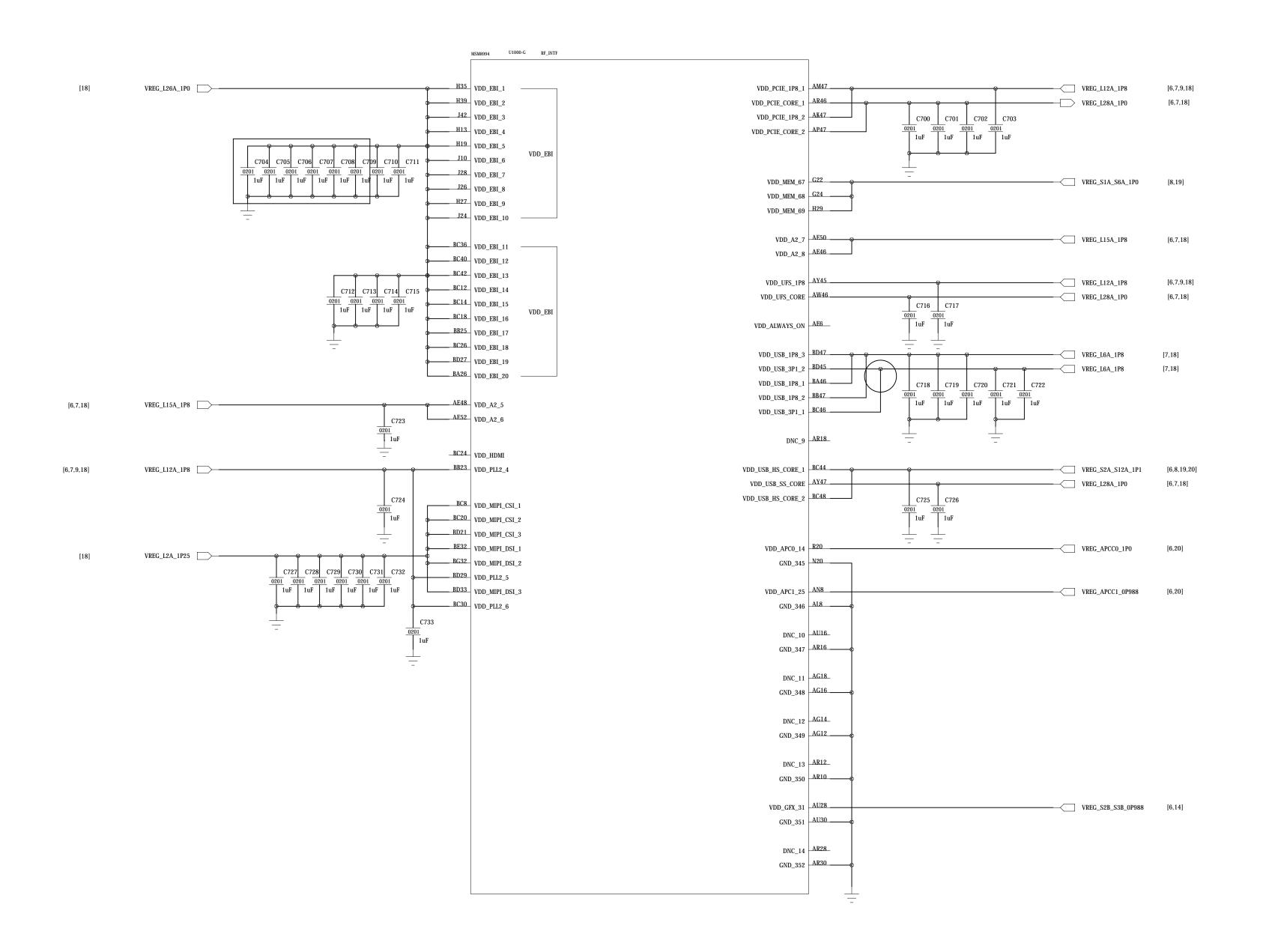
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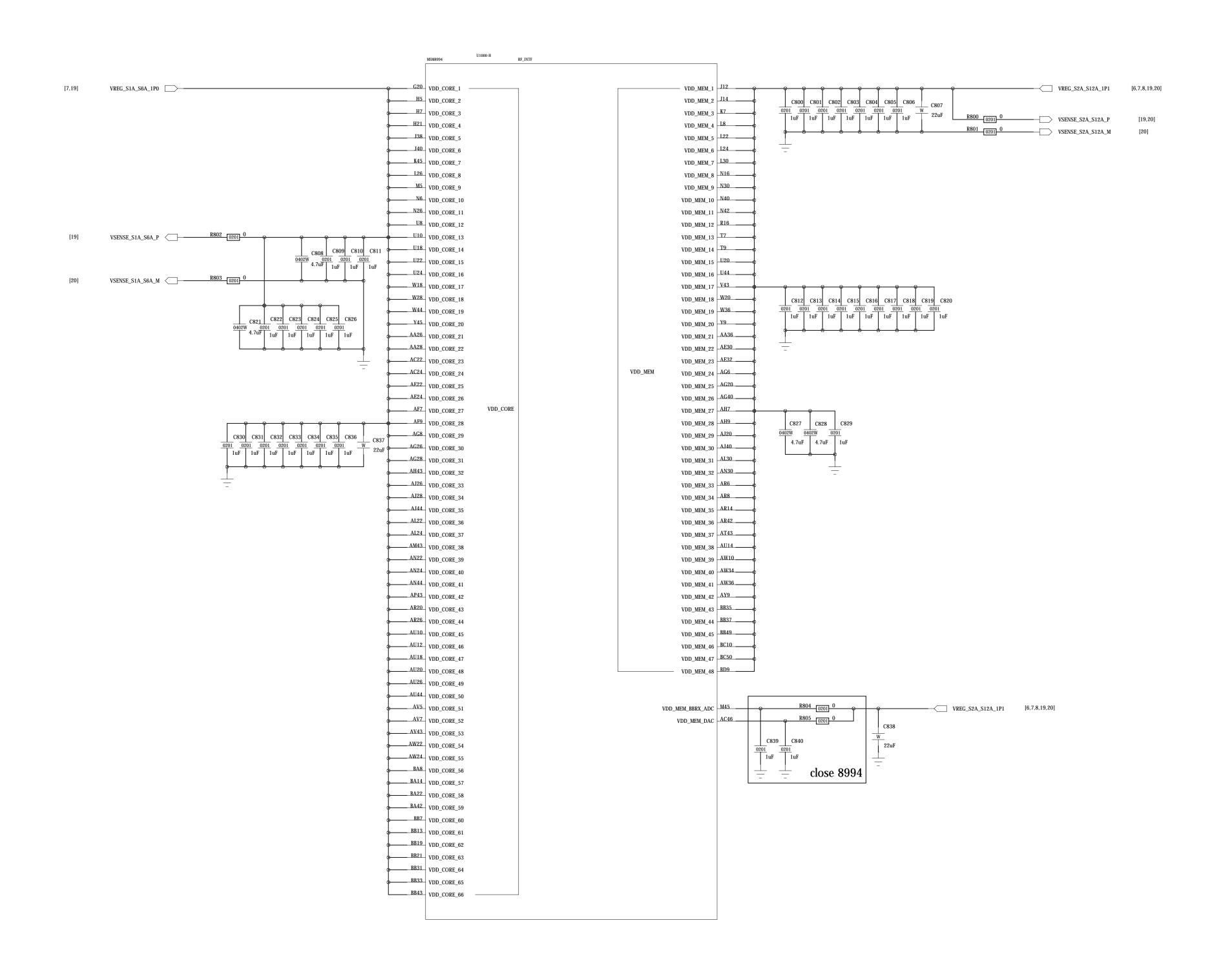
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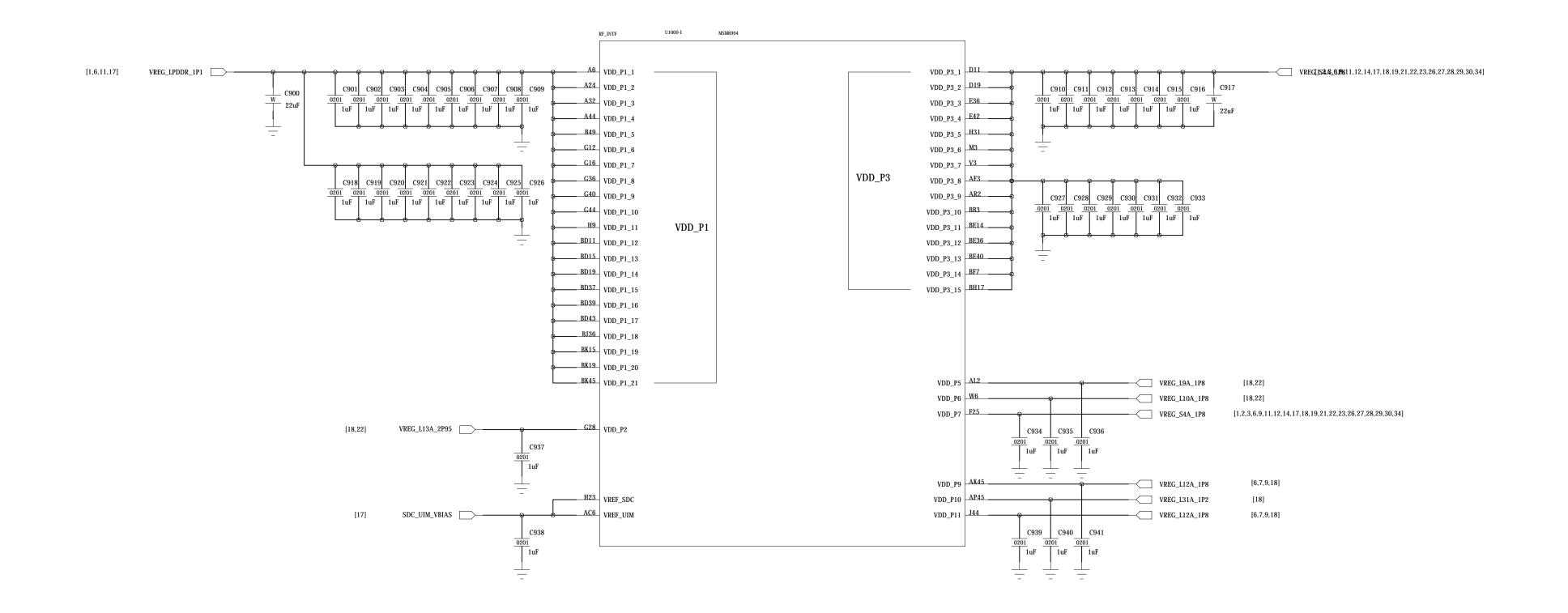
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DESIGNED:	DATE:	SCALE:	1:1	SHEET: 9 OF 36	REV: V1.0		

RF_INTF U1000-J MSM8994 GND_332 AJ50 N44 GND_303 R12 GND_304 GND_333 AG52 U12 GND_305 GND_334 BE24 W12 GND_306 GND_335 BC32 W14 GND_307 GND_336 BC34 N24 GND_308 GND_337 AM45 AW14 GND_309 GND_338 AL46 R42 GND_310 T43 GND_311 GND_340 BB45 N50 GND_312 GND_341 AD47 GND_342 AB51 K51 GND_313 GND_343 AW44 F51 GND_314 U50 GND_315 GND_344 BE46 G26 GND_316 H25 GND_317 BA24 GND_318 BB27 GND_319 M43 GND_320 J52 GND_321 E52 GND_322 V51 GND_323 J30 GND_324 BD25 GND_325 AH51 GND_326 GND_327 GND_328 BB15 GND_329 AC52 GND_330 AA44 GND_331

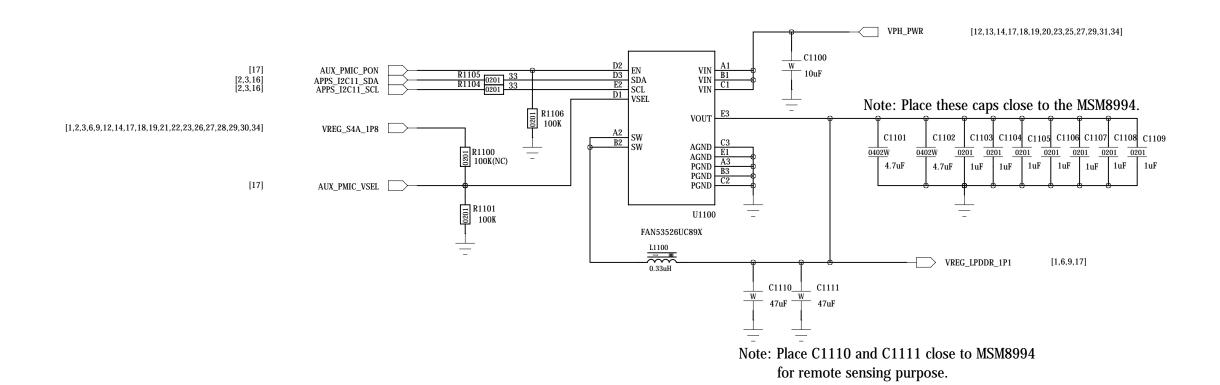
	RF_INTF	U1000-K	MSM8994		
A2	GND_1			GND_51	J16
A4	GND_1 GND_2			GND_51	J18
A10	GND_2 GND_3			GND_52	J20
A12					J22
A16	GND_4			GND_54	J32
A22	GND_5			GND_55	J34
	GND_6			GND_56	l I
A26	GND_7			GND_57	J36
A34	GND_8			GND_58	J50
A36	GND_9			GND_59	К3
A42	GND_10			GND_60	K43
A48	GND_11			GND_61	I.10 —
A50	GND_12			GND_62	L12
A52	GND_13			GND_63	L16
B1	GND_14			GND_64	L20
B3	GND_15			GND_65	L28
B41_	GND_16			GND_66	L32
B51	GND_17			GND_67	L36
C2	GND_18			GND_68	L40
C16	GND_19			GND_69	L42
C50	GND_20			GND_70	L44
C52	GND_21			GND_71	M7
D15	GND_22			GND_72	M51
D21	GND_23			GND_73	N8
D47	GND_24			GND_74	N10
D51	GND_24			GND_75	N28
E4					N32
E8	GND_26			GND_76	N36
E10	GND_27			GND_77	P5
F7	GND_28			GND_78	P7
F9	GND_29			GND_79	ľ
	GND_30			GND_80	P45
F13	GND_31			GND_81	R2
F17	GND_32			GND_82	R8
F21	GND_33			GND_83	R10
F33	GND_34			GND_84	R18
F37	GND_35			GND_85	R24
G4	GND_36			GND_86	R26
G18	GND_37			GND_87	R30
G34	GND_38			GND_88	R34
G42	GND_39			GND_89	R38
G46	GND_40			GND_90	R40
G52	GND_41			GND_91	R44
H11	GND_42			GND_92	R50
H15	GND_43			GND_93	R52
H17	GND_44			GND_94	Т3
Н33	GND_45			GND_95	T45
Н37	GND_46			GND_96	U6
H45	GND_40 GND_47			GND_97	U26
J2					U30
	GND_48			GND_98	U34
IA	GND_49			GND_99	
J4	GND_50			GND_100	U38

1	RF_INTF	U1000-L	MSM8994		1
U42	GND_101			GND_151	AE34
U52	GND_102			GND_152	AE36
V7	GND_103			GND_153	AE42
V45	GND_104			GND_154	AF1
W10	GND_105			GND_155	AF47
W22	GND_106			GND_156	AG2
W24				GND_157	AG10
W26	GND_107				AG22
W30	GND_108			GND_158 GND_159	AG24
W32	GND_109				AG30
W38	GND_110			GND_160	AG32
W42	GND_111			GND_161	AG38
W50	GND_112			GND_162	AG42
	GND_113			GND_163	
W52	GND_114			GND_164	AG46
Y51	GND_115			GND_165	AH3
AA2	GND_116			GND_166	AH45
AA6_	GND_117			GND_167	AJ8
AA8	GND_118			GND_168	AJ12
AA12	GND_119			GND_169	AJ16
AA16	GND_120			GND_170	AJ22
AA20	GND_121			GND_171	AJ24
AA22	GND_122			GND_172	AJ30
AA24	GND_123			GND_173	AJ32
AA30	GND_124			GND_174	AJ38
AA32	GND_125			GND_175	AJ42
AA38	GND_126			GND_176	AJ46
AA40	GND_127			GND_177	AJ48
AA42	GND_128			GND_178	AJ52
AA48	GND_129			GND_179	AK7
AA50	GND_130			GND_180	AK9
AA52	GND_131			GND_181	AK51
AB3	GND_132			GND_182	AL12
AB9	GND_133			GND_183	AL16
AB45	GND_134			GND_184	AL20
AC8	GND_135			GND_185	AL26
AC12	GND_136			GND_186	AL28
AC16	GND_137			GND_187	AL34
AC20	GND_138			GND_188	AL36
AC26	GND_139			GND_189	AL42
AC28	GND_140			GND_190	AL44
AC34	GND_141			GND_191	AM7
AC36	GND_142			GND_192	AM9
AC42	GND_143			GND_193	AN12
AD9					AN16
AE8	GND_144			GND_194	AN20
AE12	GND_145			GND_195	AN26
AE12	GND_146			GND_196	AN28
AE10 AE20	GND_147			GND_197	AN34
	GND_148			GND_198	
AE26	GND_149			GND_199	AN42
AE28	GND_150			GND_200	AN42

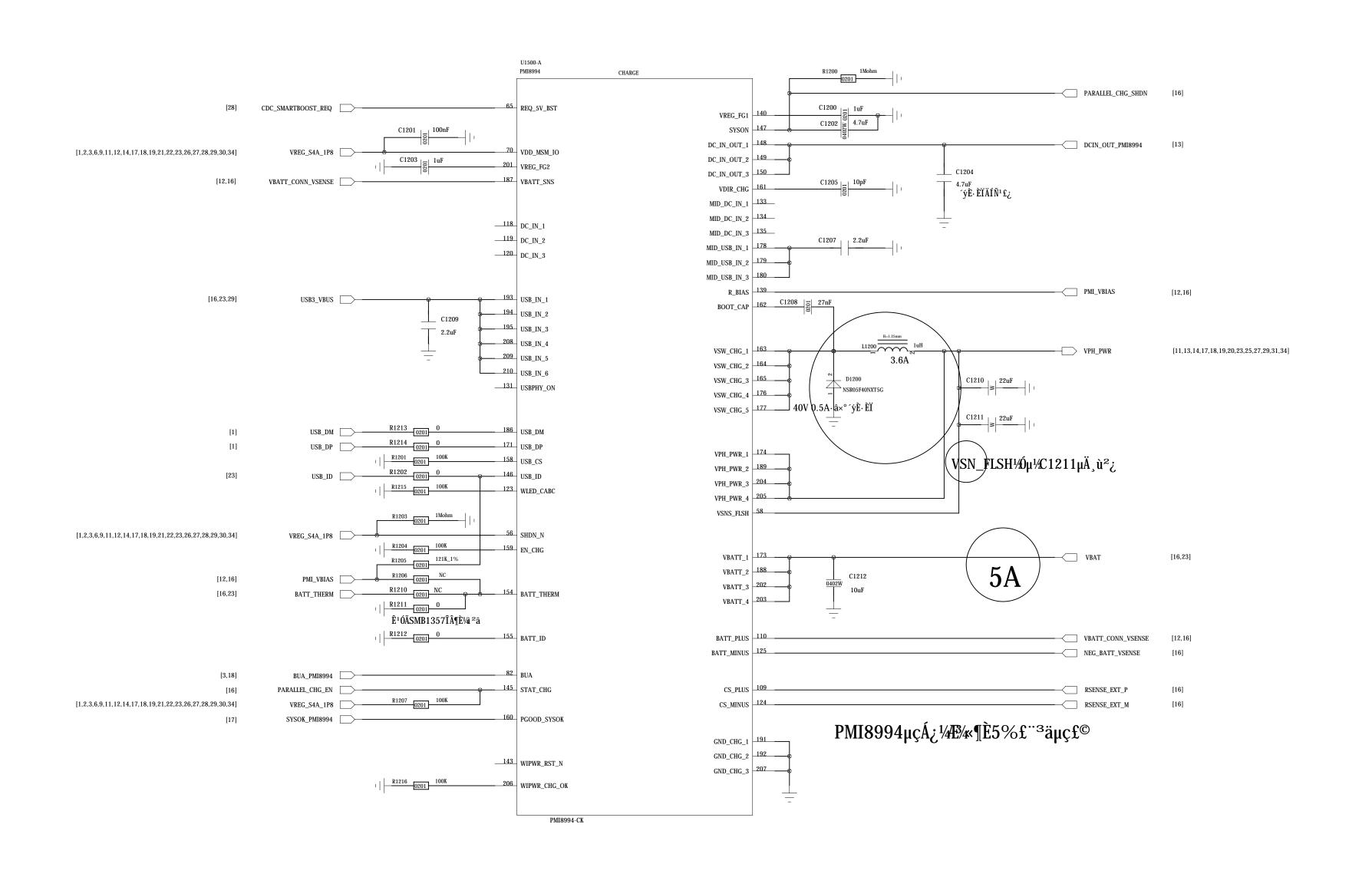
	RF_INTF	U1000-M	MSM8994		
AN46	GND_201			GND_253	BB51
AP1	GND_202			GND_254	BC2
AP7	GND_203			GND_255	BC16
AP9	GND_204			GND_256	BC22
AR22	GND_205			GND_257	BC28
AR24	GND_206			GND_258	BD3
AR32	GND_207			GND_259	BD17
AR38	GND_208			GND_260	BD23
AR40	GND_209			GND_261	BD41
AR44	GND_210			GND_262	BD49
AT7	GND_211			GND_263	BD51
AT9	GND_212			GND_264	BE12
AU8	GND_213			GND_265	BE16
AU22	GND_214			GND_266	BE22
AU24	GND_215			GND_267	BE28
AU32	GND_216			GND_268	BE42
AU38	GND_217			GND_269	BE50
AU40	GND_218			GND_270	BE52
AU42	GND_219			GND_271	BF3
AU52	GND_220			GND_272	BF37
AV3	GND_221			GND_273	BF45
AV9	GND_222			GND_274	BH43
AW6	GND_223			GND_275	BJ8
AW8_	GND_224			GND_276	BJ12
AW12	GND_225			GND_277	BJ14
AW16	GND_226			GND_278	BJ16
AW18	GND_227			GND_279	BJ20
AW20	GND_228			GND_280	BJ24
AW26	GND_229			GND_281	BJ38
AW28	GND_230			GND_282	BJ40
AW42	- GND_231			GND_283	BJ42
AY7	GND_232			GND_284	BJ46
AY43	GND_233			GND_285	BJ48
BA2	GND_234			GND_286	BJ50
BA10	GND_235			GND_287	BJ52
BA12	- GND_236			GND_288	BK1
BA18 BA20	GND_237			GND_289	BK3
BA20 BA28	GND_238			GND_290	BK9
BA34	GND_239			GND_291	BK13
BV38	GND_240			GND_292	BK17
BA44	GND_241			GND_293	BK21
BA48	GND_242			GND_294	BK23
PA FO	GND_243			GND_295	BK25
BA52	GND_244			GND_296	BK29
PR5	GND_245			GND_297	BK33
BB9	GND_246			GND_298	BK37
BB11	GND_247			GND_299 GND_300	BK47
BB17	GND_248				BK49
BB29	GND_249 GND_250			GND_301 GND_302	BK51
BB39	GND_250 GND_251			G14D_90%	
BB41	GND_251 GND_252				
	GIVD_LUL				_
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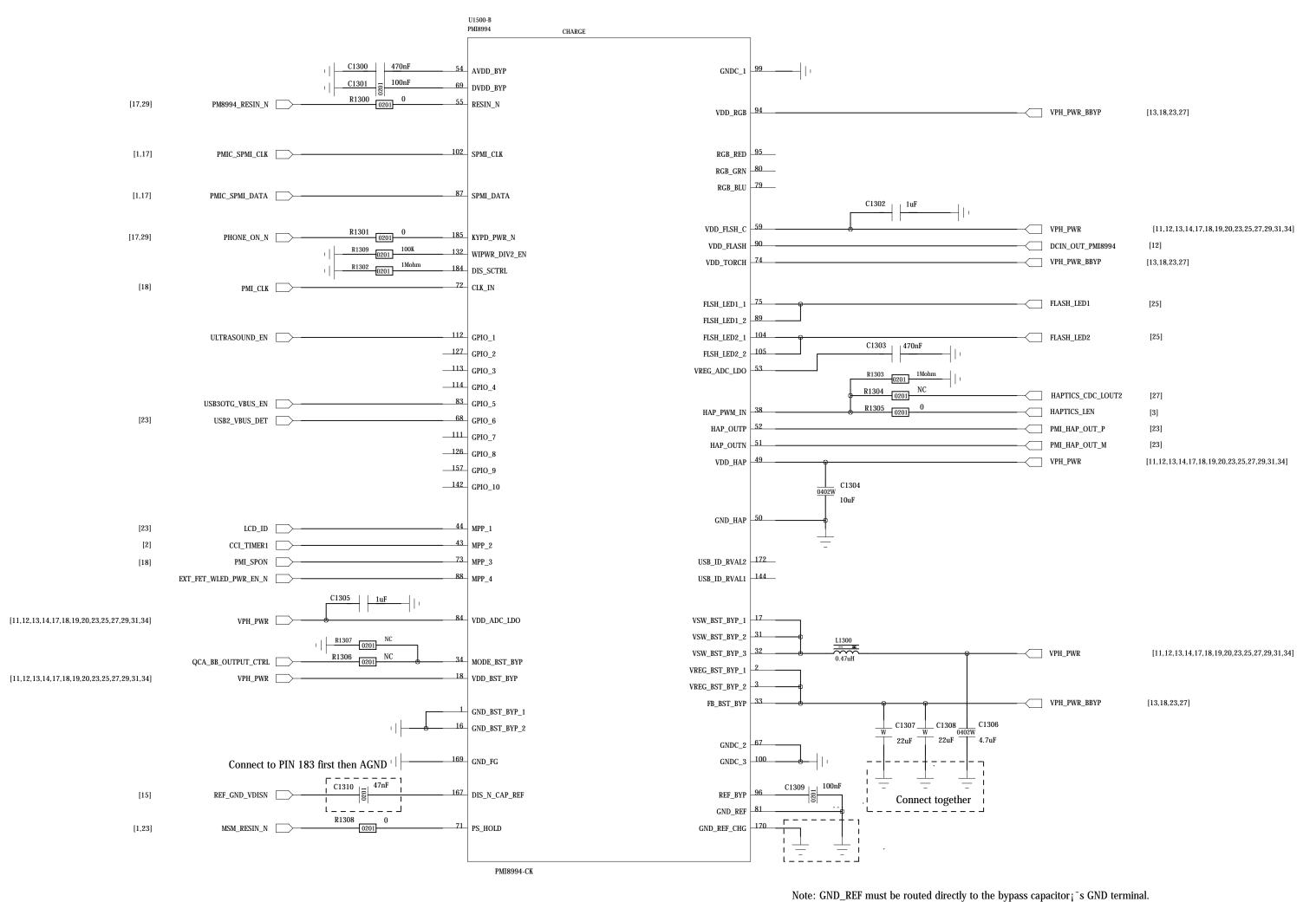
MSM8994 Additional power supplies



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Note: GND_REF must be routed directly to the bypass capacitor; s GND terminal.

A via to main GND should only be present at the bypass capacitor; s GND terminal,

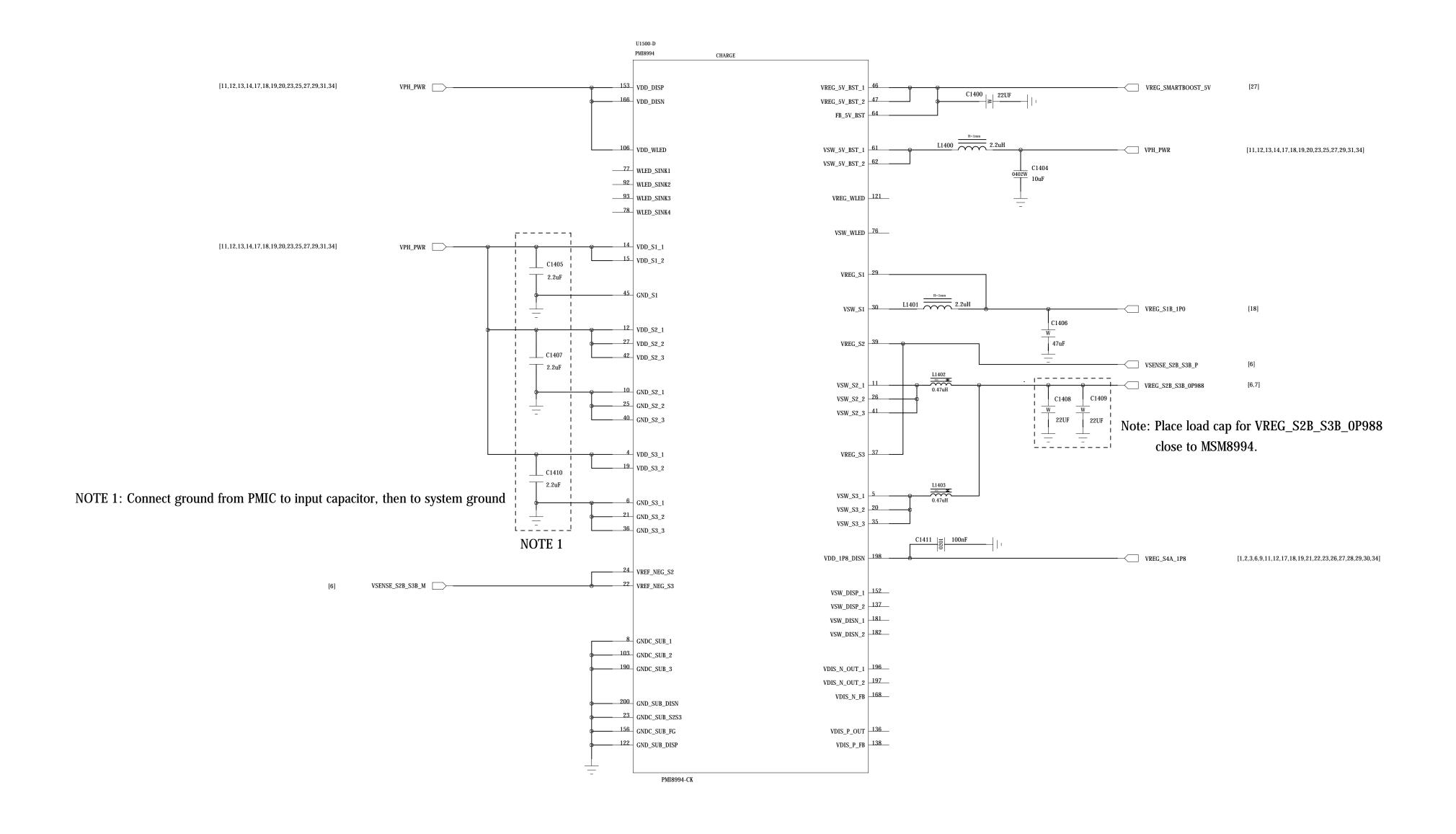
NOT at the PMIC; s GND_REF pad.

Note: The capacitance (C416) required on the PMI8994; s DIS_N_CAP_REF pin depends on whether you plan to support an LCD or an AMOLED panel.

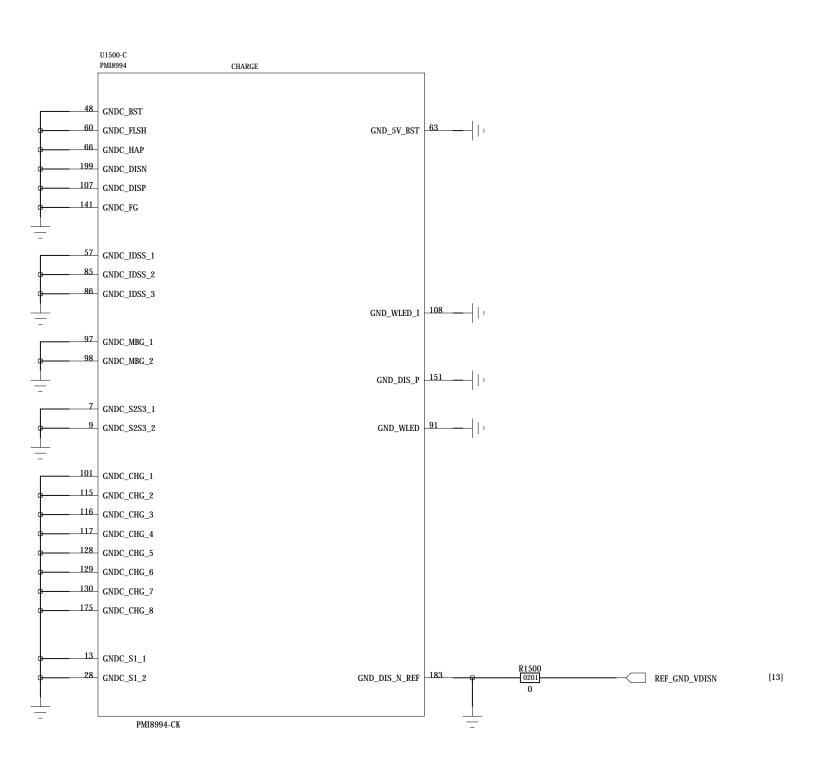
If you plan to support an LCD panel, 47nF is required on DIS_N_CAP_REF.

If you plan to support an AMOLED panel, 1.5nF is required on DIS_N_CAP_REF.

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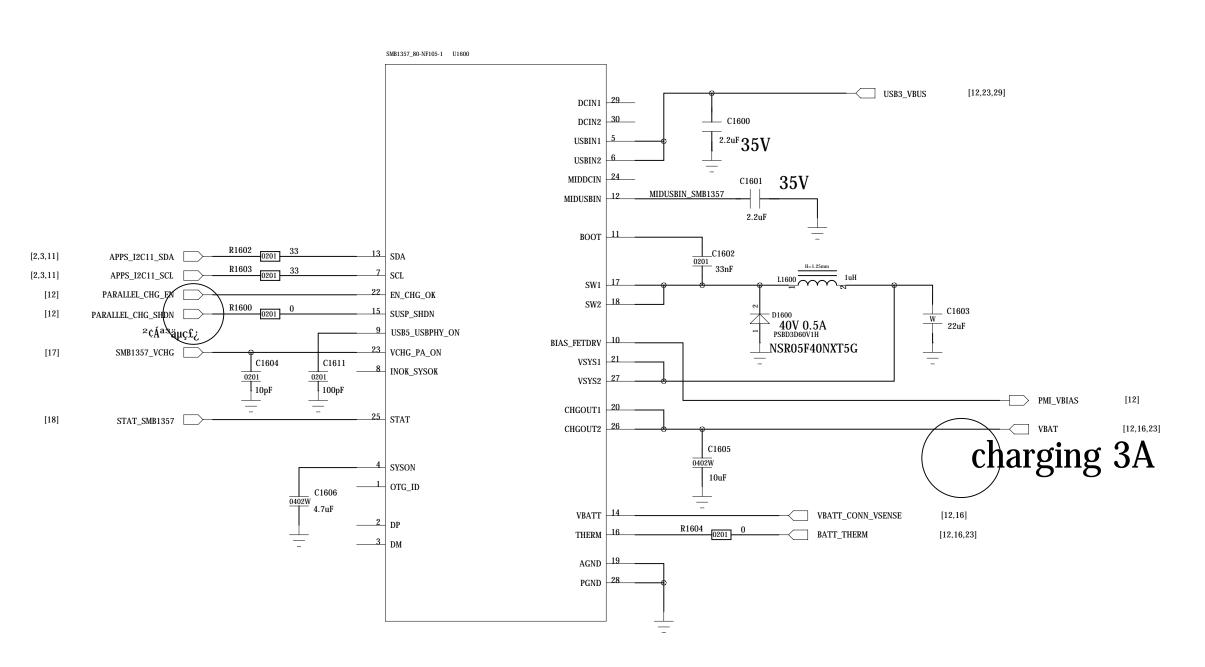


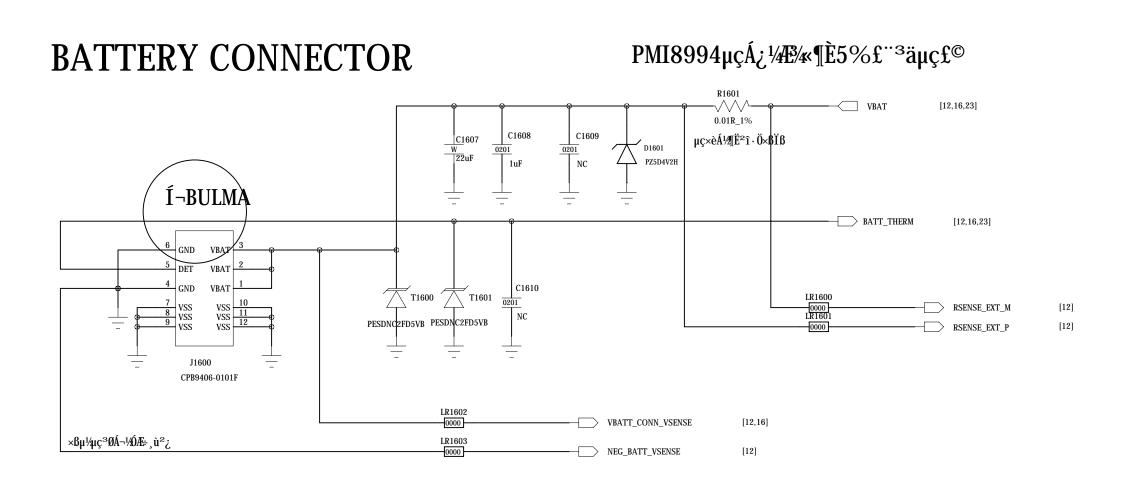
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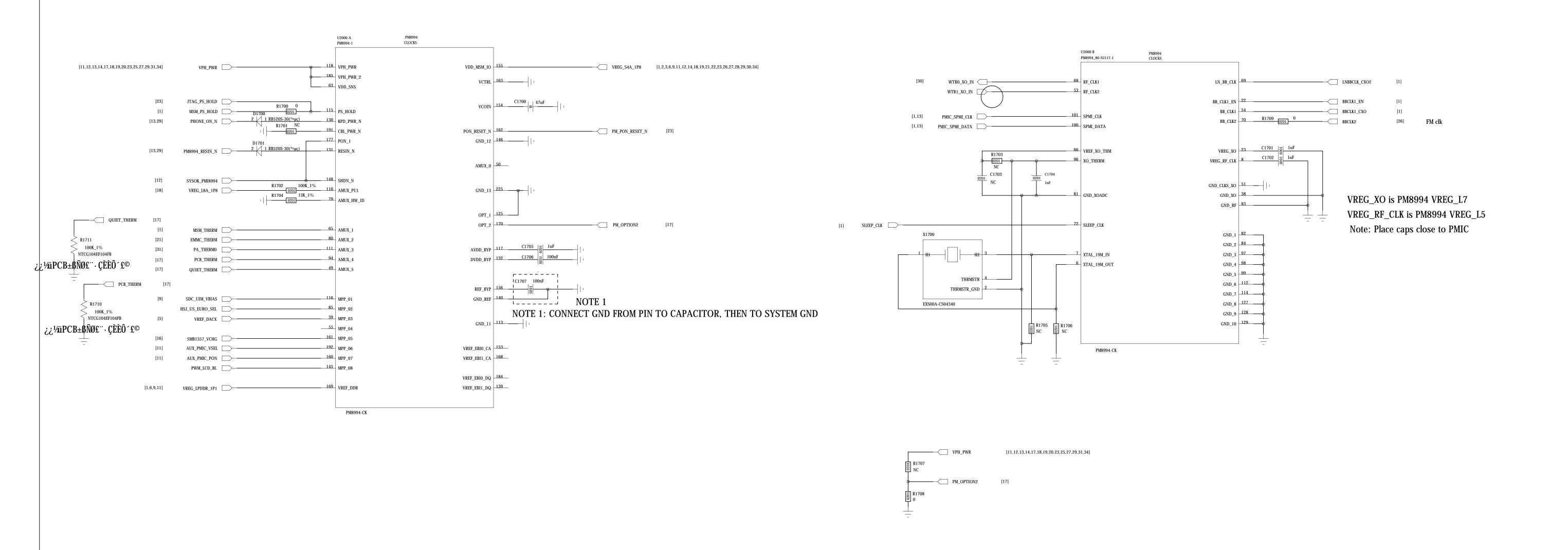
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Charger

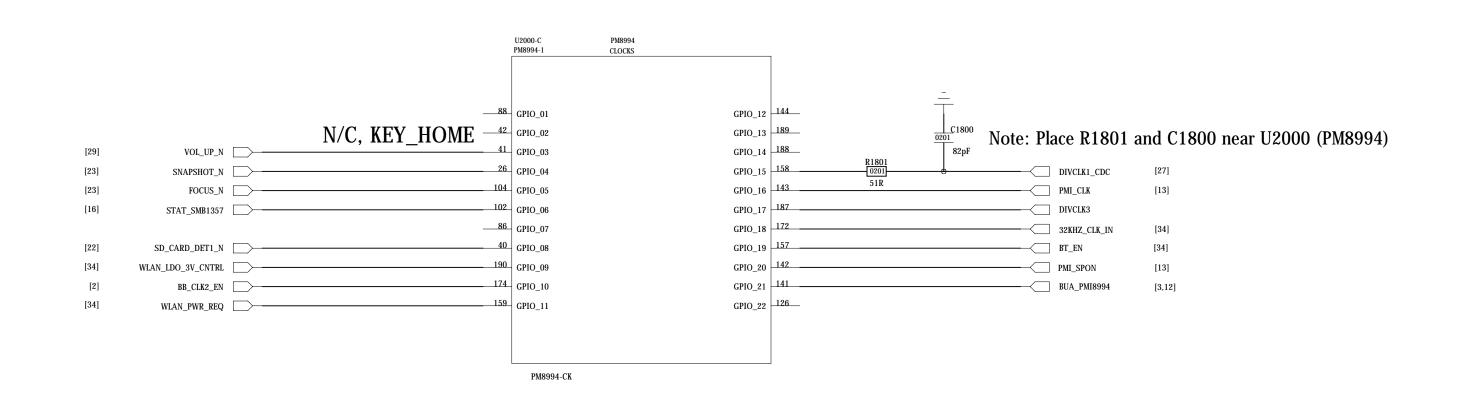


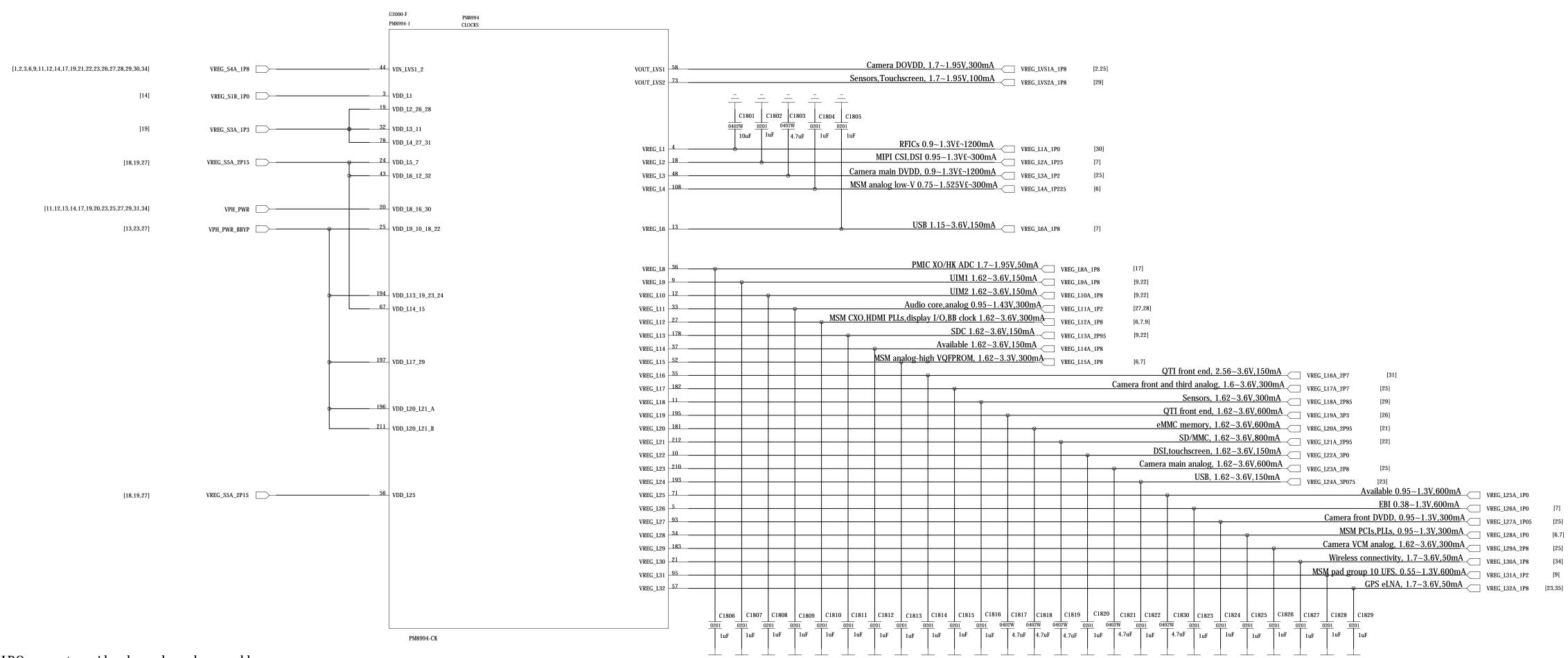


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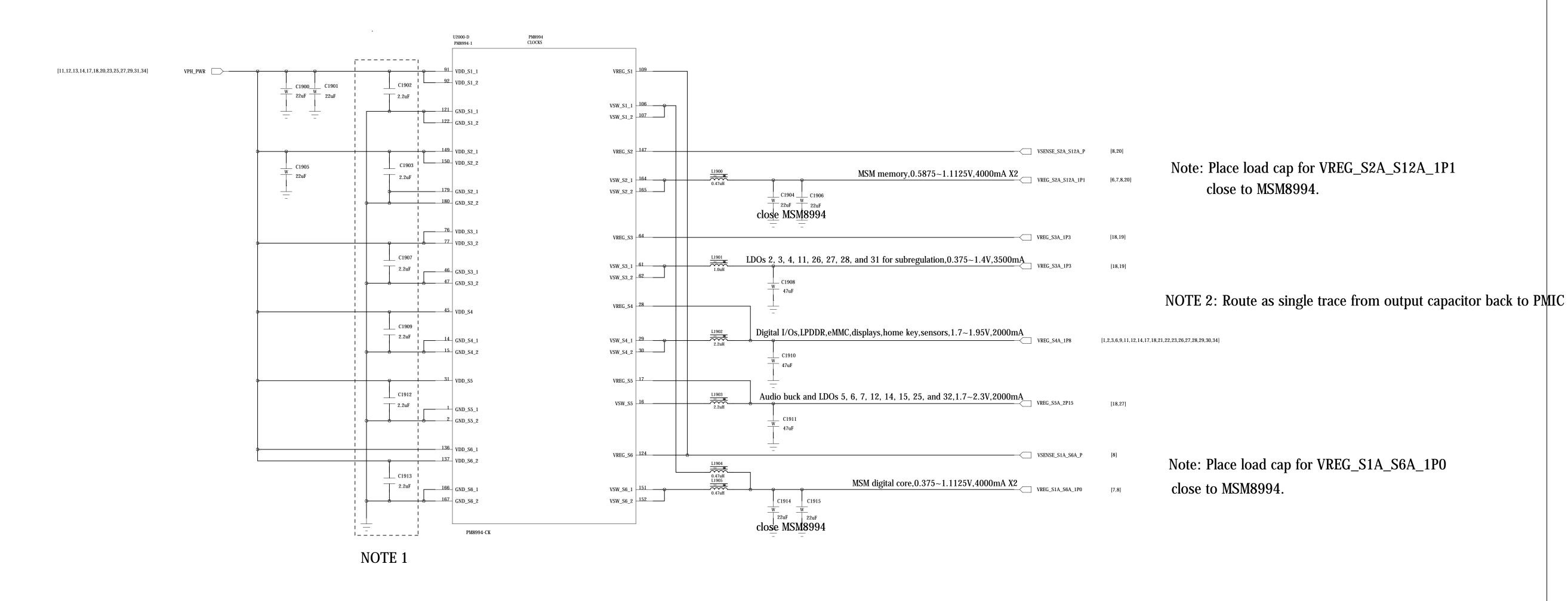
Note: The following LDOs are not considered pseudo-capless capable:

L1, L3, L5, L7, L26 and L31.

The remaining LDOs are considered pseudo-capless capable.

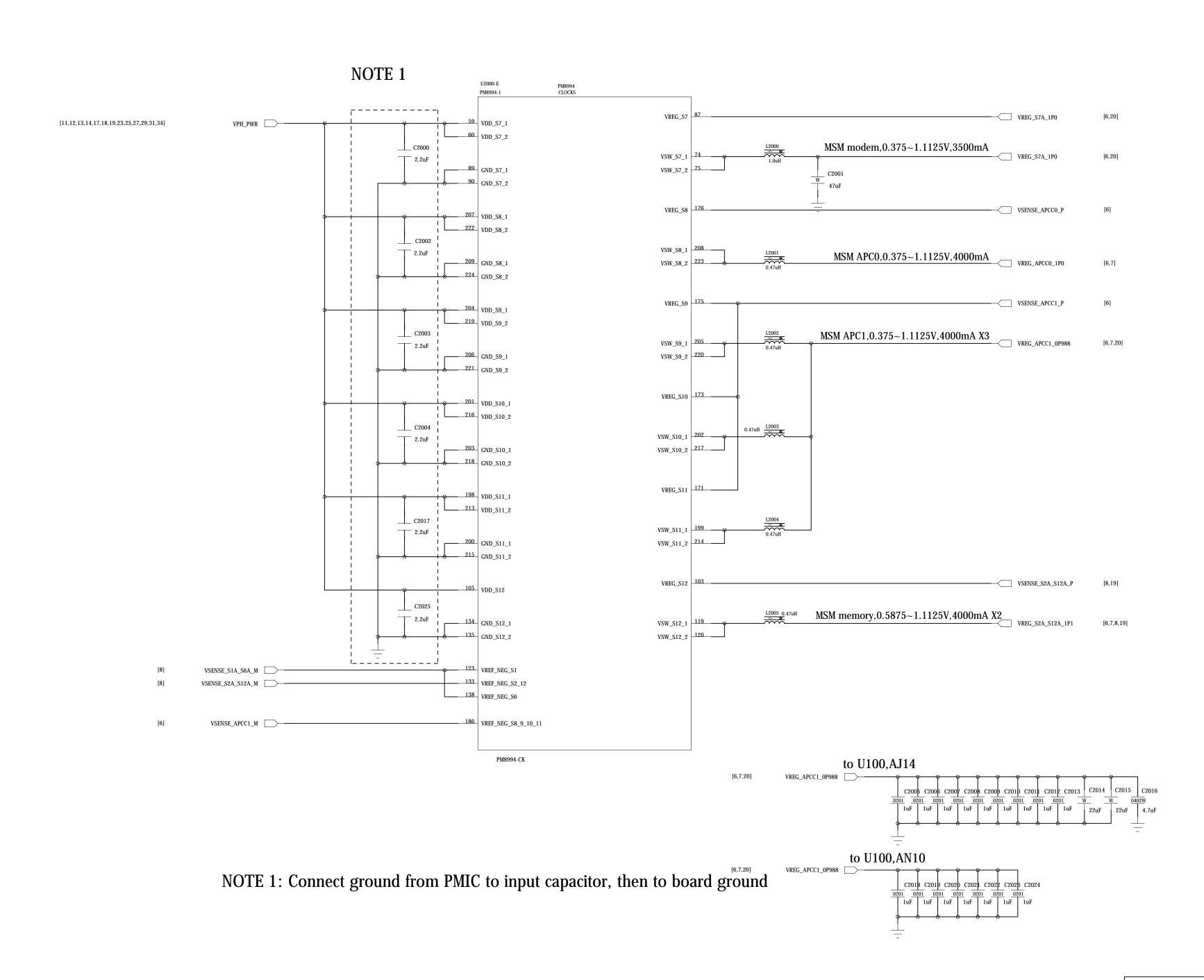
Note 1: If VREG_L25 is used, place a 4.7uF as output capacitor.

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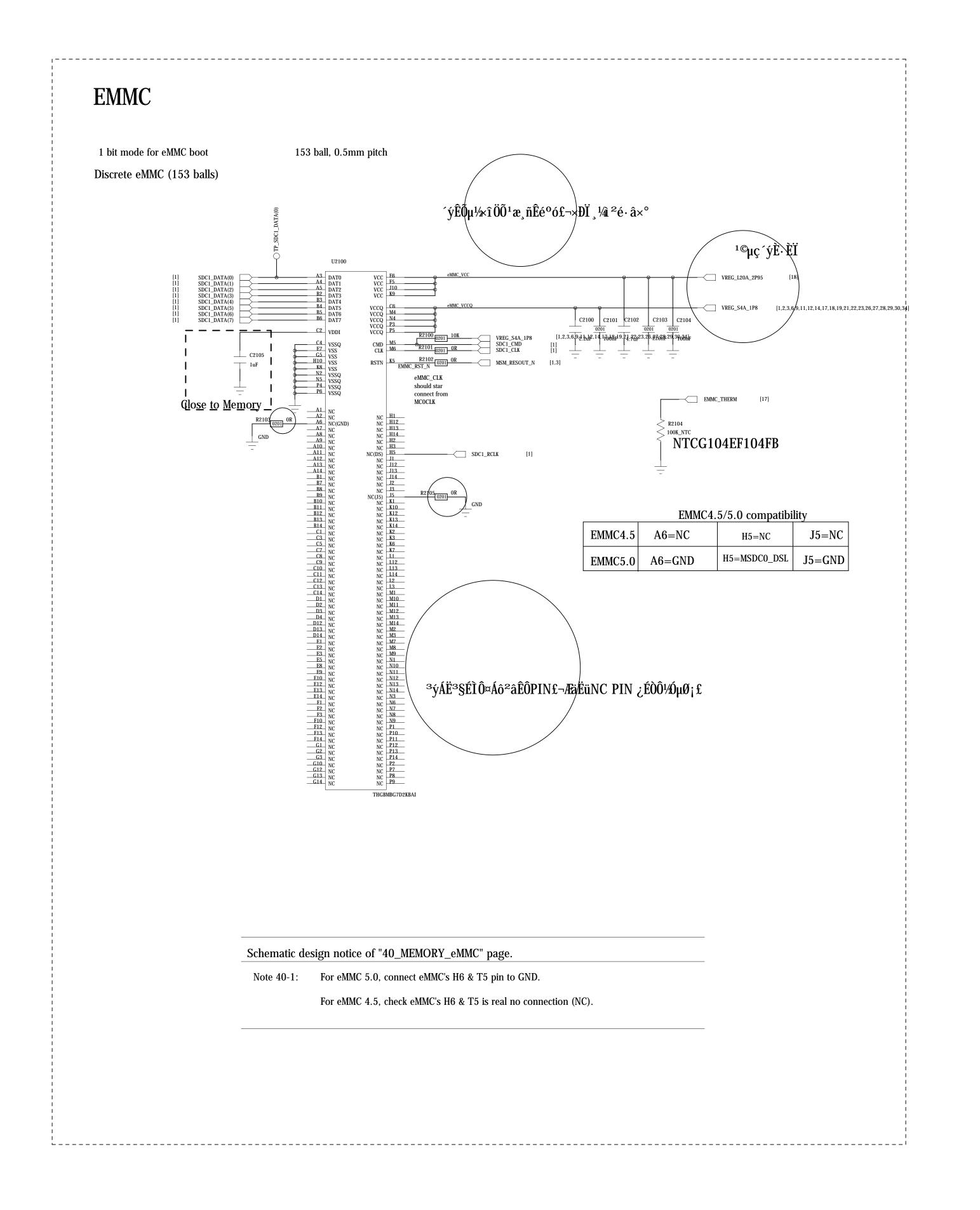


NOTE 1: Connect ground from PMIC to input capacitor, then to system ground

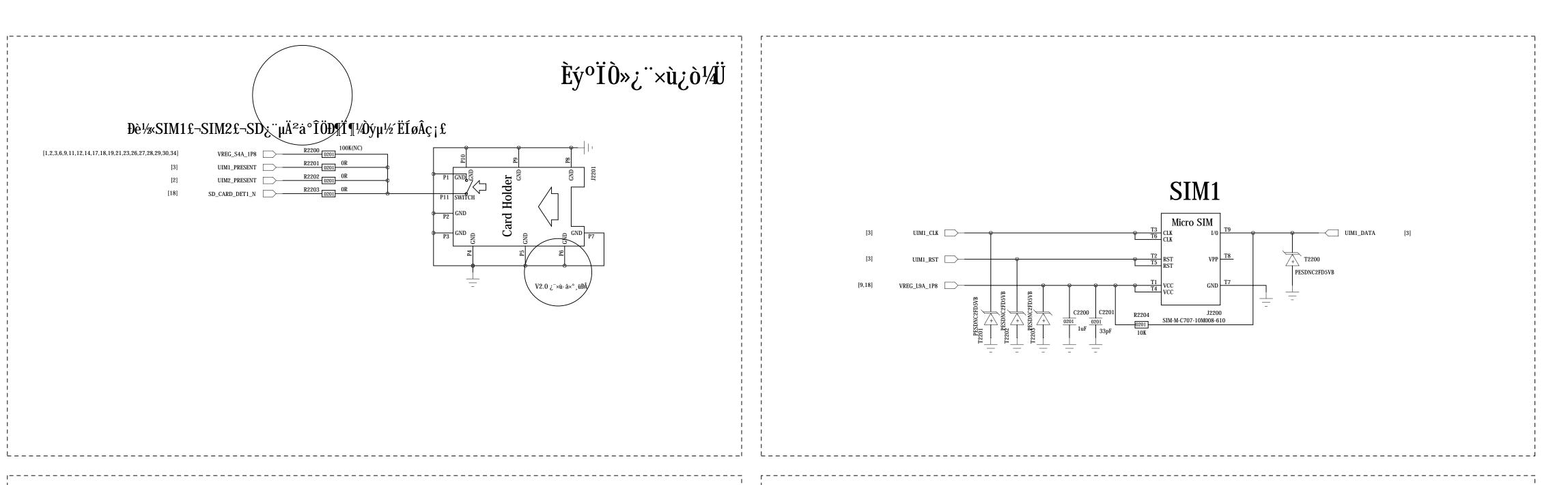
THIS DOCUMEN' INFORMATION AND S OR DISCLOSED TO O' WROTTEN PERMISSIO Communication Techno	T CONTAINS PROPRIETARY HALL NOT BE DUPLICATED THERS WITHOUT THE PRIO NN OF CK Techfaith ology Limited	R		e <mark>lecom</mark> Technology	' Limi	ted
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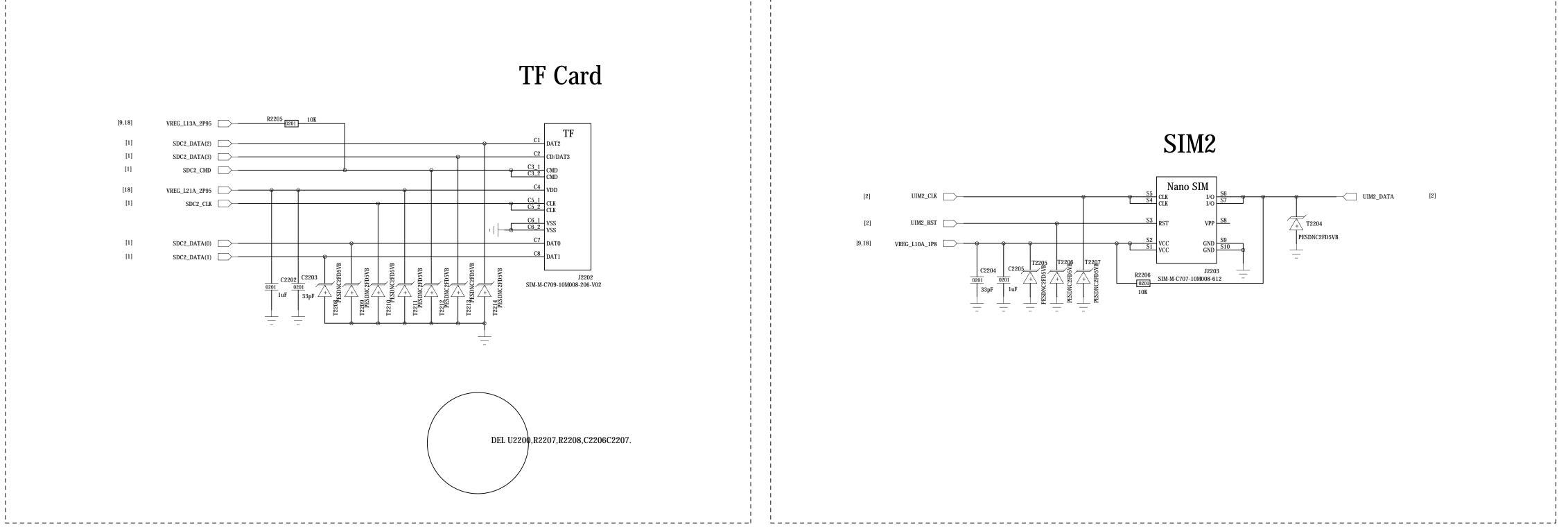


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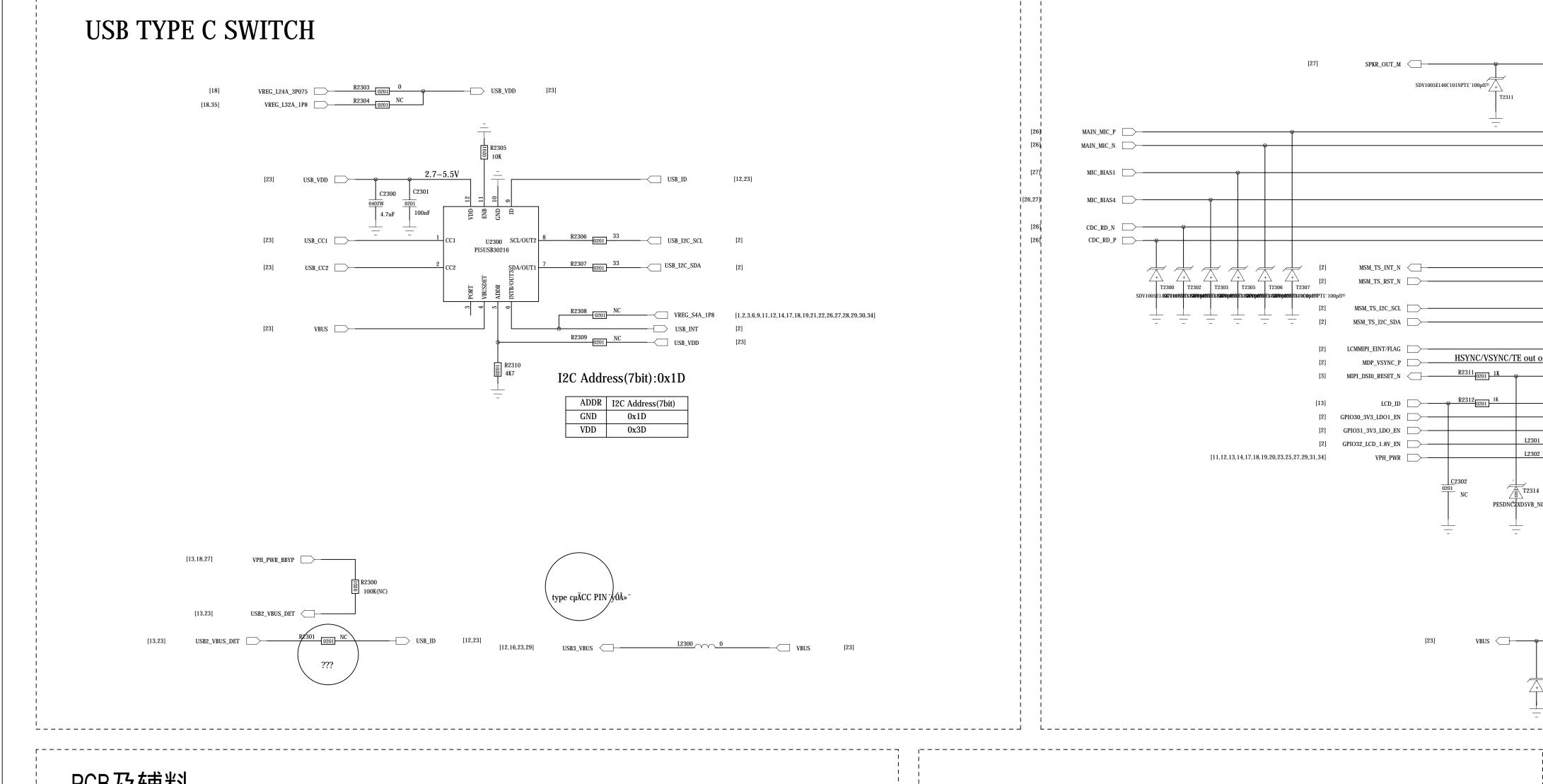


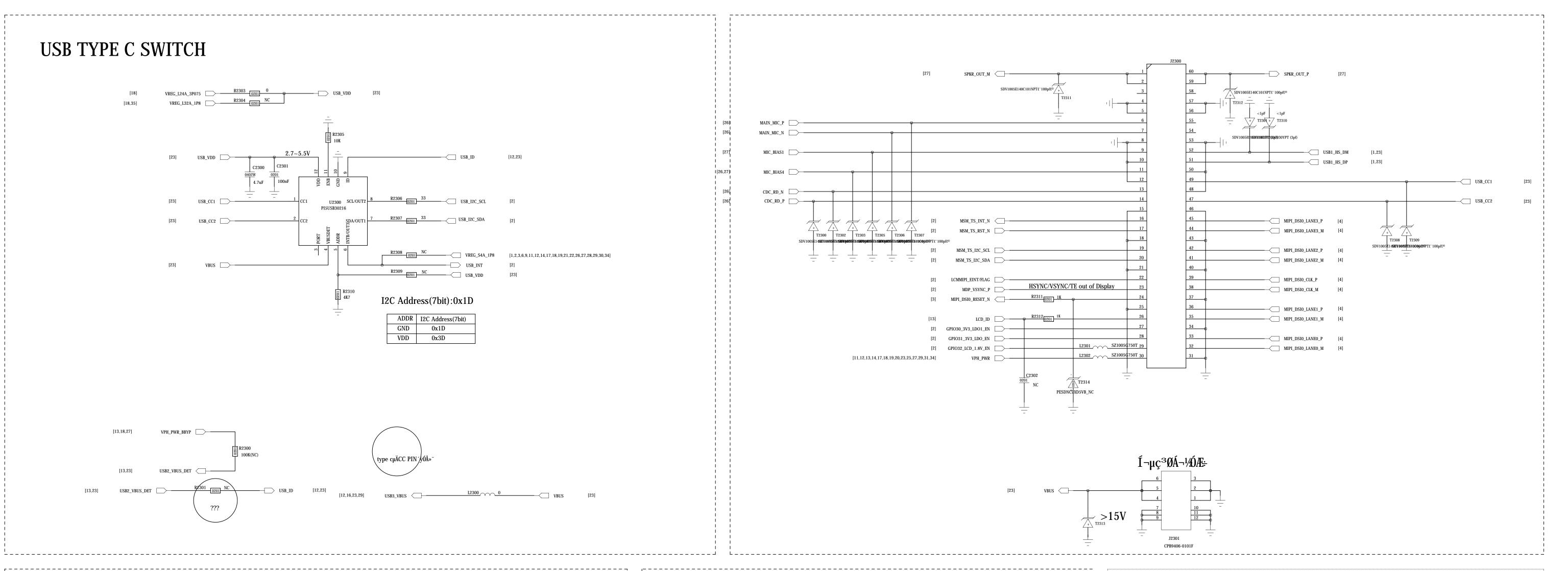
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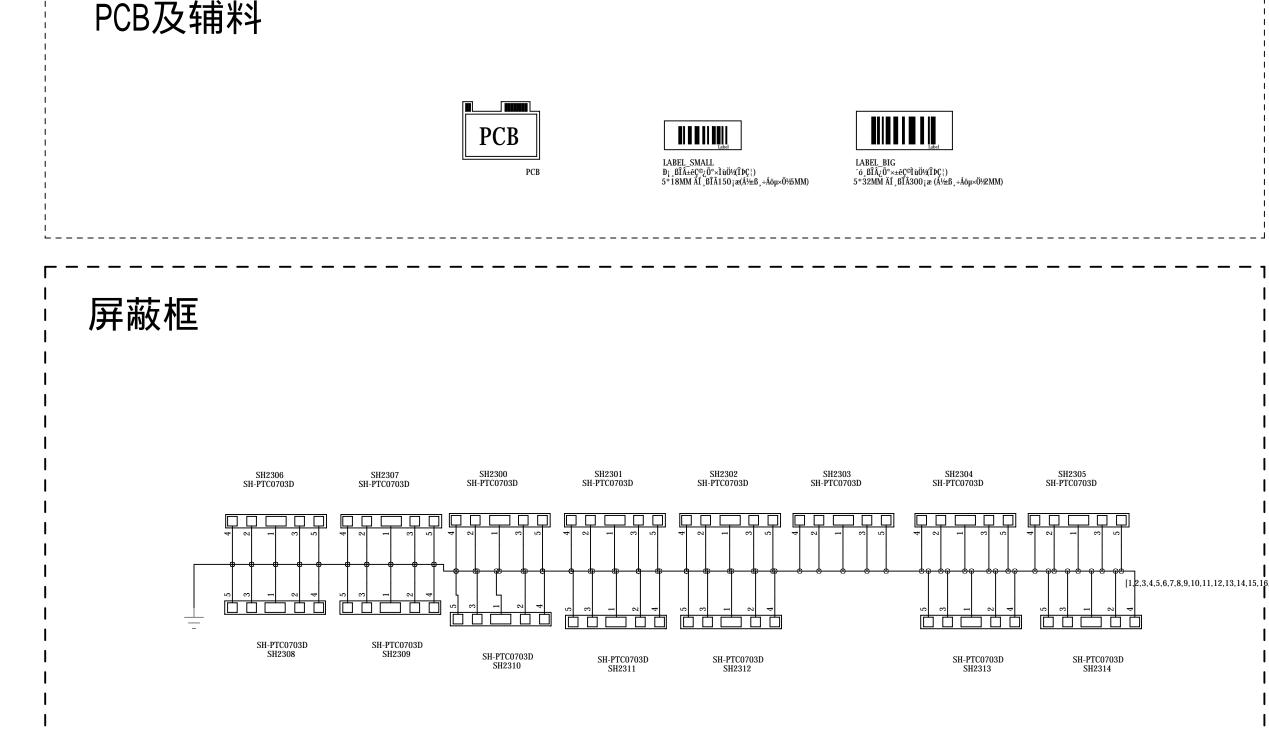


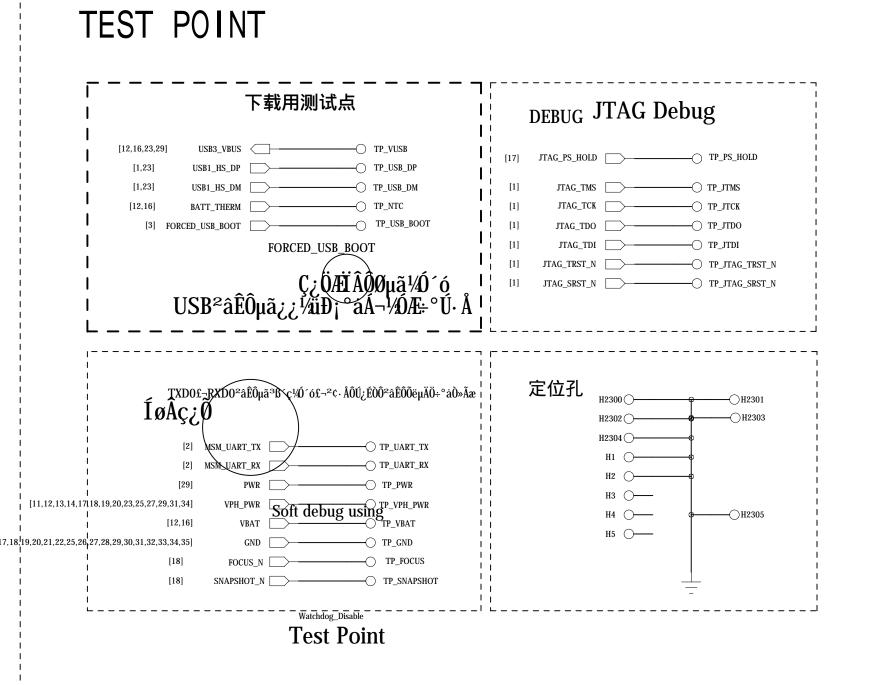


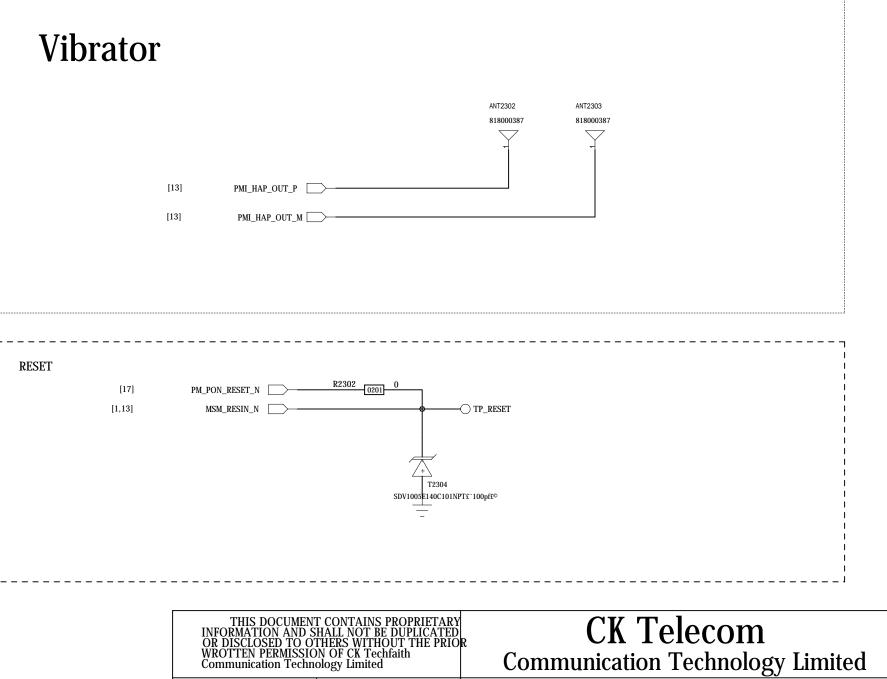
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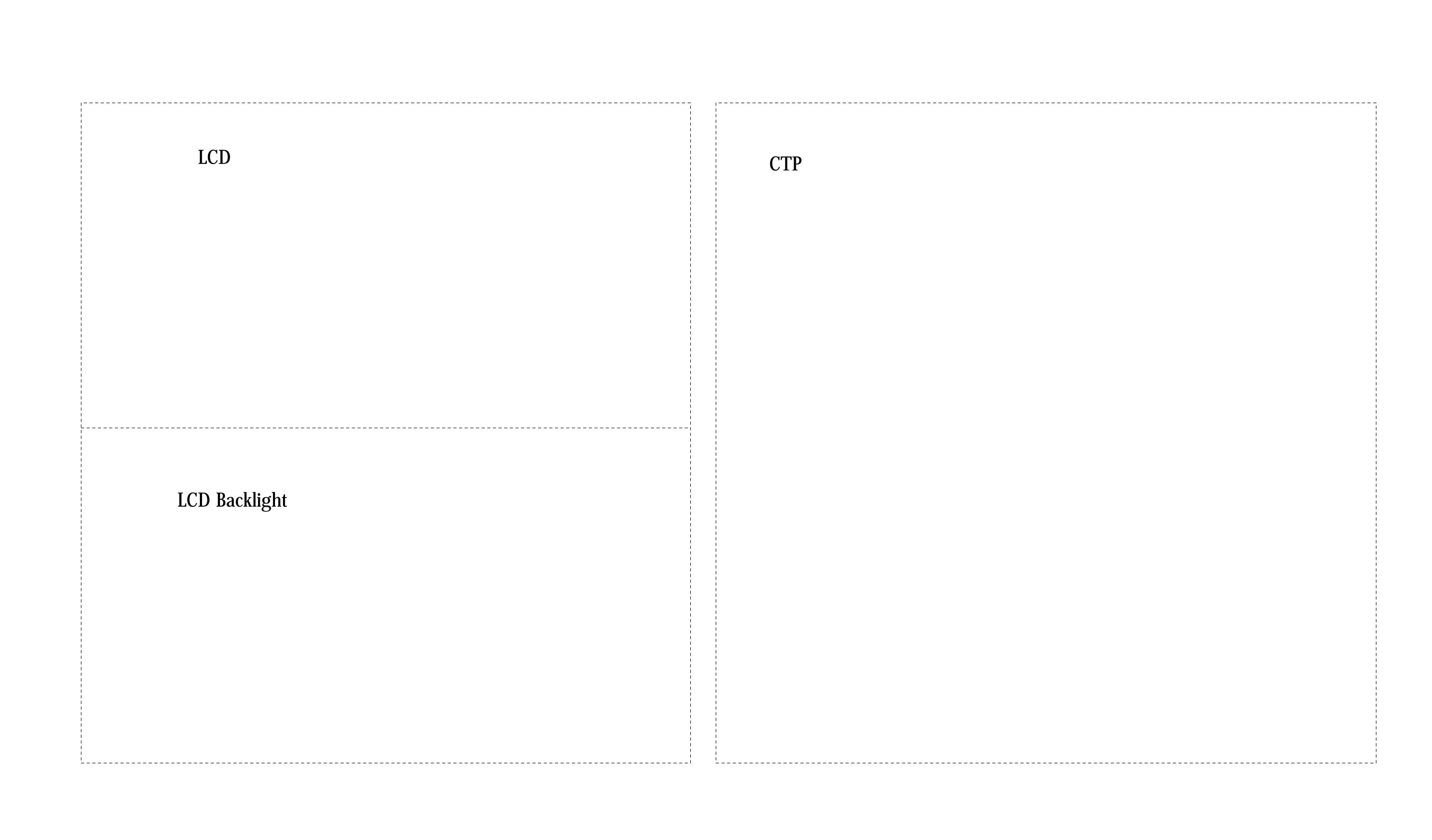
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RIMO

23--TYPE-C_USB_TEST_POINT

23 OF 36

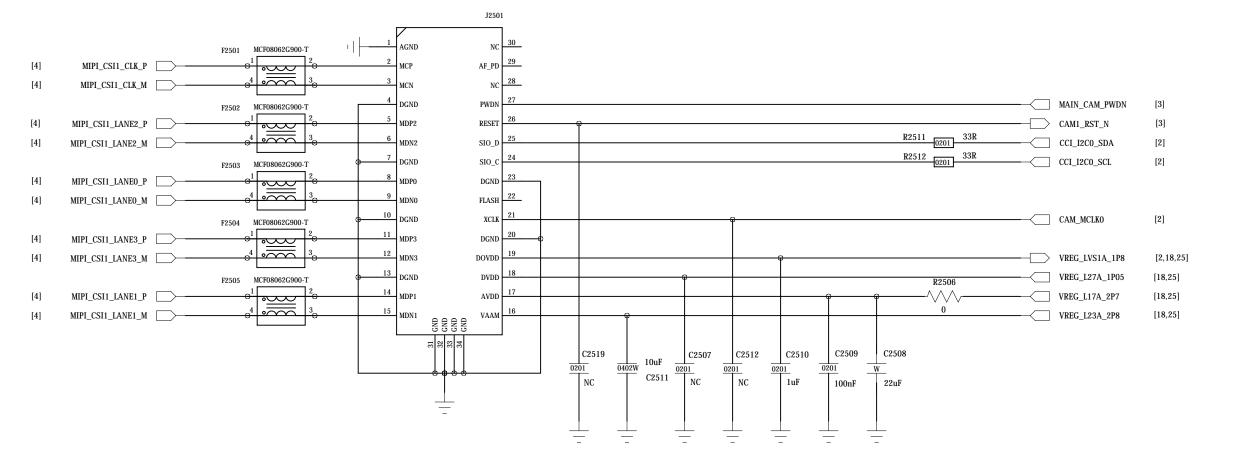


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Main Camera module

MAIN CAMERA CONNECOR

MIPI CSI1



I2C Address(7bit): 0x6C

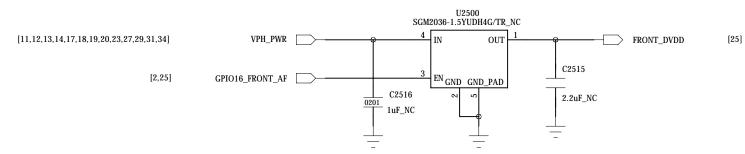
PM8994 camera power supply main module power supply

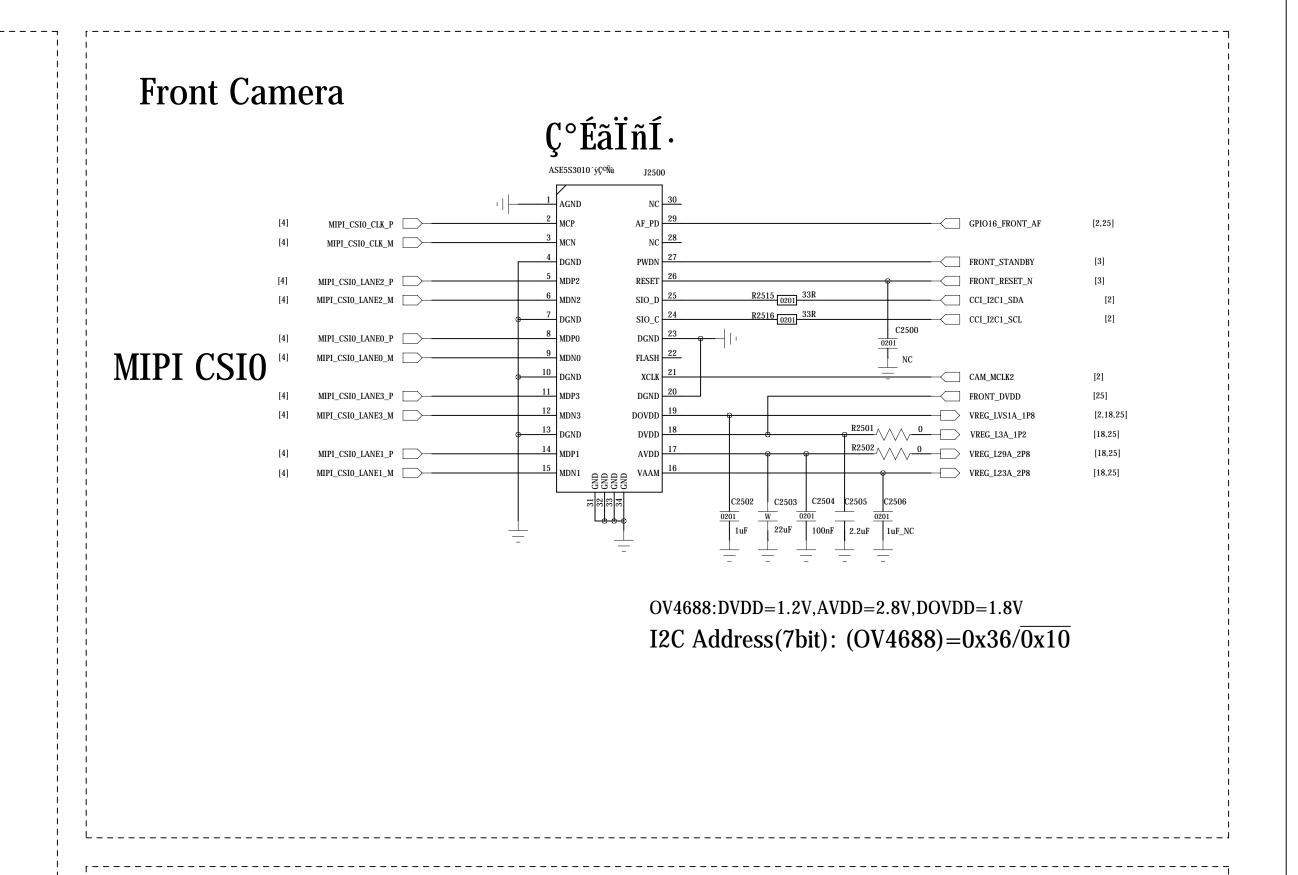
[2,18,25]	VREG_LVS1A_1P8 Camera DOVDD, 1.7~1.95V£¬300mA VREG_LVS1A_1P8	[2,18,25]	DOVDD
[18,25]	VREG_L27A_1P05 Camera front DVDD, 0.95~1.3V£¬300mA VREG_L27A_1P05	[18,25]	Main DVDD
[18,25]	VREG_L17A_2P7 Camera front and third analog, 1.6~3.6V,300mA VREG_L17A_2P7 Camera proin analog, 1.6~2.3.6V,600mA VREG_L17A_2P7	[18,25]	Main AVDD
[18,25]	VREG_L23A_2P8 Camera main analog, 1.62~3.6V,600mA VREG_L23A_2P8	[18,25]	AF VDD

Module power supply

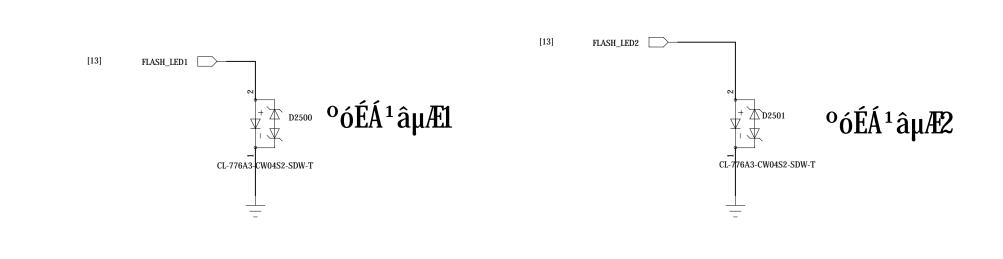
Front and Third module power supply

[2,18,25]	VREG LVS1A 1P8	Camera DOVDD, 1.7~1.95V£¬300mA	[2,18,25]	DOVDD
		Camera main DVDD, 0.9~1.3Vf¬1200mA		DOIDD
[18,25]	VREG_L3A_1P2	Camera VCM analog, 1.62~3.6V,300mA	[18,25]	Front DVDD
[18,25]	VREG_L29A_2P8	VREG L29A 2P8	[18,25]	Front AVDD
[18,25]	VREG_L23A_2P8	Camera main analog, 1.62~3.6V,600mA VREG_L23A_2P8	[18,25]	AF VDD

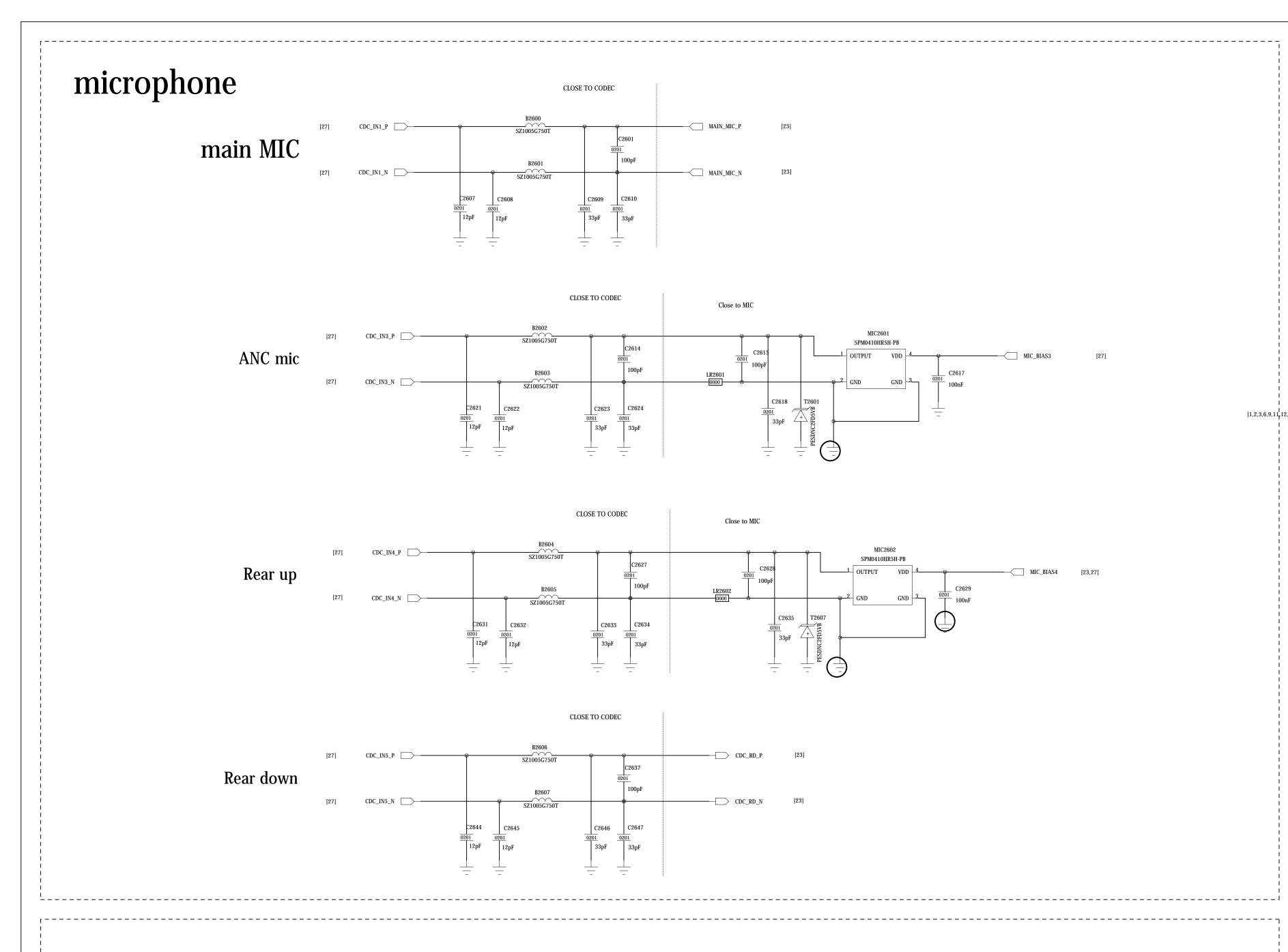




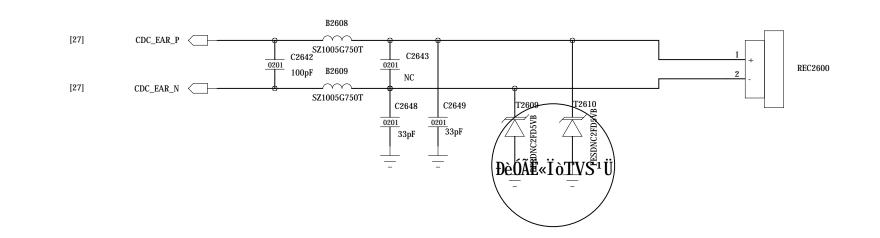




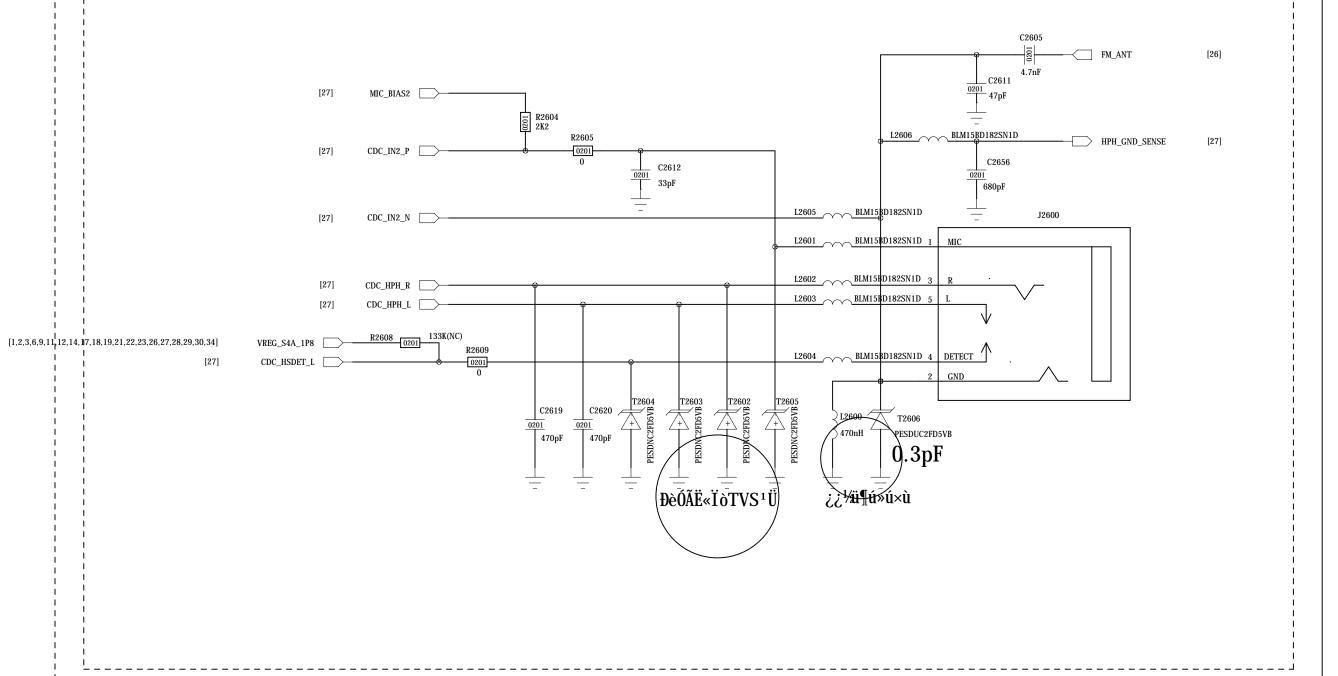
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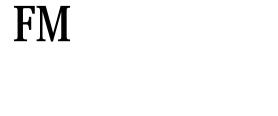


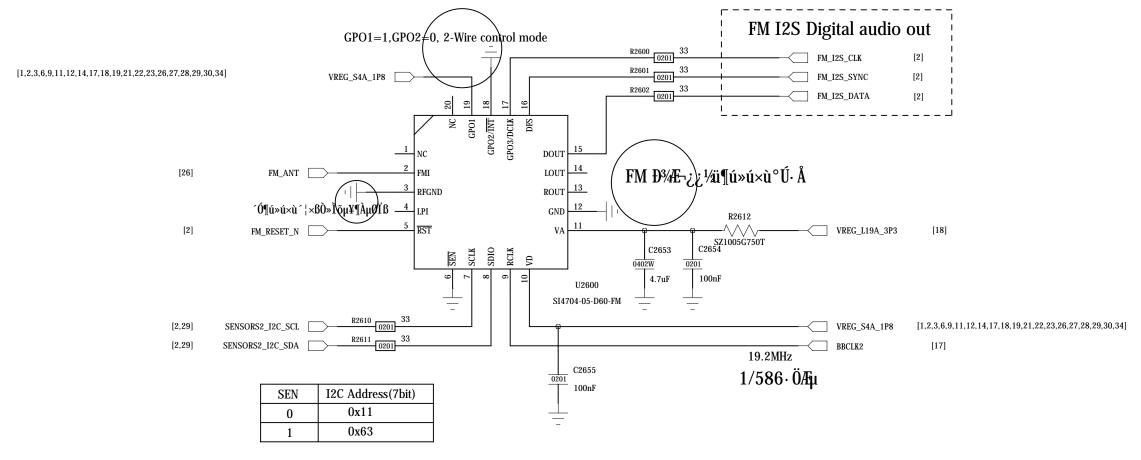
RECEIVER



Audio Jack

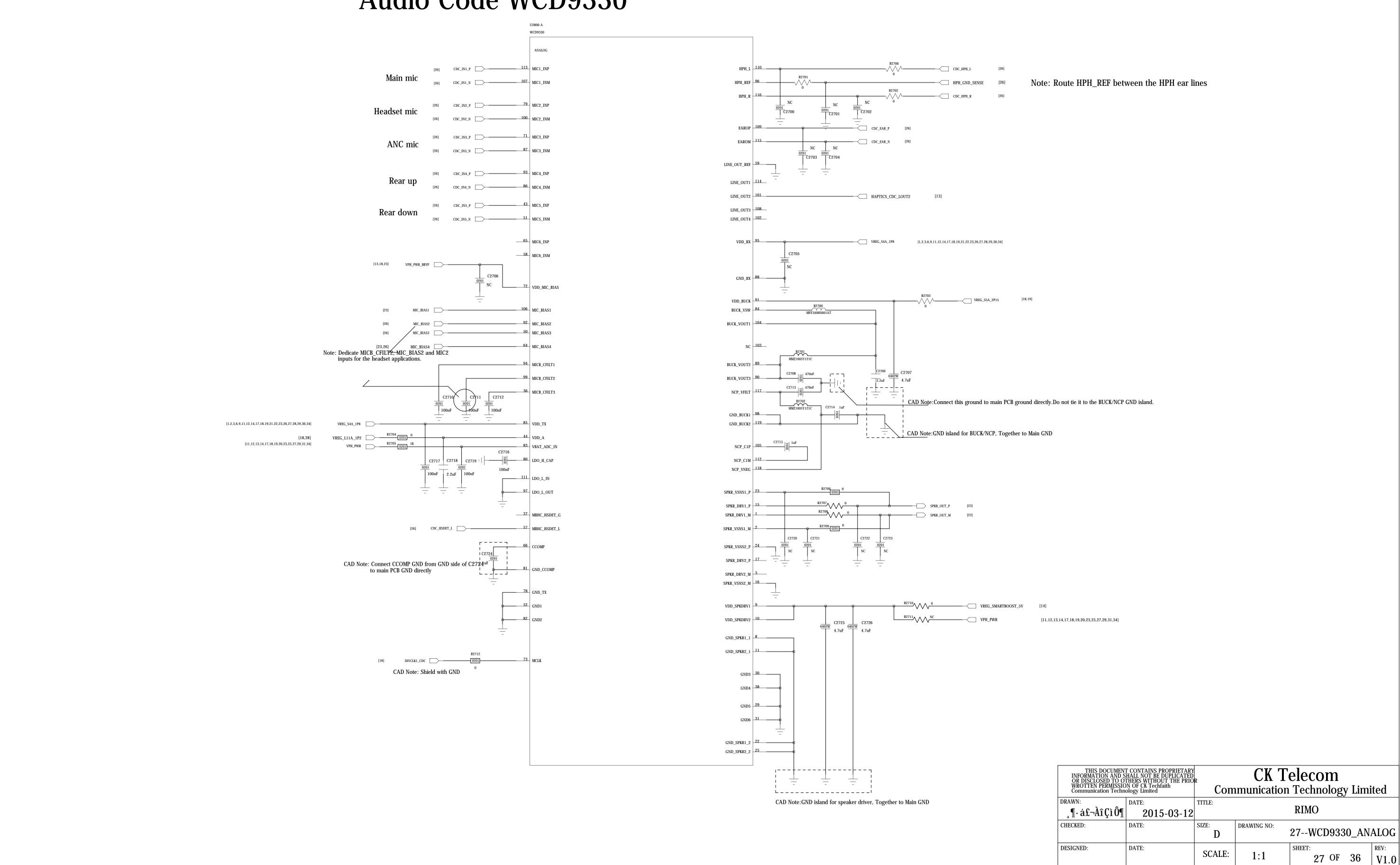


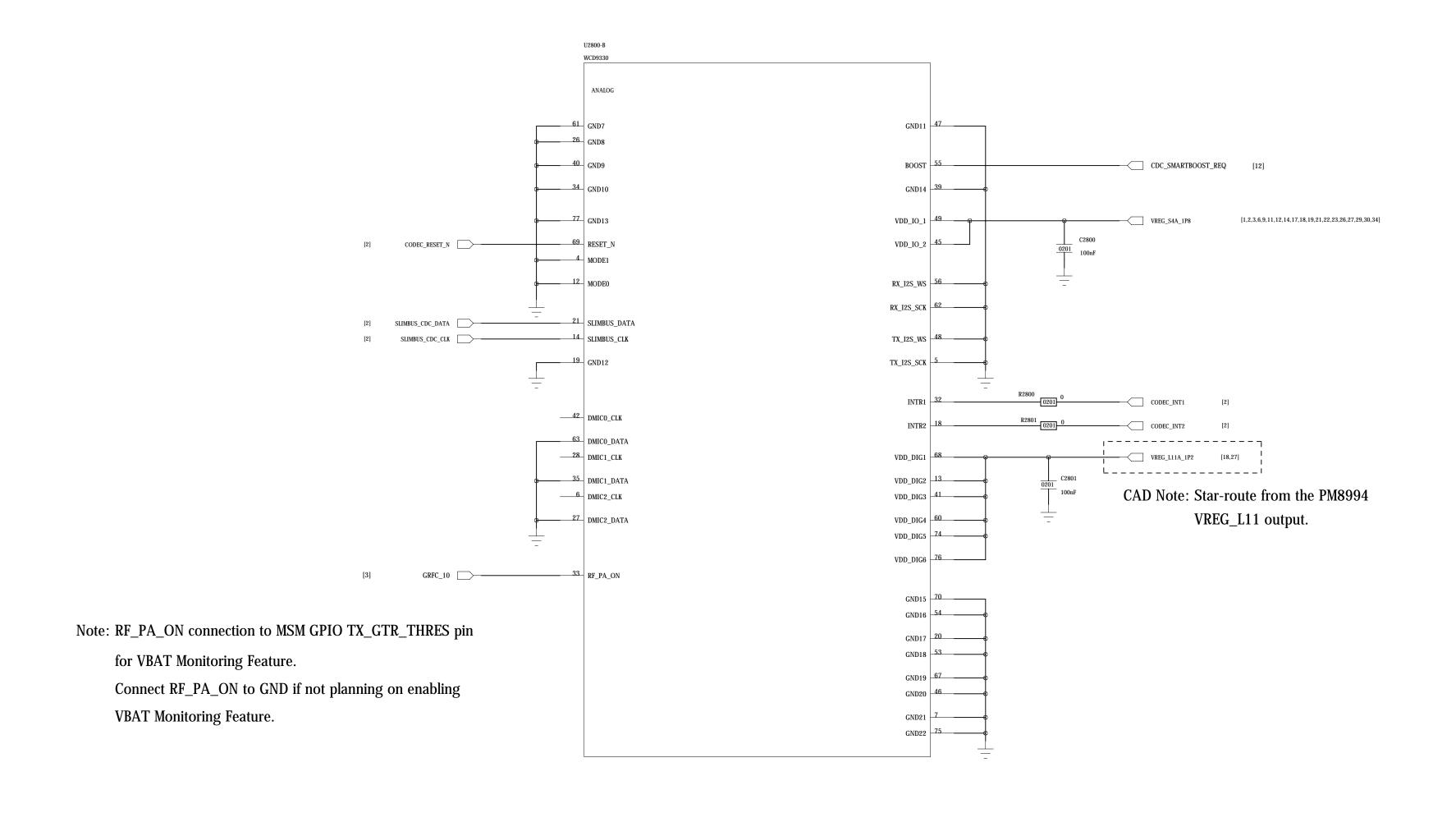




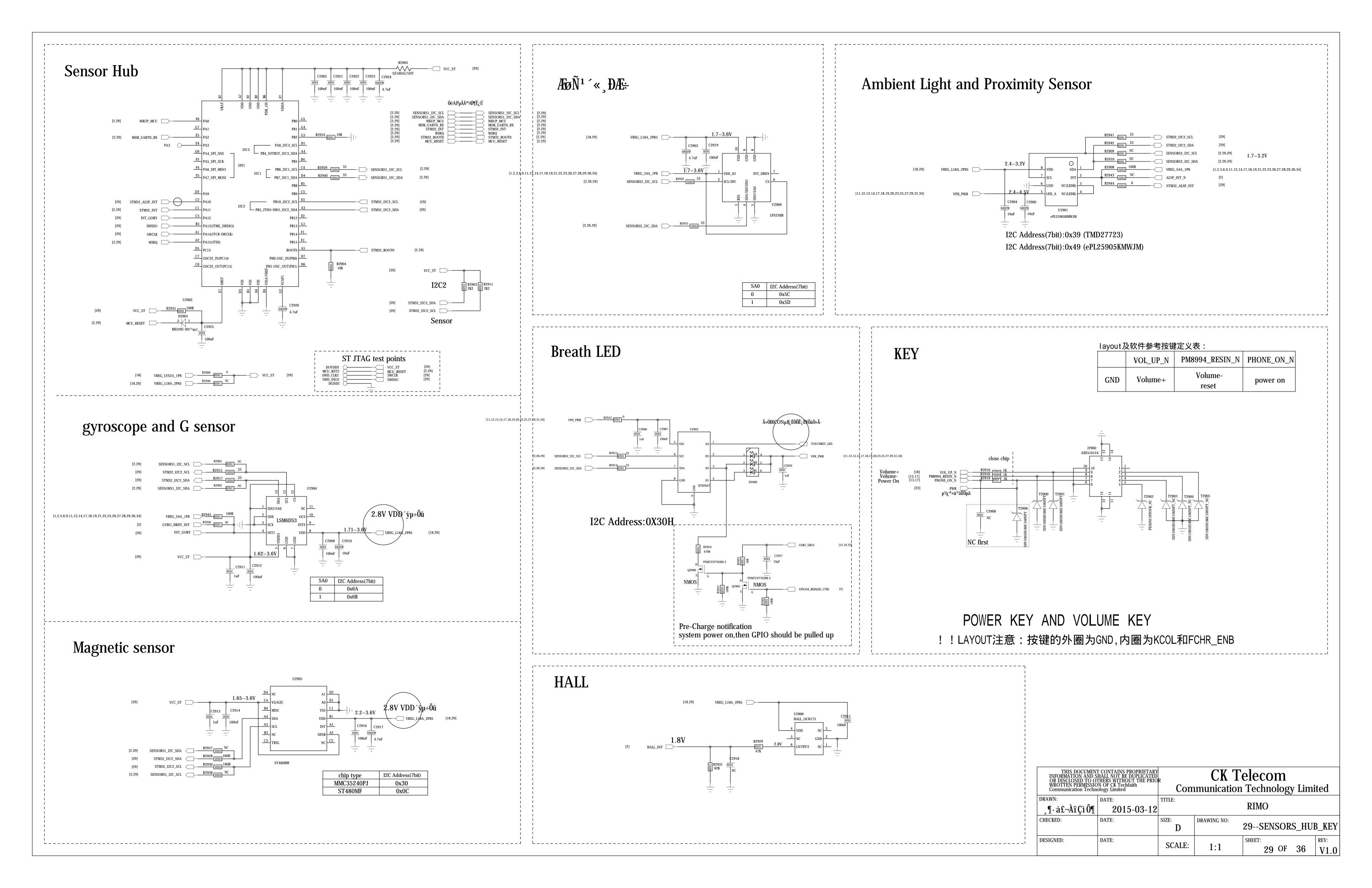
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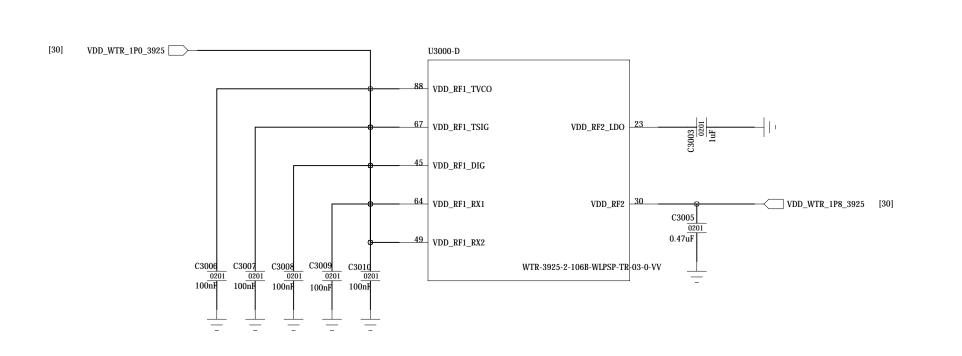


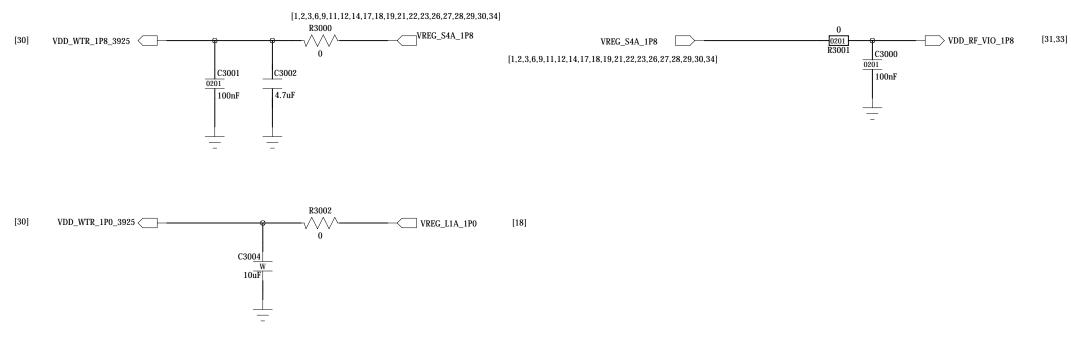


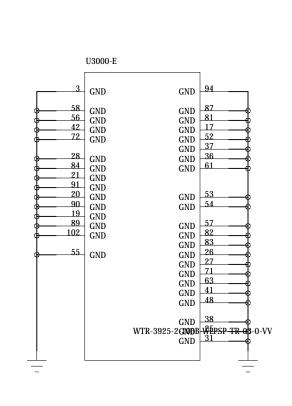
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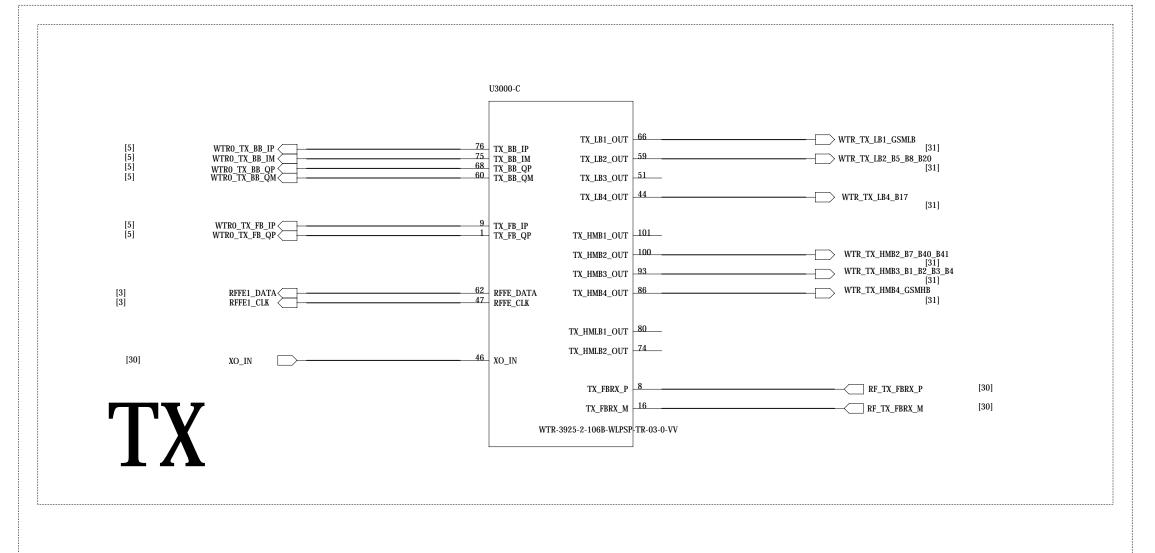


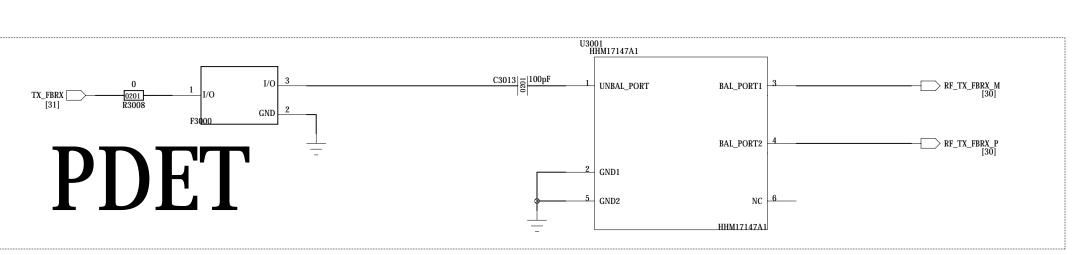


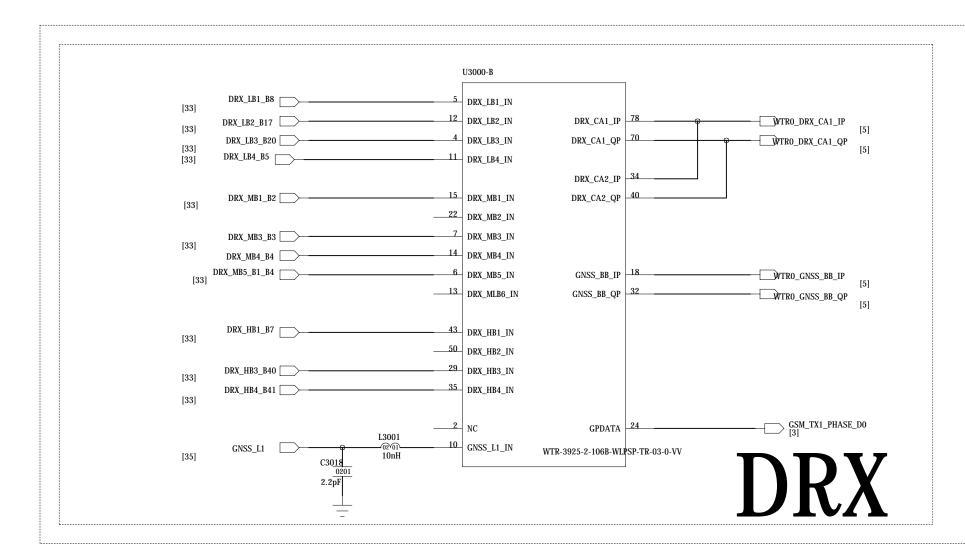


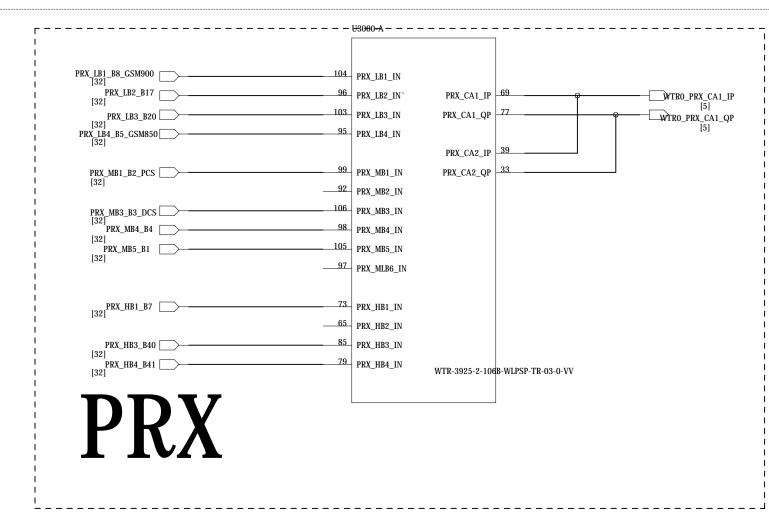


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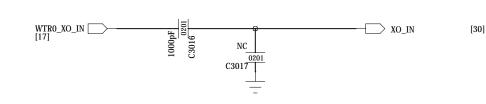




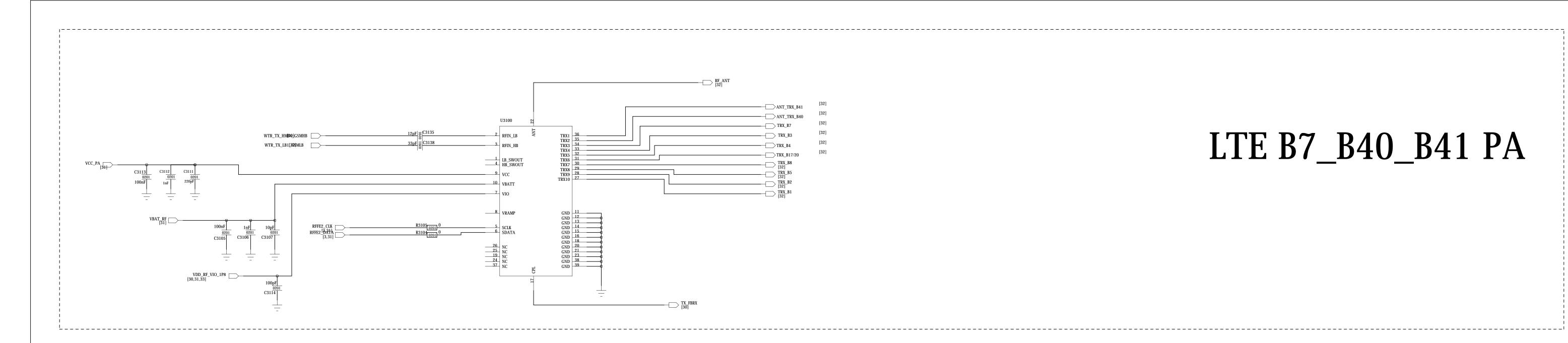




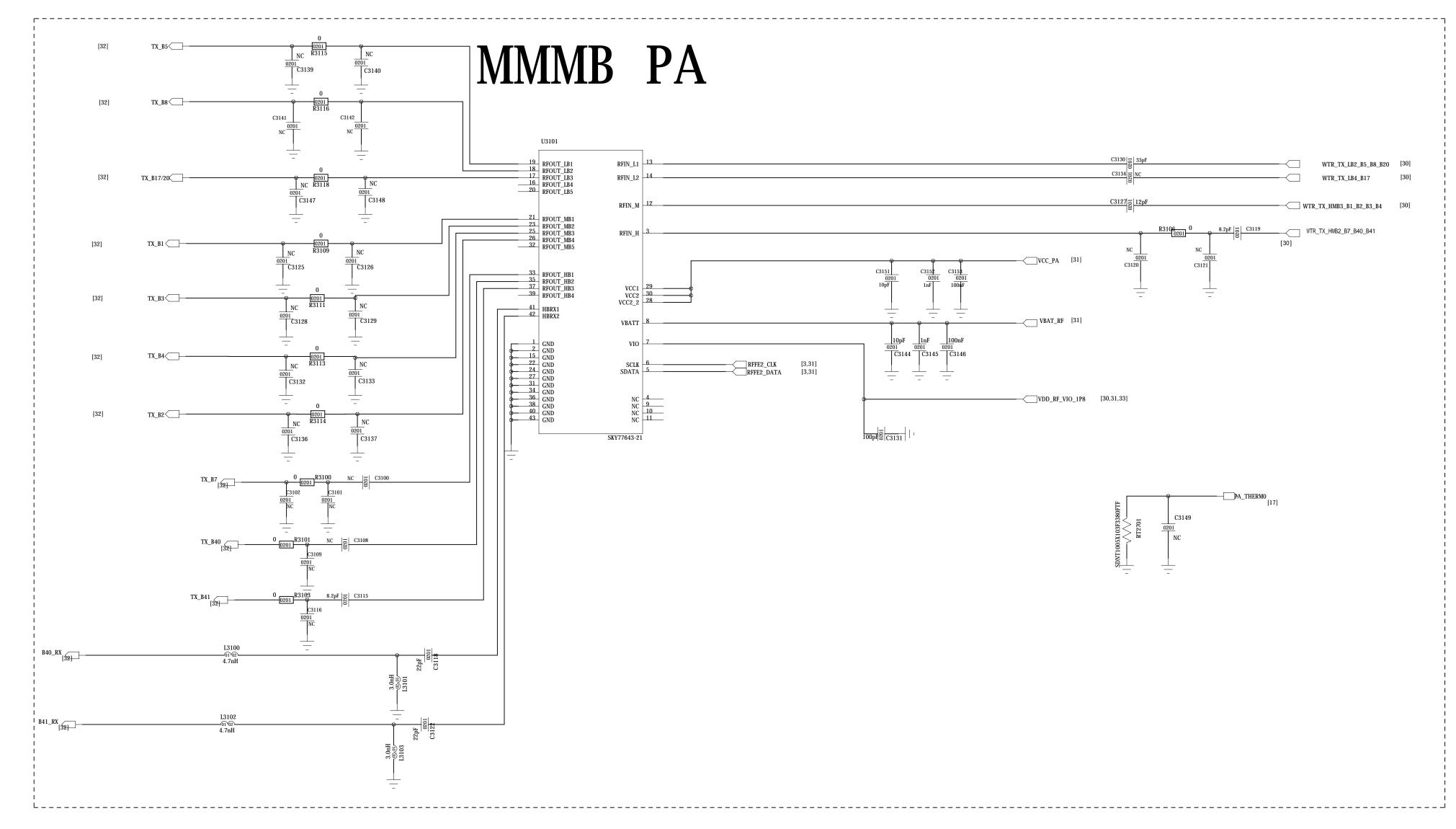
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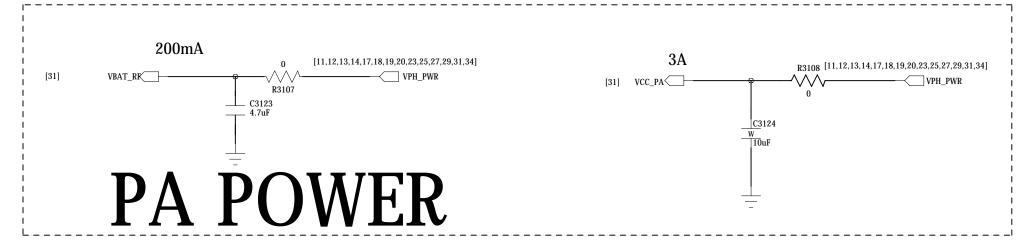


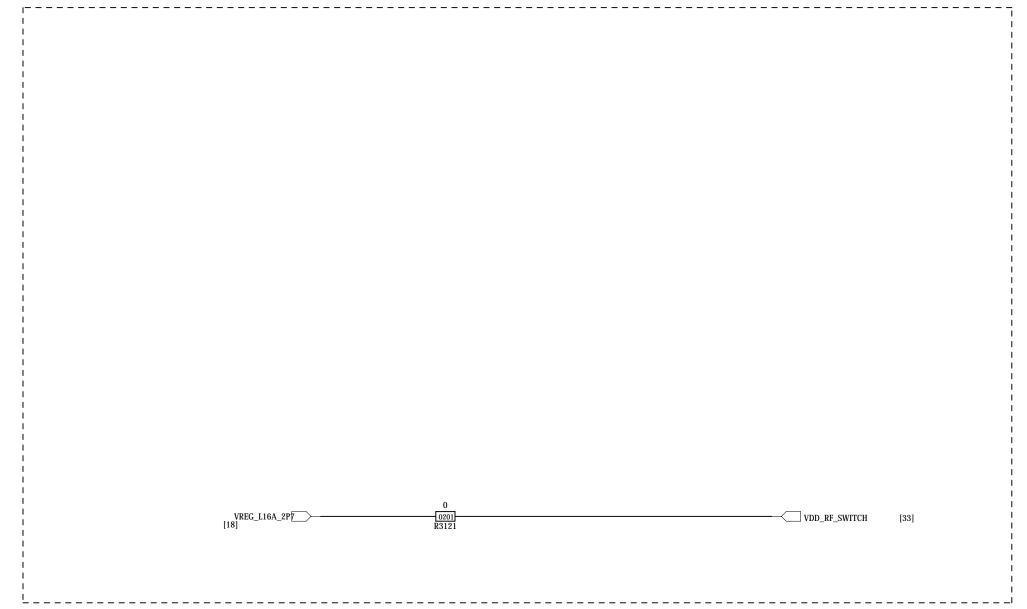
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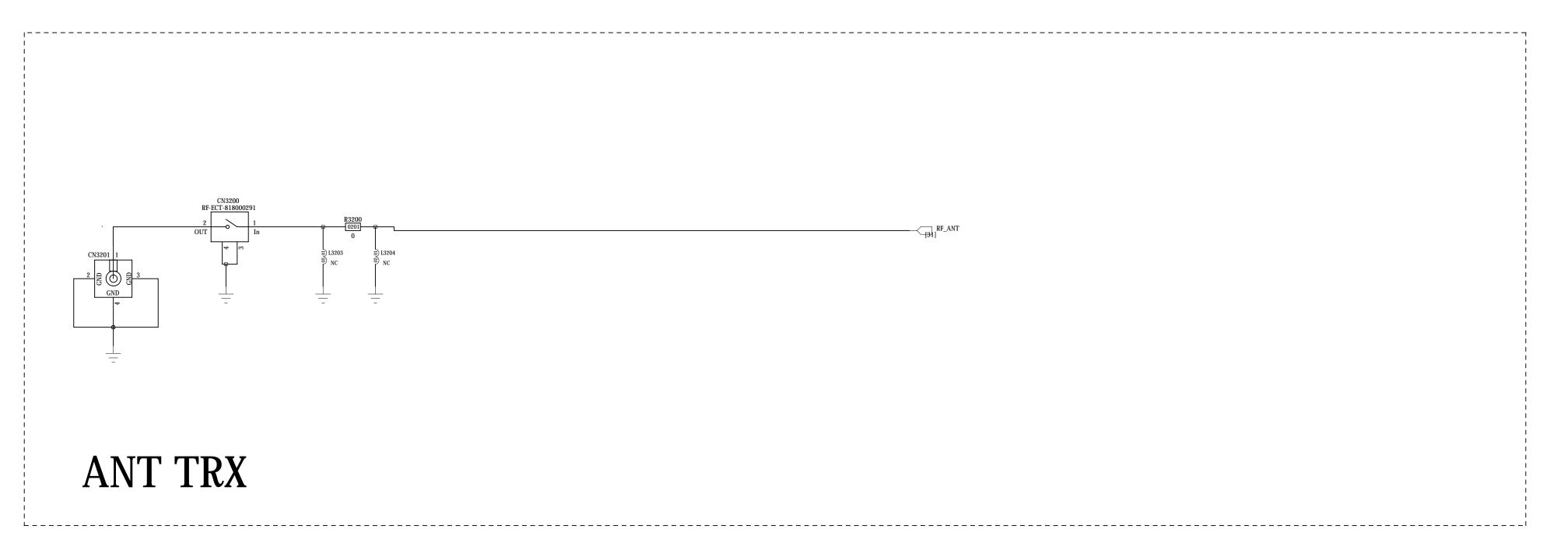
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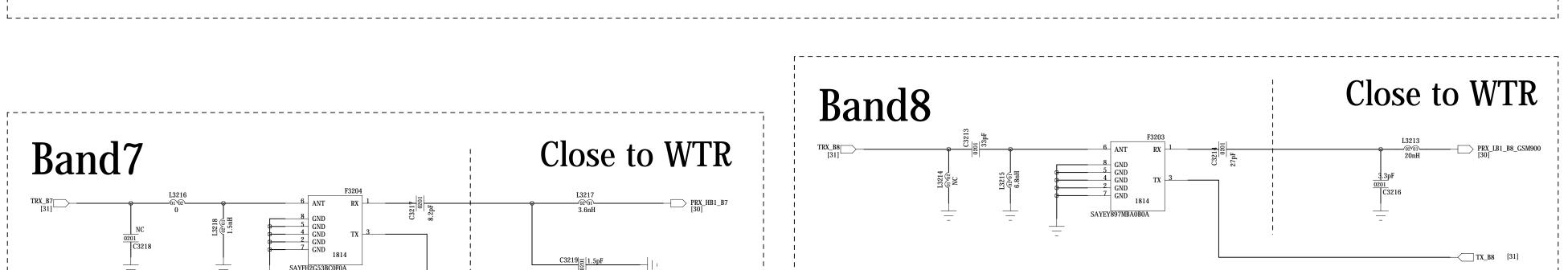


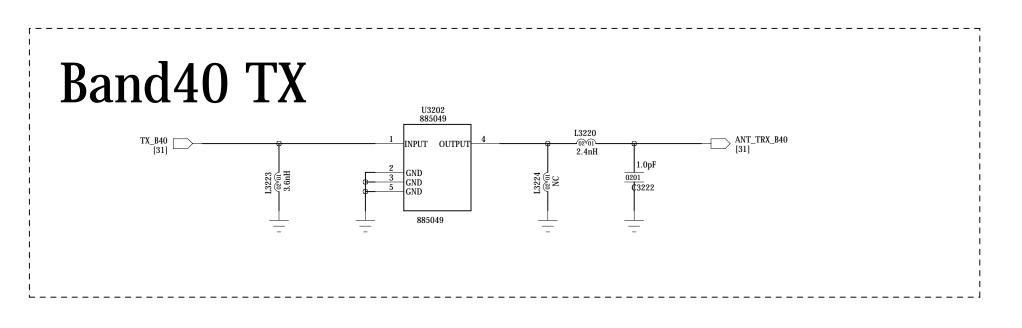


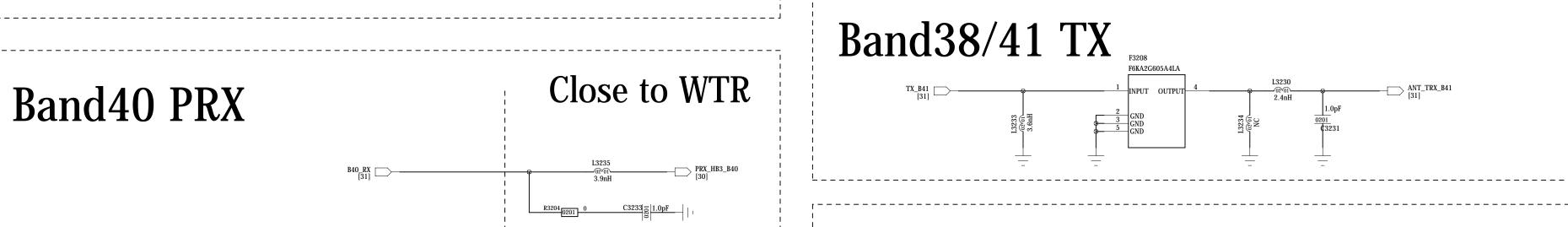
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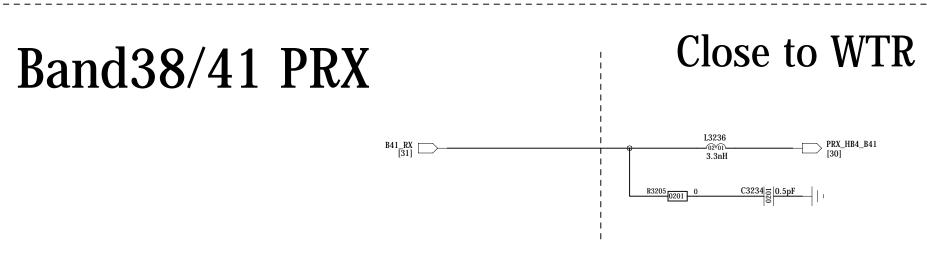
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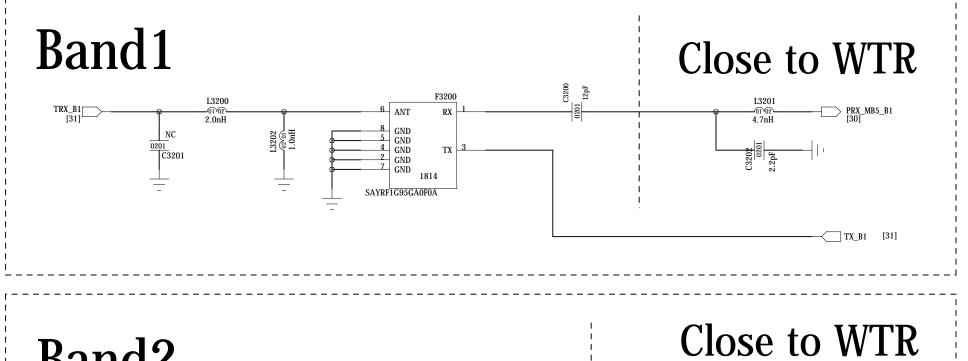


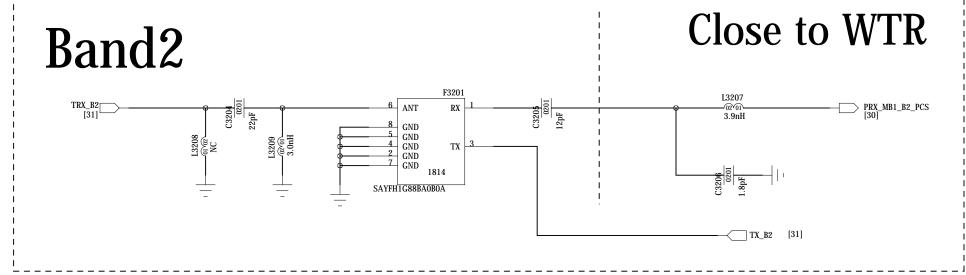


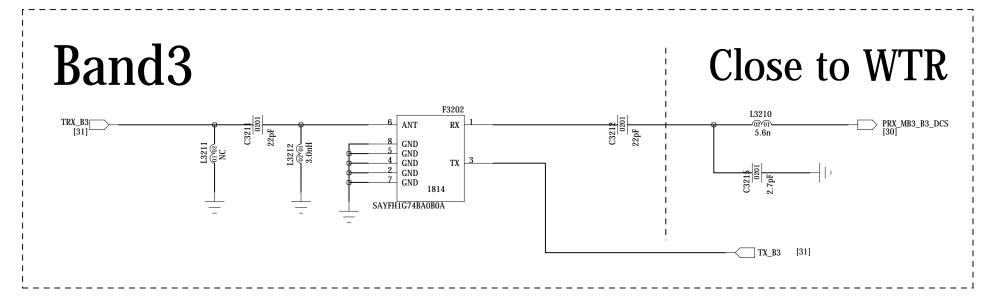


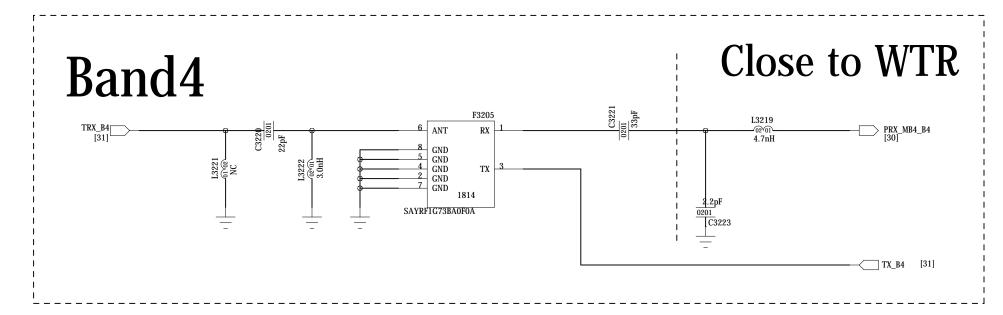
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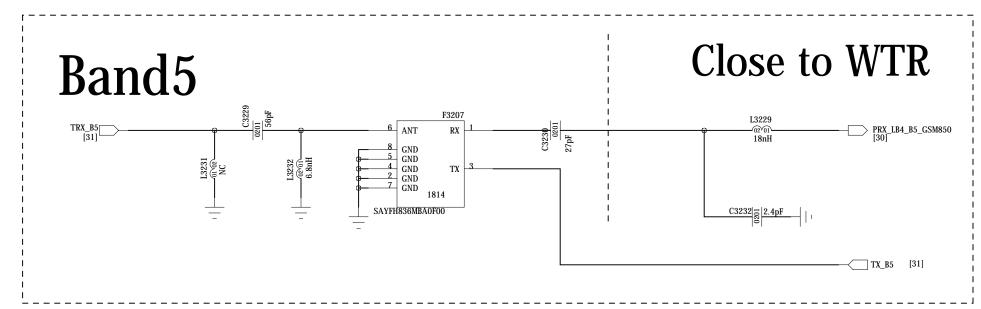




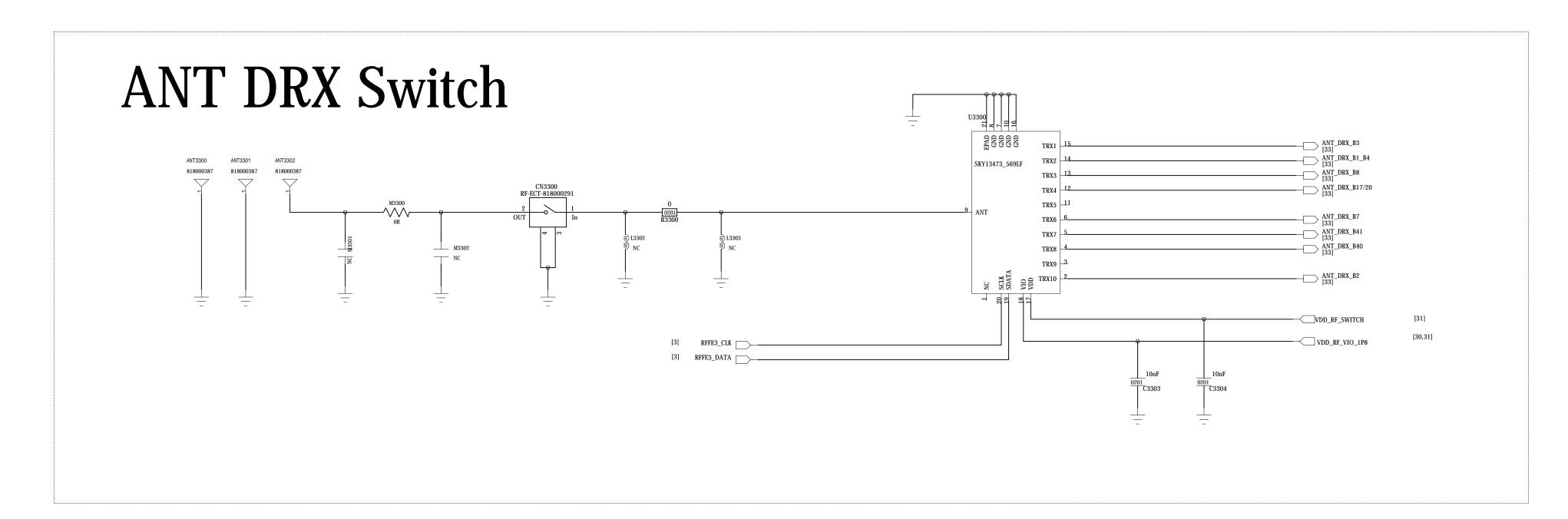


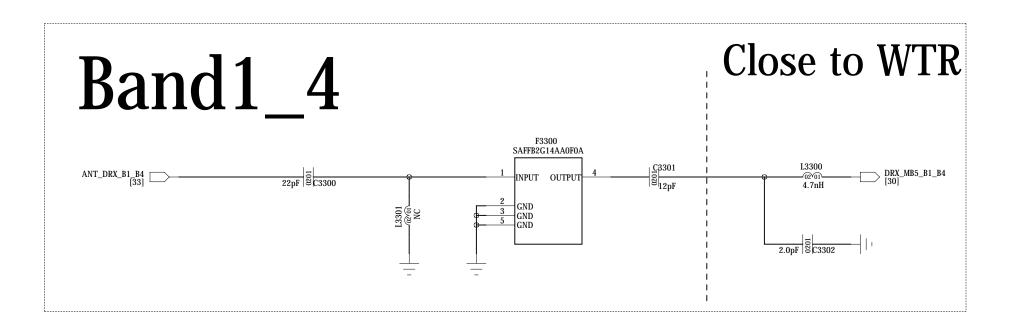


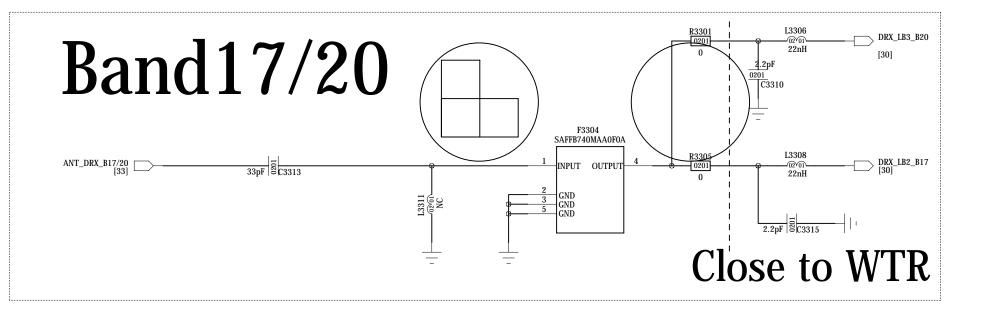


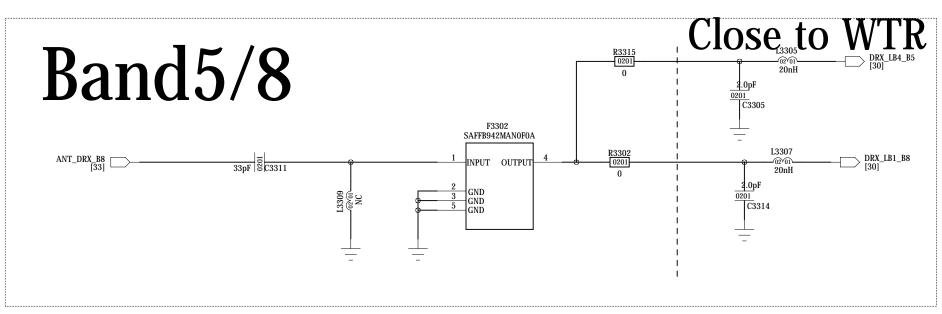


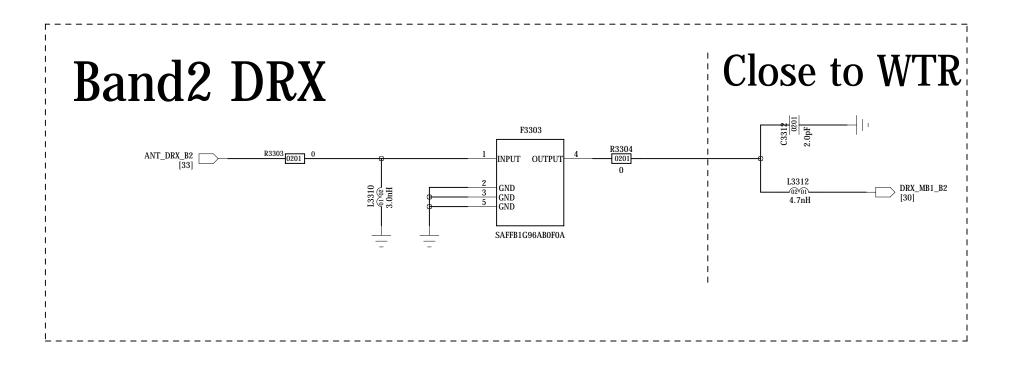
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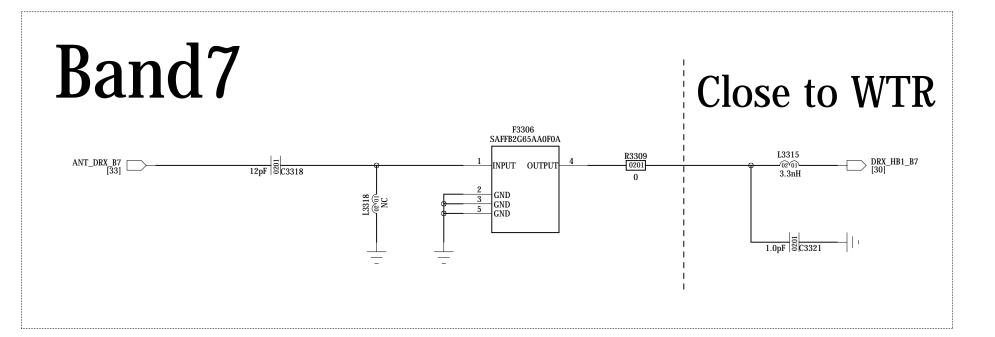


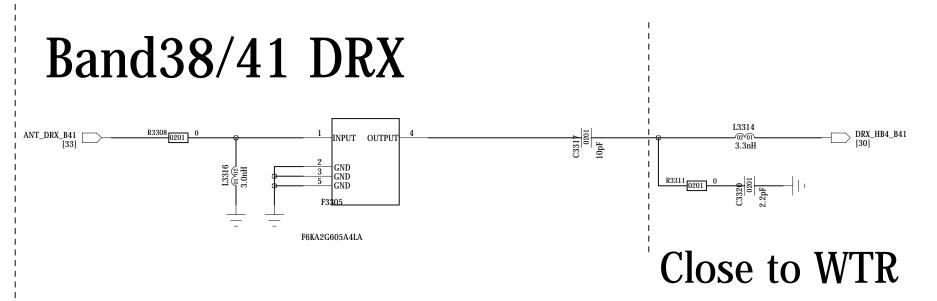


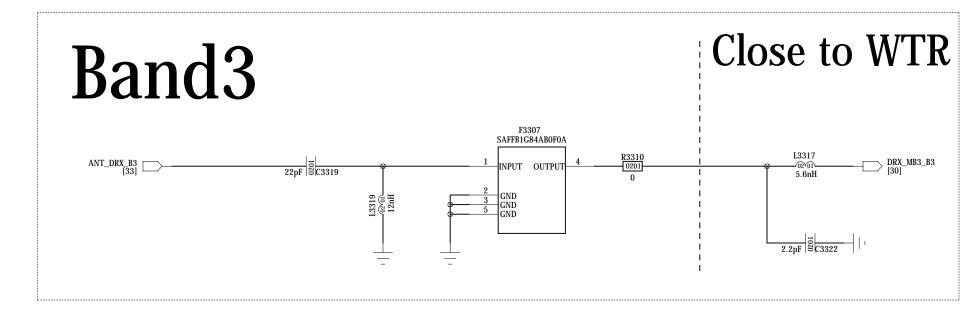


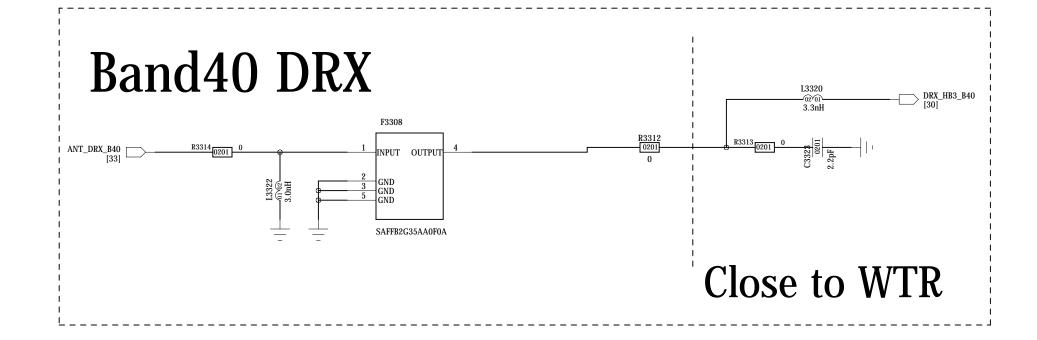




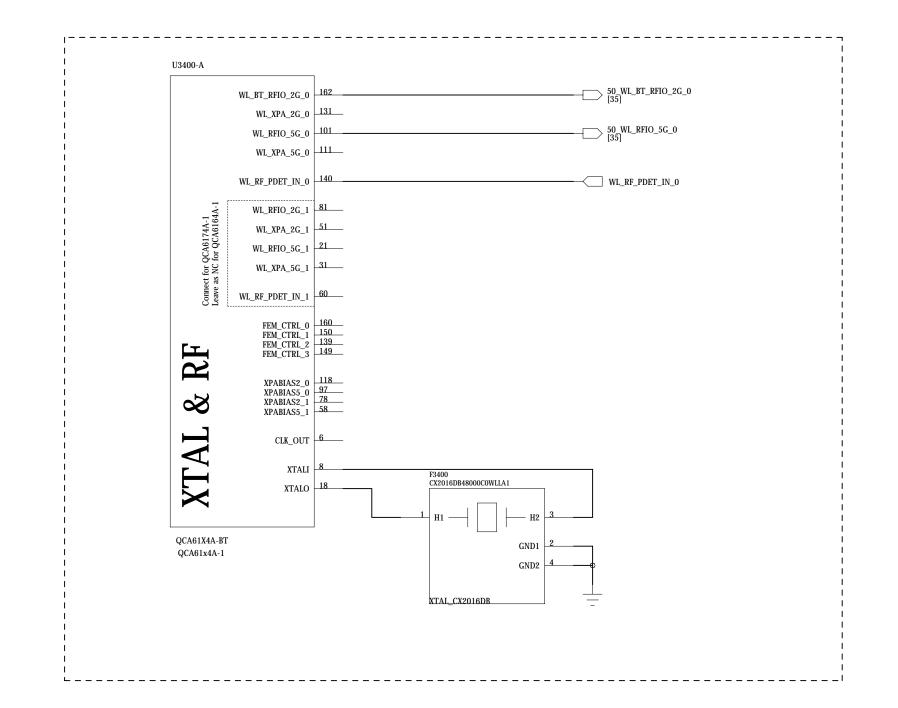


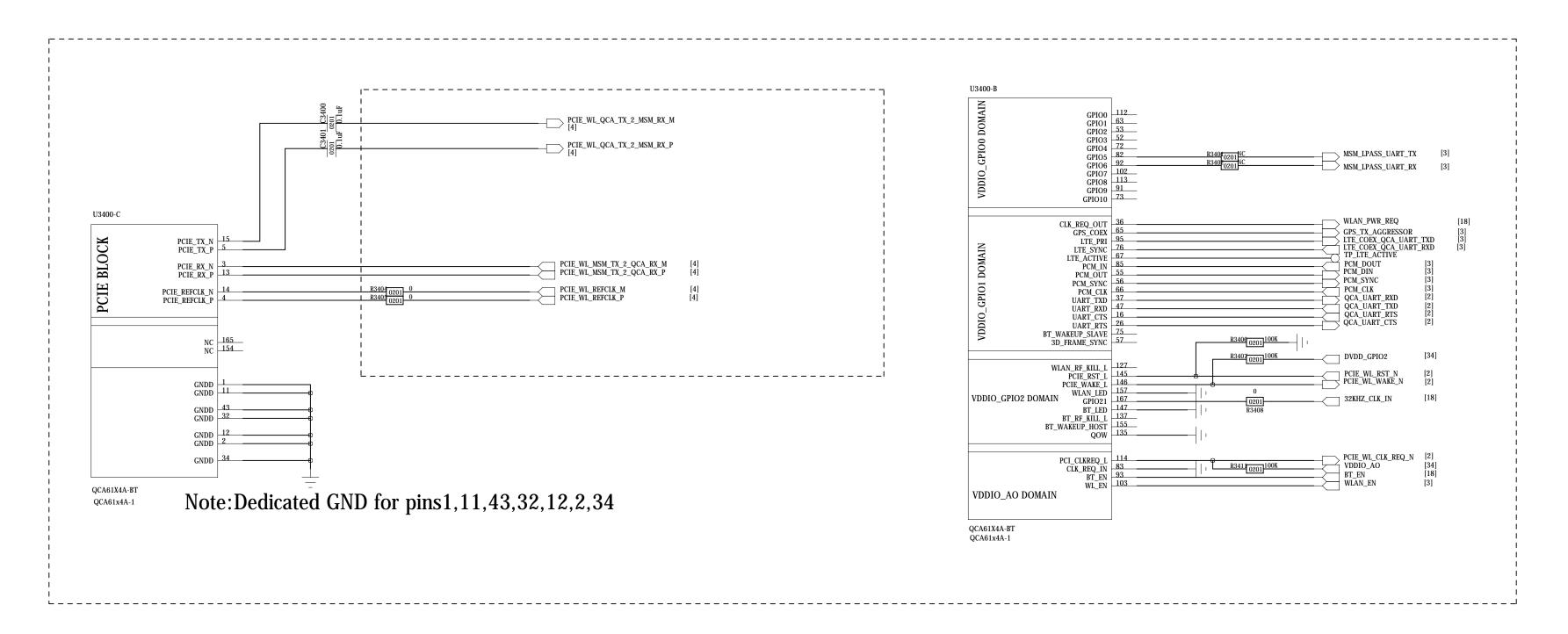


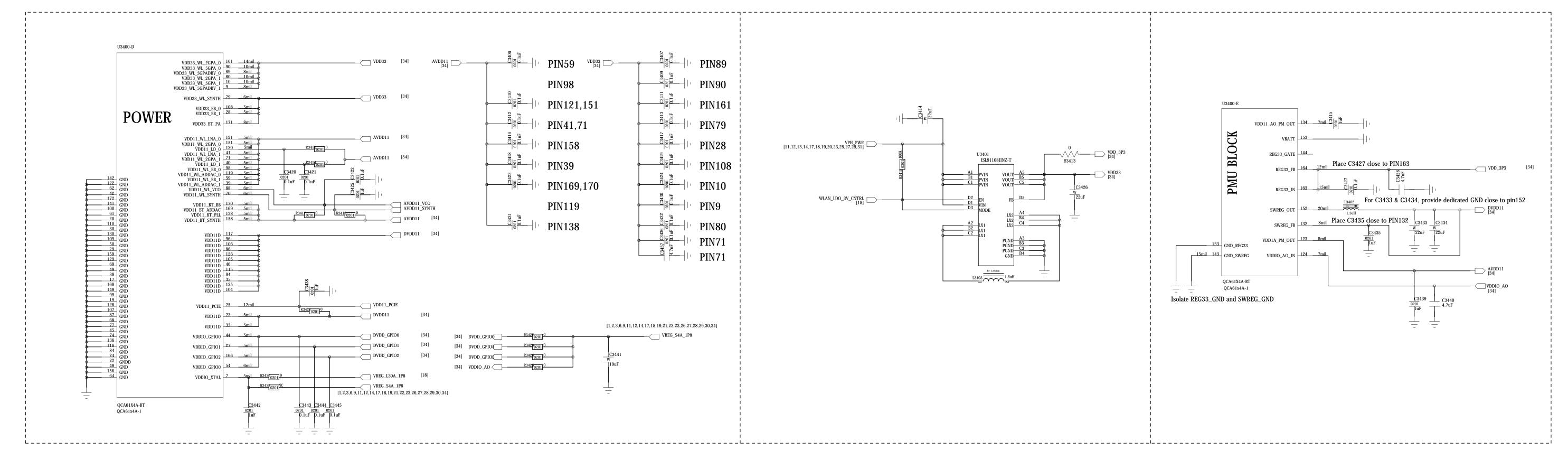




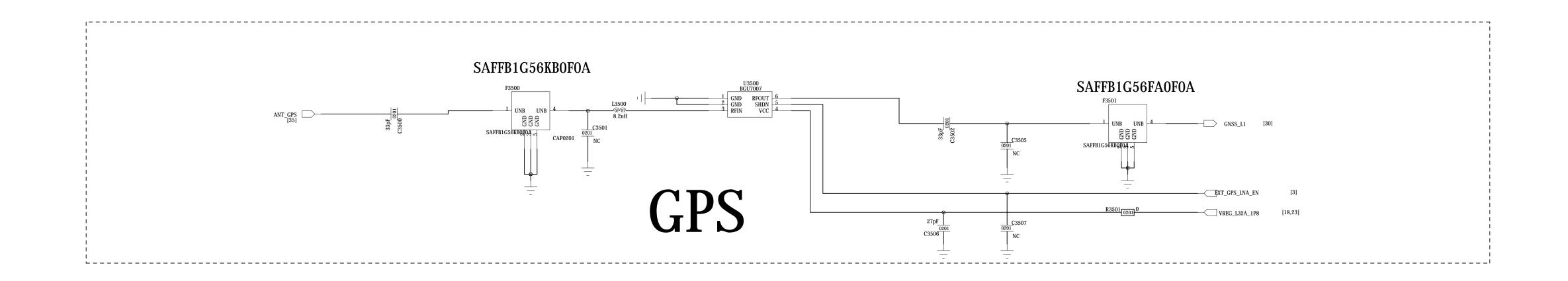
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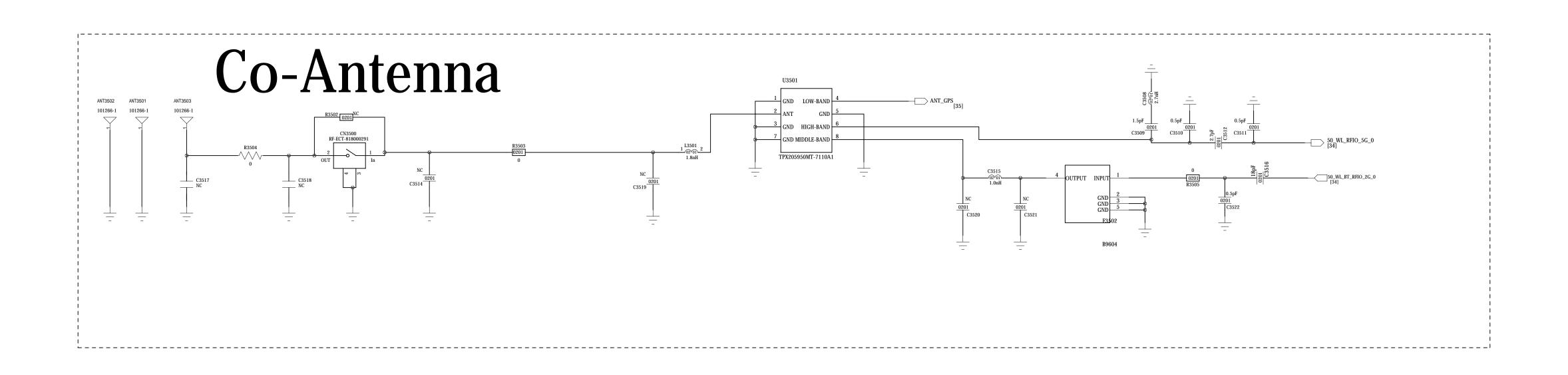






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版本	原理图初始变更纪录	时间	记录人	
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版本				

, 原理图评审注意事项:

- I 1.在评审前一天把原理图(PDF文档)及基带芯片规格书,发给基线经理、基线工程师、软件专家
- 1 2.对于与原项目或者平台商提供的参考设计不一致的10口更换,必须要列出来在此表格,1 供软件工程师参考。
- ${\bf l}$ 3.原理图评审参加人员:基带部全体员工, PCB经理、射频经理及射频工程师 ${\hat 0}_{\|}$ ${\bf l}$ 软件人员:基线经理、基线工程师、软件专家
 - 4.评审流程:1)与软件确认IO口的配置情况(STEP1)
 - 2)电路设计评审(STEP2)
 - 5.评审会后第二天提供评审报告,上传PLM及发给所有参与评审人员, 对于不能确定的评审点,可以在发出报告后继续评估。

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