

## Mid Sem Question Paper

Name of the program:	BTech	Semester
Subject Name	Engineering Chemistry-SCY41106	
Maximum Marks:	20	Time D
Total No. of Questions:	8	Total N

### Mid sem question paper

#### GROUP A(1 X 5)

- 1> A variable on its own or in its complemented form is known as: a) Product Term b) Literal c) Sum Term d) Word
- 2> The output of an EX-NOR gate is 1. Which input combination is correct? a)  $A = 1, B = 0$  b)  $A = 0, B = 1$  c)  $A = 0, B = 0$  d)  $A = 0, B' = 1$ .
- 3> 3 bits full adder contains a) 3 combinational inputs b) 4 combinational inputs c) 6 combinational inputs d) 8 combinational inputs
- 4> The expression for Absorption law is given by: a)  $A + AB = A$  b)  $A + AB = B$  c)  $AB + AA' = A$  d)  $A + B = B + A$
- 5> Which of the following is not a sequential circuit? 1) Flip-Flop 2) Counter 3) Shift register 4) Multiplexer

#### GROUP B(3 X 5)

- 6> Classify the logic families based on its operation
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  - 8> Explain the process of converting SOP to SSOP and POS to SPOS.
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