Mid Sem Question Paper

Name of the program:	BTech CSE	Semester:	I
Subject Name	Engineering Mechanics	Subject Code	EME41104
Maximum Marks:	20	Time Duration:	2 Hrs
Total No. of Questions:	8	Total No of Pages:	2

GROUP A(1 X 5)

1> 3 bits full adder contains a)3 combinational inputs b) 4 combinational inputs c)6 combinational inputs d) 8 combinational inputs

2> The expression for Absorption law is given by: a) A + AB = A b) A + AB = B c) AB + AA' = A d) A + B = B + A

3> Which of the following is not a sequential circuit? 1)Flip-Flop 2)Counter 3)Shift register 4)Multiplexer

4> For example, 3x – 5 = 16 is an equation.

5> The expression for Absorption law is given by: a) A + AB = A b) A + AB = B c) AB + AA' = A d) A + B = B + A

GROUP B(3 X 5)

6> Classify the logic families based on its operation

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8> Explain the method of Boolean expression minimization using K MAP for 3-variable.

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