Mid Sem Question Paper

Name of the program:	B.tech	Semester:	III
Course/Subject Name:	Formal Languages and Automata Theory	Course/Subject Code:	CSE11005
Maximum Marks:	50	Time Duration:	3 Hrs
Total No. of Questions:	18	Total No of Pages:	4

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The output of an EX-NOR gate is 1. Which input combination is correct? a) A = 1, B = 0 b) A = 0, B = 1 c) A = 0, B = 0 d) A = 0, BÂ' =

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3> The expression for Absorption law is given by: a) A + AB = A b) A + AB = B c) AB + AAÂ' = A d) A + B = B + A

4> A variable on its own or in its complemented form is known as: a) Product Term b) Literal c) Sum Term d) Word

5> 3 bits full adder contains a)3 combinational inputs b) 4 combinational inputs c)6 combinational inputs d) 8 combinational inputs

---6> Explain the process of converting SOP to SSOP and POS to

7> Classify the logic families based on its operation

8> Discuss the classifications of FET devices.