Mid Sem Question Paper

Name of the program:	B.tech	Semester:	III
Course/Subject Name:	Formal Languages and Automata Theory	Course/Subject Code:	CSE11005
Maximum Marks:	50	Time Duration:	3 Hrs
Total No. of Questions:	18	Total No of Pages:	4

Mid sem question paper

GROUP A(1 X 5)

- 1> The output of an EX-NOR gate is 1. Which input combination is correct? a) A = 1, B = 0 b) A = 0, B = 1 c) A = 0, B = 0 d) A = 0, B' = 1.
- 2> The output of an EX-NOR gate is 1. Which input combination is correct? a) A = 1, B = 0 b) A = 0, B = 1 c) A = 0, B = 0 d) A = 0, B' = 1.
- 3> 3 bits full adder contains a)3 combinational inputs b) 4 combinational inputs c)6 combinational inputs d) 8 combinational inputs
- 4> Which of the following is not a sequential circuit? 1)Flip-Flop 2)Counter 3)Shift register 4)Multiplexer
- 5> 3 bits full adder contains a)3 combinational inputs b) 4 combinational inputs c)6 combinational inputs d) 8 combinational inputs

GROUP B(3 X 5)

- 6> Discuss the classifications of FET devices.
- 7> Explain the major properties of Boolean algebra
- 8> Explain the method of Boolean expression minimization using K MAP for 3-variable.
