Mid Sem Question Paper

Name of the program:	BTech CSE	Semester:	I
Subject Name	Engineering Chemistry	Subject Code	SCY41106
Maximum Marks:	20	Time Duration:	2 Hrs
Total No. of Questions:	8	Total No of Pages:	2

GROUP A(1 X 5)

- 1> 3 bits full adder contains a)3 combinational inputs b) 4 combinational inputs c)6 combinational inputs d) 8 combinational inputs
- 2> Which of the following is not a sequential circuit? 1)Flip-Flop 2)Counter 3)Shift register 4)Multiplexer
- 3> The expression for Absorption law is given by: a) A + AB = A b) A + AB = B c) AB + AA' = A d) A + B = B + A
- 4> A variable on its own or in its complemented form is known as: a) Product Term b) Literal c) Sum Term d) Word 5> nan

GROUP B(3 X 5)

- 6> Explain the major properties of Boolean algebra
- 7> Explain the method of Boolean expression minimization using K MAP for 3-variable.
- 8> Classify the logic families based on its operation
