

## Mid Sem Question Paper

Name of the program:	BTech	Semester
Course/Subject Name and Code:	Engineering Chemistry-SCY41106	
Maximum Marks:	20	Time D
Total No. of Questions:	8	Total N

### Mid sem question paper

#### GROUP A(1 X 5)

- 1> 3 bits full adder contains a)3 combinational inputs b) 4 combinational inputs c)6 combinational inputs d) 8 combinational inputs
- 2> Which of the following is not a sequential circuit? 1)Flip-Flop 2)Counter 3)Shift register 4)Multiplexer
- 3> 3 bits full adder contains a)3 combinational inputs b) 4 combinational inputs c)6 combinational inputs d) 8 combinational inputs
- 4> Which of the following is not a sequential circuit? 1)Flip-Flop 2)Counter 3)Shift register 4)Multiplexer
- 5> A variable on its own or in its complemented form is known as: a) Product Term b) Literal c) Sum Term d) Word

#### GROUP B(3 X 5)

- 6> Explain the method of Boolean expression minimization using K MAP for 3-variable.
  - 7> Explain the process of converting SOP to SSOP and POS to SPOS.
  - 8> Discuss the classifications of FET devices.
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