

1

DSHB

1

DSFB

0

SSHB

1

SSFB

0

SDFB

Number of cells

1

5

9

2

6

10

3

7

11

4

8

12

Number of Phases

3-phase

6-phase LV

6-phase HV

Information

SM - Submodule.
SC - Semiconductor.

NOTE:
Before you run the application,
kindly close
'Dashboard_raw_data.xlsx' file. To
avoid program CRASH!

Error

Done!

Running

Completed

Run

Parameters

SMs and Capacity

Parallel Cells & SCs

SM Currents

SM Capacitor

Zeta and didt

Battery loss

Capacitor loss

Temperature

SC loss

Total loss

Efficiency

Converter Costs

Total Costs

3-phase system

3-phase system

6-phase LV system

6-phase HV system

Topologies

1

DSHB

1

DSFB

Converter Parameters

Battery Parameters

Thermal Parameters

Cost Constants

MOSFET parameters vector

MOSFET switching parameters

Motor and drive-cycle parameters

Battery modeling

Capacitor modeling

2-level inverter

Optimizing minimum number of parallel MOSFETs

Module Selection

(a) Double-Star Half-Bridge (DS-HB)

(b) Double-Star Full-Bridge (DS-FB)

(c) Single-Star Half-Bridge (SS-HB)

(d) Single-Star Full-Bridge (SS-FB)

(e) Single-Delta Full-Bridge (SD-FB)

3D surface plot showing Total converter costs (including capacitor costs) as a function of Topologies (DSHB, DSFB, SSHB, SSFB, SDFB) and Number of cells per SM (1, 5, 9, 10, 11, 12). The cost ranges from approximately 0.98 to 1.00.

3D surface plot showing Total efficiency at average power as a function of Topologies and Number of cells per SM. The efficiency ranges from approximately 0.98 to 0.99.

3D surface plot showing Total efficiency at average power as a function of Topologies and Number of cells per SM. The efficiency ranges from approximately 0.98 to 0.99.