ARVIN PAOLO Q. DIAZ

B.S. ELECTRONICS ENGINEERING

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BLOCK 6 LOT 39 LINCOLN HEIGHTS, SAN PABLO, DINALUPIHAN, BATAAN

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✉ **arvinpaoloq.diaz@gmail.com**

in **linkedin.com/in/arvin-paolo-diaz**

⚲ PERSONAL PROFILE

An Honest, Committed, Well-Adjusted person. A logical thinker that can quickly adapt to certain situations and can manage his time efficiently. A Professional Civil Service Passer. An Electronics Engineering graduate focused on Instrumentation. Possesses a wide range of technical skills.

🔧 PERSONAL SKILLS

- PROFICIENT WITH MICROSOFT OFFICE PROGRAMS
- BASIC PCB DESIGN AND PRODUCTION
- FAMILIAR WITH FEW PROGRAMMING LANGUAGES:
  - C++, MATLAB, PYTHON, VERILOG, R LANGUAGE, SQL

💼 WORK EXPERIENCE

INTERN
POWER INTEGRATIONS INC. PASIG CITY JUNE 2019 - JULY 2019
COMPLETED 240 HOURS OF INTERNSHIP WORK

⚙ TRAININGS ATTENDED

CCNA ROUTING WORKSHOP
CNAG, DEPARTMENT OF ELECTRONICS ENGINEERING, OCTOBER 23, 2019

✏ EDUCATION

UNIVERSITY OF SANTO TOMAS AUGUST 2015-2020
Electronics Engineering, BSECE

REGIONAL SCIENCE HIGH SCHOOL III JUNE 2011 - MARCH 2015
Secondary Education

🎙 SEMINARS ATTENDED

INTRODUCTION TO IOT

CISCO NETWORKING ACADEMY, CORE COUNCIL OF THE PHILIPPINES, JULY 15, 2020

ECE TECHNICAL SEMINARS : BROADCAST FACILITIES

NECES, DEPARTMENT OF ELECTRONICS ENGINEERING, FEBRUARY 29, 2020

ECE TECHNICAL SEMINARS : PASSIVE OPTICAL NETWORKS

NECES, DEPARTMENT OF ELECTRONICS ENGINEERING, FEBRUARY 29, 2020

ECE TECHNICAL SEMINARS : E2E TELECOM ARCHITECTURE

NECES, DEPARTMENT OF ELECTRONICS ENGINEERING, FEBRUARY 29, 2020

ECE TECHNICAL SEMINARS : FAILURE ANALYSIS

NECES, DEPARTMENT OF ELECTRONICS ENGINEERING, FEBRUARY 29, 2020

5G: THE NEXT CHAPTER OF TELECOM NETWORKS

CNAG, DEPARTMENT OF ELECTRONICS ENGINEERING, NOVEMBER 27, 2019

NETIQUETTE: A SEMINAR ON SOCIAL MEDIA ETIQUETTE

CNAG, DEPARTMENT OF ELECTRONICS ENGINEERING, OCTOBER 25, 2019

FIGHT-ON!: GAINING PHYTON TO YOUR ARSENAL OF PROGRAMMING

UST IEEE - STUDENT BRANCH, DEPARTMENT OF ELECTRONICS ENGINEERING,OCTOBER 22, 2019

REBOOT: THE UST IEEE ARDUINO WORKSHOP 2019

UST IEEE - STUDENT BRANCH, DEPARTMENT OF ELECTRONICS ENGINEERING,OCTOBER 21, 2019

INTERCONNECTED: INTERNET OF THINGS

CNAG, DEPARTMENT OF ELECTRONICS ENGINEERING, SEPTEMBER 16, 2019

BRAIN HACKING: CRACKING THE NEURAL CODE AND REWRITING THE BRAIN NETWORK FOR THE TREATMENT OF NEURO-DEGENERATIVE DISEASES

MRC, DEPARTMENT OF ELECTRONICS ENGINEERING, MARCH 28, 2019

LINKED: A SEMINAR ON NETWORK OPERATIONS

CNAG, DEPARTMENT OF ELECTRONICS ENGINEERING, DECEMBER 7, 2018

EV3LUTION: EVOLVING VALIANTLY VIA LEGO 3

MRC, DEPARTMENT OF ELECTRONICS ENGINEERING, OCTOBER 25, 2018

HACKED: CALCULATOR TECHNIQUES 2018

NECES, DEPARTMENT OF ELECTRONICS ENGINEERING, OCTOBER 17, 2018

5G: NEXT GENERATION OF MOBILE NETWORKS

CNAG, DEPARTMENT OF ELECTRONICS ENGINEERING, OCTOBER 15, 2018

A SEMINAR ON INTERNET OF THINGS

CNAG, DEPARTMENT OF ELECTRONICS ENGINEERING, SEPTEMBER 22, 2018

ANDROID APPLICATION DEVELOPMENT WORKSHOP

MRC, DEPARTMENT OF ELECTRONICS ENGINEERING, MARCH 3, 2018

ARDUINO WORKSHOP

MRC, DEPARTMENT OF ELECTRONICS ENGINEERING, MARCH 2, 2018

IRIS RECOGNITION AS A BIOMETRIC RECOGNITION

NECES, DEPARTMENT OF ELECTRONICS ENGINEERING, OCTOBER 19, 2017