## Nirma University

Institute of Technology

Semester End Examination (IR/RPR), December 2018
B.Tech. in Computer Engineering, Semester – VII
IT794 – COMPILER CONSTRUCTION

Roll / Exam No							Super with D	visor's I ate	nitial		
Time: 3 I	Hours									Max Ma	rks:100
Instructi	2. 3.	Figure Draw n	to righ eat sk	t indica etches v	te full wherev	marks er nece	essary.			ook.	e.
					SECT	TION -	I				
Q-1.	Do as directed.									[18]	
A)										(5)	
B)											(5)
C)											
D)										cify (4)	
Q-2.	Q-2. Answer following Questions.									[16]	
A)	Is follo	owing $E \rightarrow A$ $A \rightarrow a$ $B \rightarrow b$	B   c	nar LL(	1), LR	(1) or 1	both?				(8)
A)	Gram 1) S	mar an mar:	nd par 2) S <del>&gt;</del>	se table	e shov	vn belo	ow:			bb', for	
	Parse	Table			ACTION				СОТО		
		State	a	Ь	0	1	S	S	A	В	
		Ö	S1		S2	\$3		11	4	5	
		1	S1		\$2	S3.			6	7	
		2		r4	r4		r4				
		3	r6	r6	r6	r6	r6				
		4	r1	rI	rl	-0	r1 r2				
		6	r2	r2 S8	r2	r2	12				
		7		S9	-						
		8		r3							
		9		S10							

[18]

(5)

BI Eliminate left recursion from grammar described in following syntax directed definition and modify semantic rules accordingly. Semantic rule Grammar  $E.val = E_1.val + T.val$  $E \rightarrow E_1 + T$  $E \rightarrow E_1 - T$  $E.val = E_1.val - T.val$  $E \rightarrow T$ E.val = T.val  $T \rightarrow T_1 * NUM$ T.val = T<sub>1</sub>.val \* NUM.val T.val = T1.val / NUM.val T→ T<sub>1</sub> / NUM  $T \rightarrow NUM$ T.val = NUM.val Q-3. Do as directed. [16] A) Which of these grammar is ambiguous? Prove using constructing (6) parse tree for an input string 'ABC\*D\*EF'. Grammar 2: Grammar 1:  $E \rightarrow T \mid TE$  $E \rightarrow T \mid ET$  $T \rightarrow ID \mid ID * T$  $T \rightarrow ID \mid T * T$  $ID \rightarrow A \mid B \mid C \mid D \mid E \mid F$  $ID \rightarrow A \mid B \mid C \mid D \mid E \mid F$ B) Trace panic mode error recovery for an input string 'id id \* (id - id') using following parse table. \$ Id  $E \rightarrow TE$  $E \to TE_{_{R}}$ synch E synch  $E_R$  $E \to \varepsilon$  $E_{n} \rightarrow +TE$  $E_R \to \varepsilon$  $T \to F T$  $T \to F T$ T synch synch synch  $T_R \to *FT$ T  $T_R \to \varepsilon$  $T_R \to \varepsilon$  $T_R \to \varepsilon$ F synch  $F \rightarrow id$  $F \rightarrow (E)$ synch synch synch C) Give example of shift-reduce conflict and reduce-reduce conflict. (4)(4)Explain why the following grammar is not LL(k) for any k: CI  $S \rightarrow A \mid B$ A > aaA | aa  $B \rightarrow aaB \mid a$ SECTION - II

Does it make difference to put code optimization phase module

before code generation phase or after code generation phase?

Answer following Questions.

Explain using proper example.

Q-4.

A)

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B)	Explain following static checking performed by semantic analyzer phase using suitable example.										
CI	i) Type checks ii) Flow-of-control-checks	(4)									
C) D)	Explain any two local code optimization techniques.  "Every S-attributed syntax directed definition is L-attributed syntax directed definition". Write your opinion about this statement with proper justification.										
Q-5.	Do as directed.	[16]									
A)	Write semantic rules for generating intermediate code for the										
	following two constructs:	(8)									
	i) repeat-until construct:										
	S -> repeat S1 until E										
	ii) if construct:										
	S -> if E then S1 else S2										
B)	Suppose we have the following C declarations:										
	struct { int a , b ; } CELL;										
	CELL foo[100], *PCELL;										
	PCELL bar(int x, CELL y) $\{\}$										
	Draw graphical presentation of type expressions for the types of foo and bar.										
	OR										
В)	Describe contents of 'Symbol Table' used in compiler. Draw hierarchy of symbol table for below given code fragment.  int n, a;  void main()  {  int a;  float b;  {  char b;}}	(8)									
	5,000										
Q-6.	Do as directed.	[16]									
A)	Generate three address intermediate code for 'a:= $b * (-c) + b * (-c)$ ' and represent it in triples, indirect triples and quadruples formats.	(6)									
B)	Allocate registers to each variables in following code fragment	(6)									
	using getReg algorithm	1									
	t := a - b										
	u := a - c										
	v := t + u										
	d := v + u										
C)	What is difference between syntax directed definition and translation scheme? Explain it for translating infix binary expression to postfix expression  OR	(4)									

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## OR

C) Draw the Control Flow Graph for the code given below: (4)receive m fo <- 0  $f_1 < -1$ if  $m \le 1$  goto L3 i <- 2 L1: if  $i \le m$  goto L2 return  $f_2$  $f_2 < -f_0 + f_1$ L2: fo <- f1  $f_1 < -f_2$ i <- i+1 L3: return m