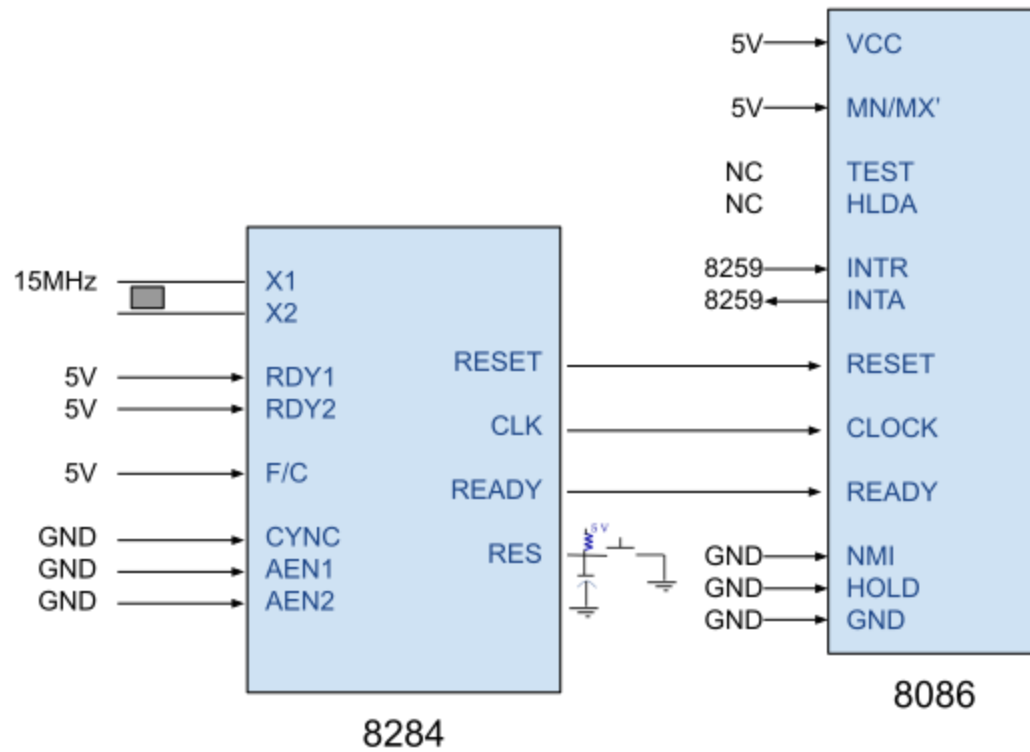
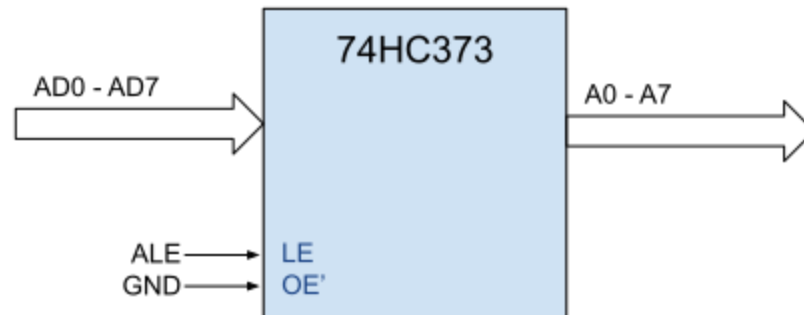
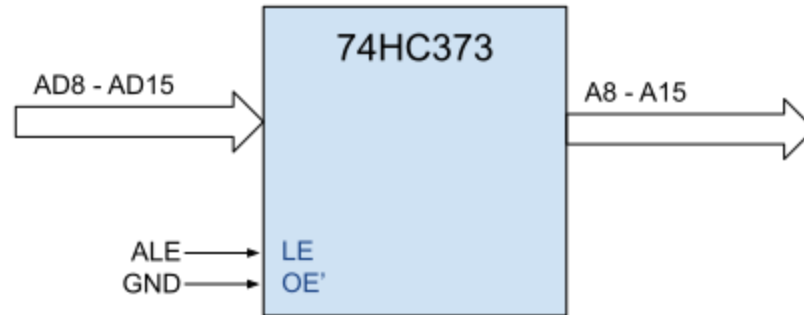
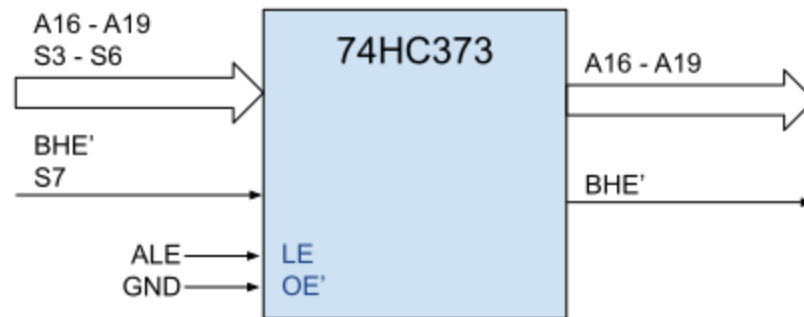


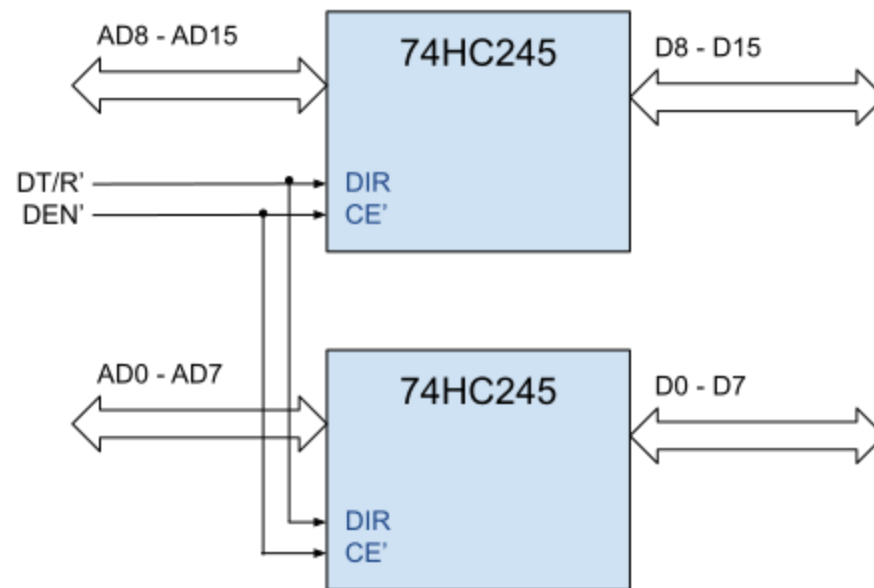
## 8086 Inputs



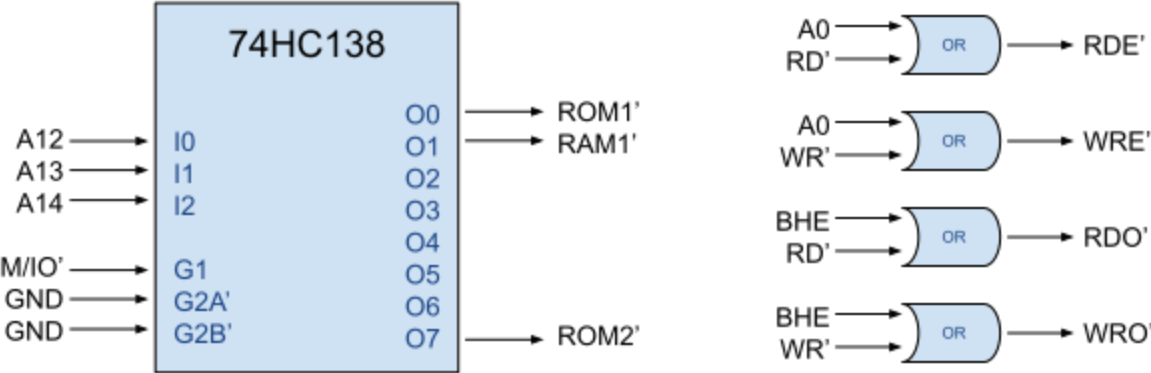
## System Bus of 8086 (Address)



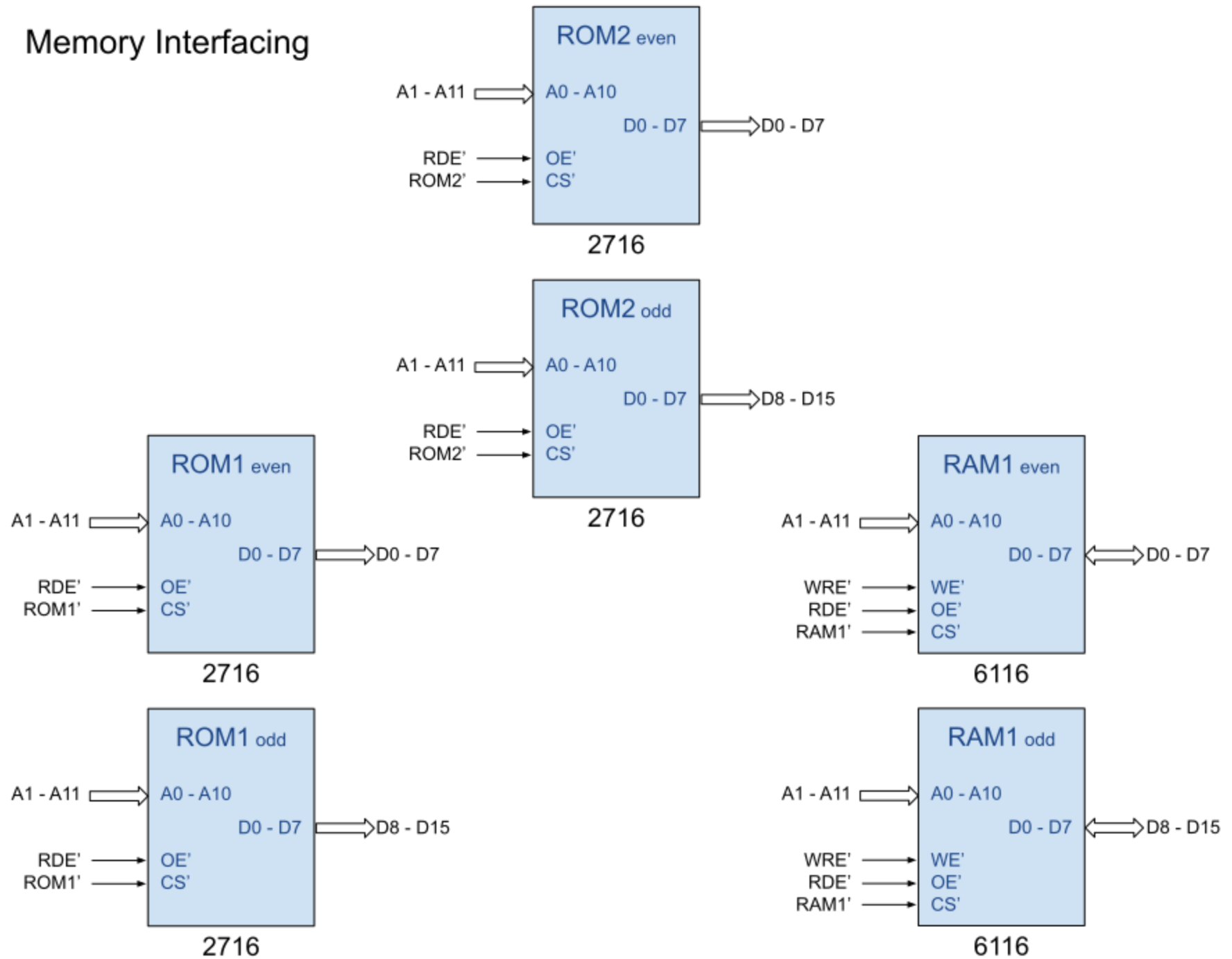
## System Bus of 8086 (Data)



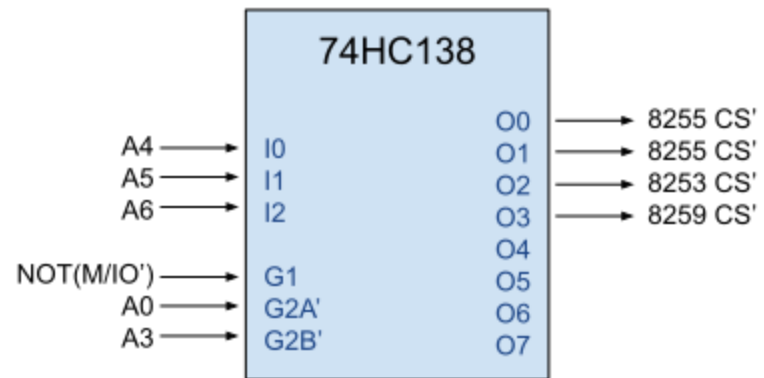
# Memory Decoder



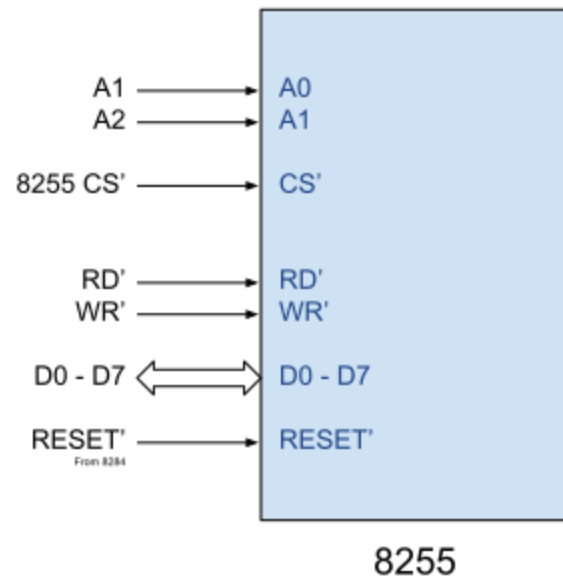
# Memory Interfacing



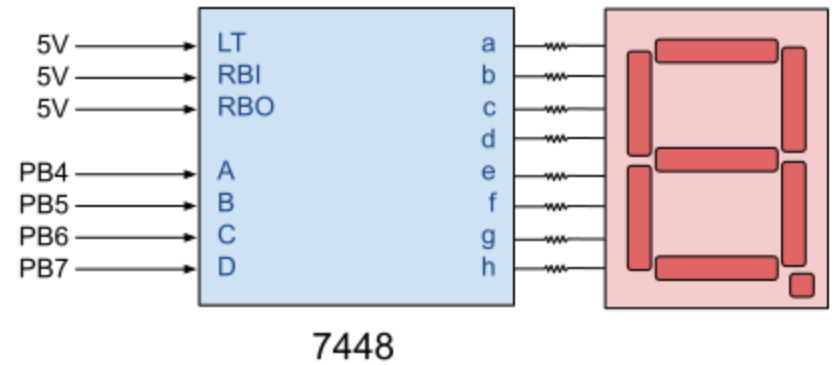
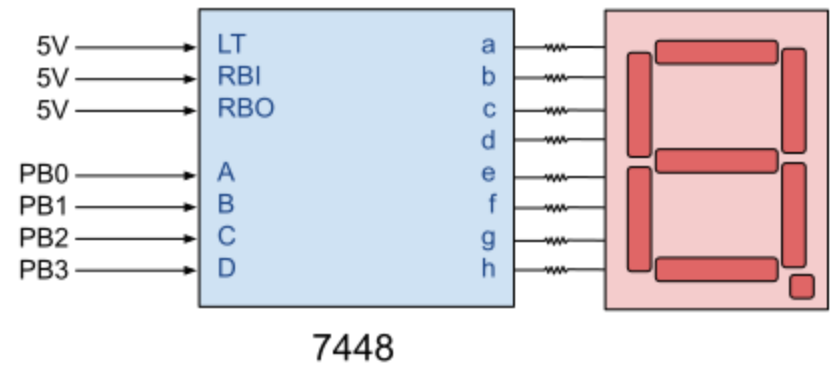
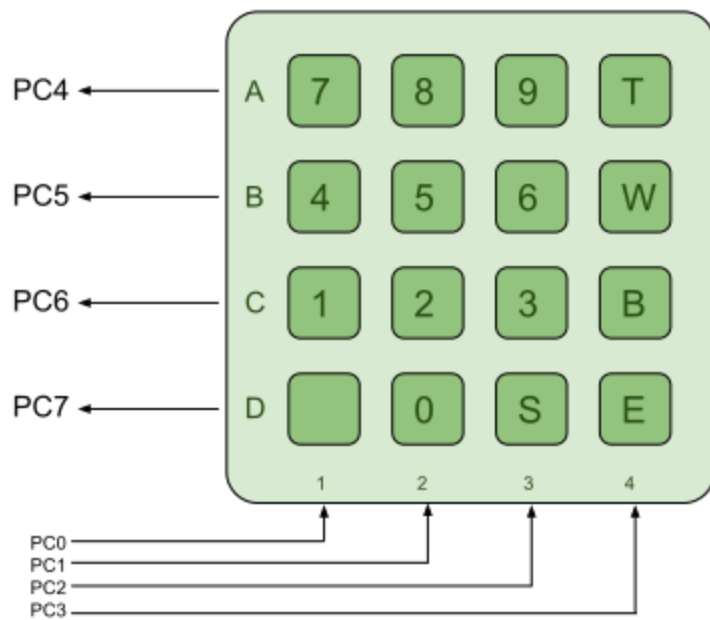
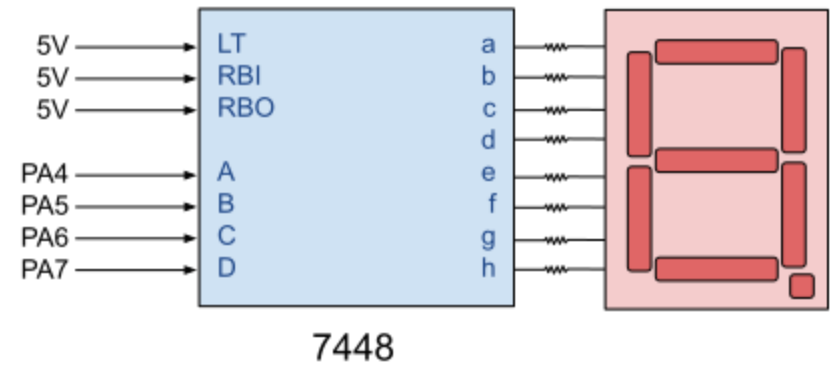
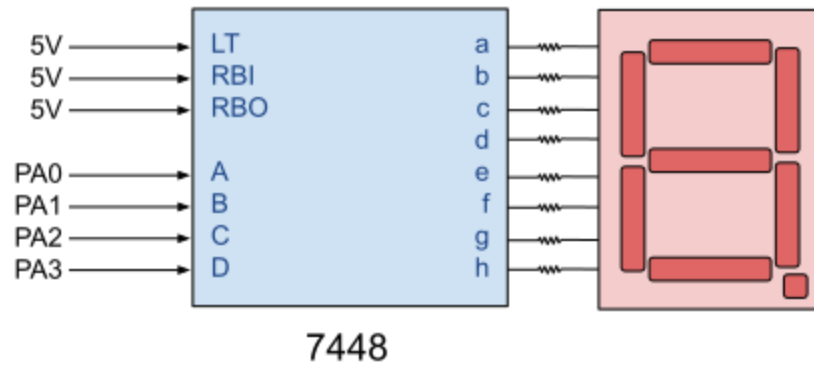
# IO Decoder



## 8255 Interface to the Processor

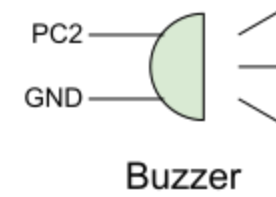
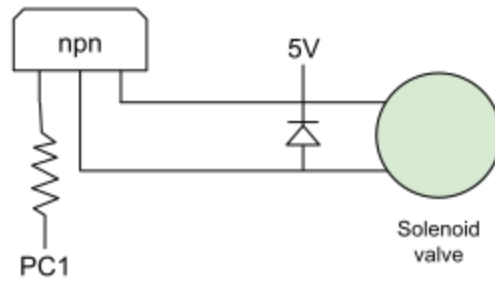
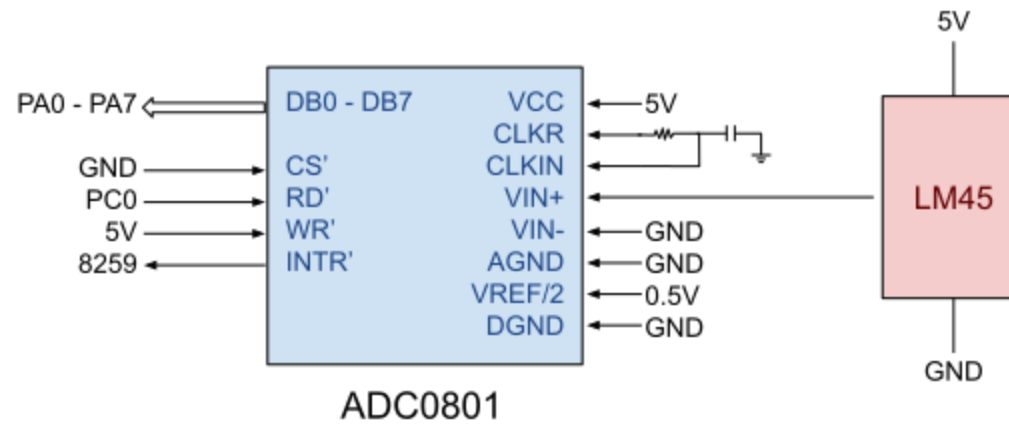


## 8255 Interface to the Processor

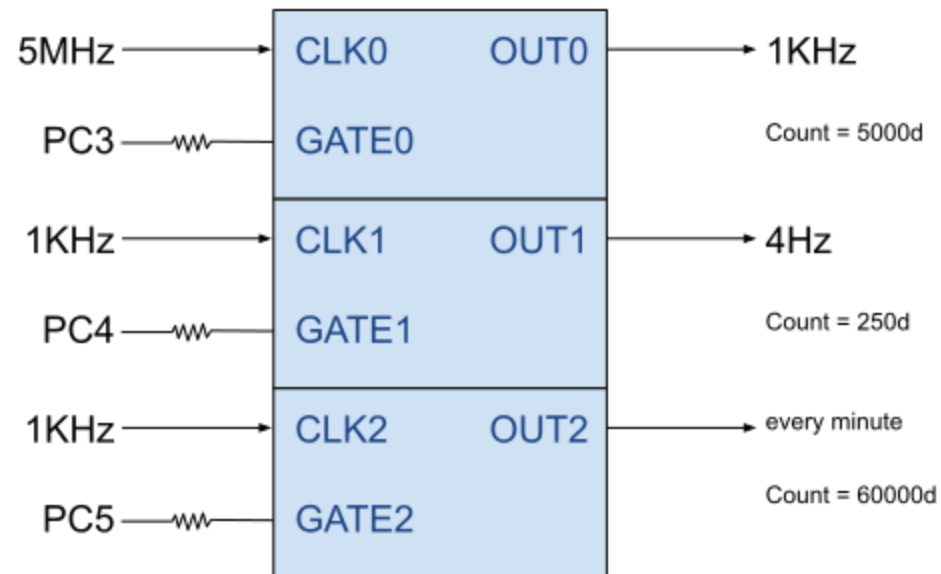
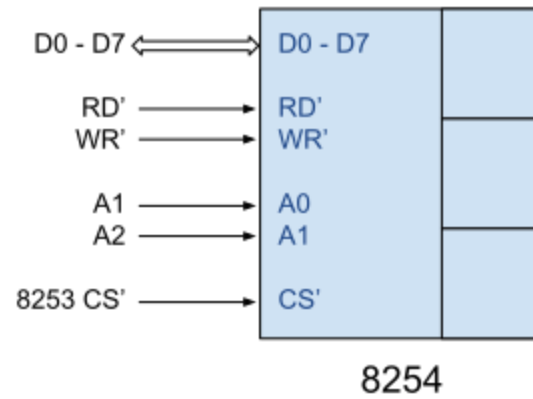




## 8255 Interface to the Processor



8254



8259

