

ECSE 548 - Status Report: 8-bit Booth Multiplier

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I. INTRODUCTION

The current document is the submitted Status Report for the ECSE 548: Introduction to VLSI Systems course. A brief description of the completed action points follows.

II. COMPLETED DELIVERABLES

A. *Design part*

Hmmm...

B. *Implementation Part*

Hmmm(cont'd)...

III. ENCOUNTERED ISSUES

Not really...

IV. CHANGES IN THE IMPLEMENTATION PLAN

- Change 1...
- Oh wait, there is a “Change 2”

REFERENCES

- [1] Weste and Harris, *CMOS VLSI Design*, 4th edition, Addison-Wesley, 2011.
- [2] Electric open-source EDA system, <http://www.staticfreesoft.com/>.