STM32MP15XXAD_LPDDR2x16

Schematics not complete, only for layout example

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U_STM32MP15XXAD_LPDDR2x16-Top
STM32MP15XXAD_LPDDR2x16-Top.SchDoc

Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

Notes to generate the board layout.

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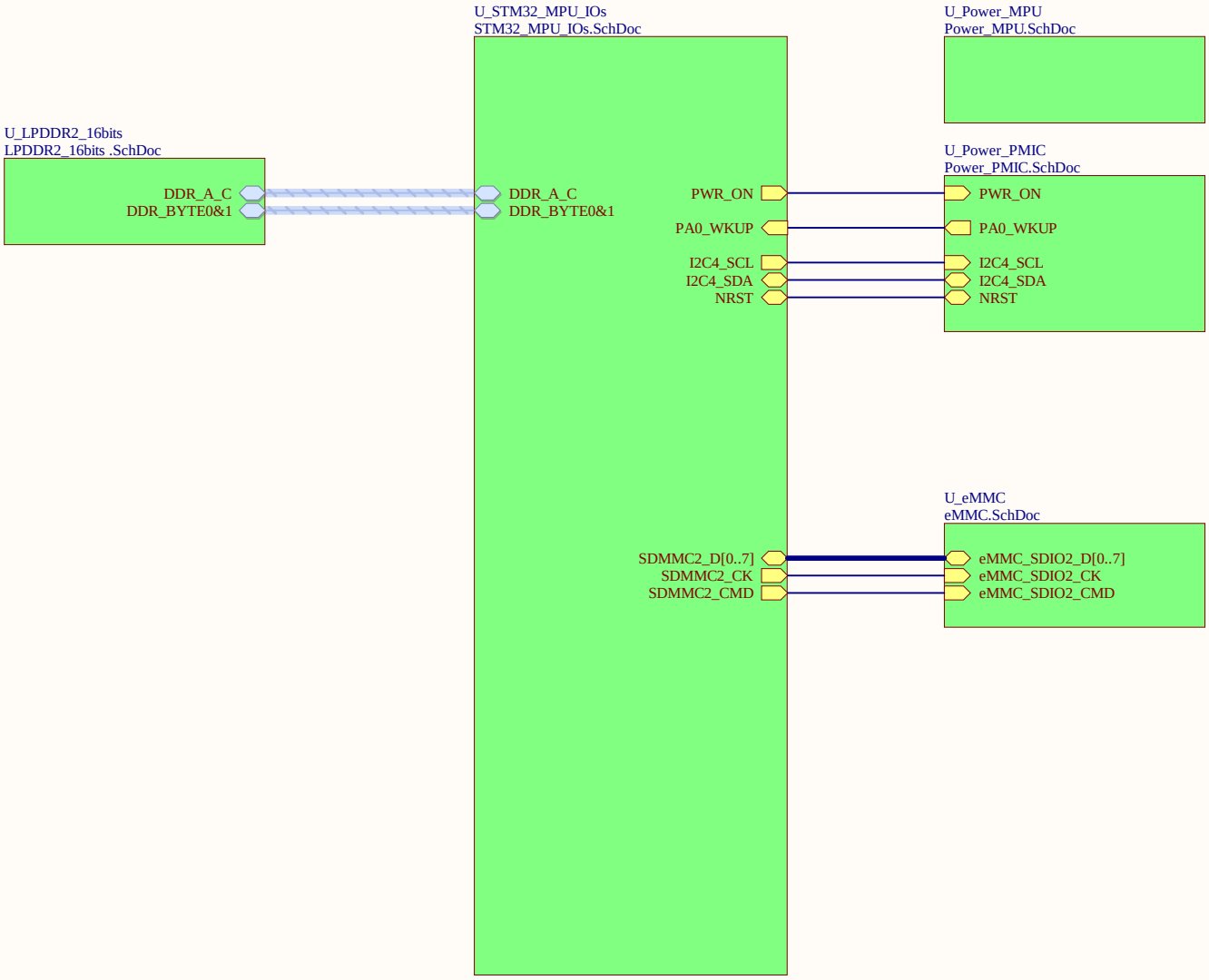
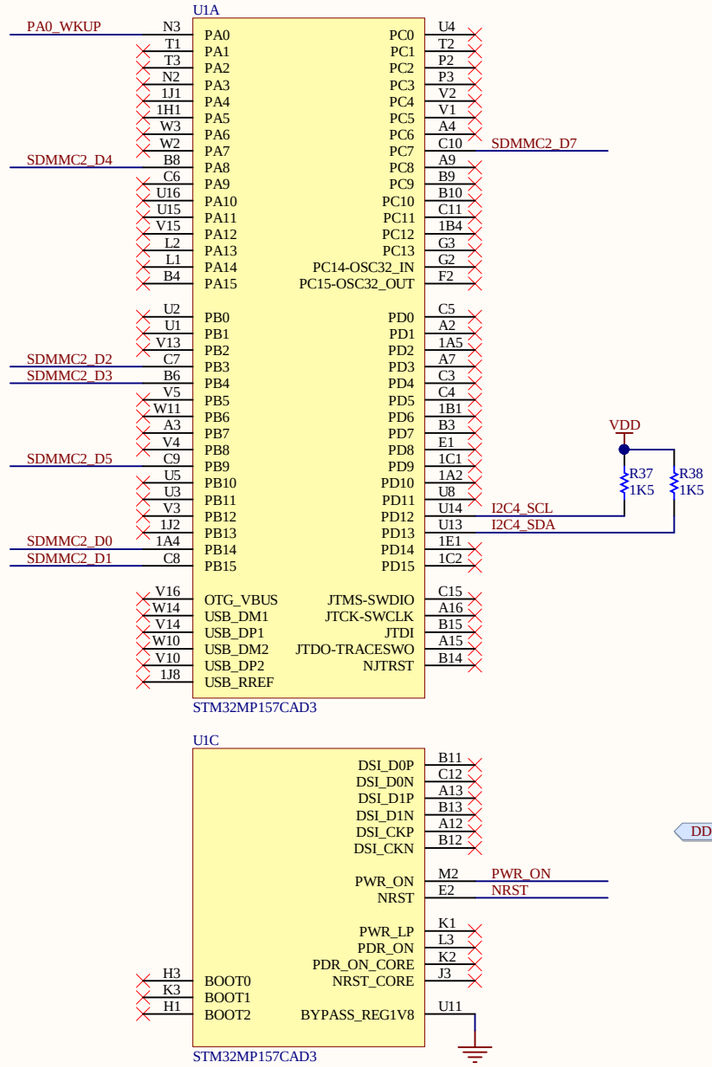
Title: **STM32MP15XXAD_LPDDR2x16-Project**		
Project: **STM32MP15XXAD_LPDDR2x16**		
Variant: Example		
Revision: A-01		Reference:
Size: A4	Date: 14-MAR-19	Sheet: 1 of 7

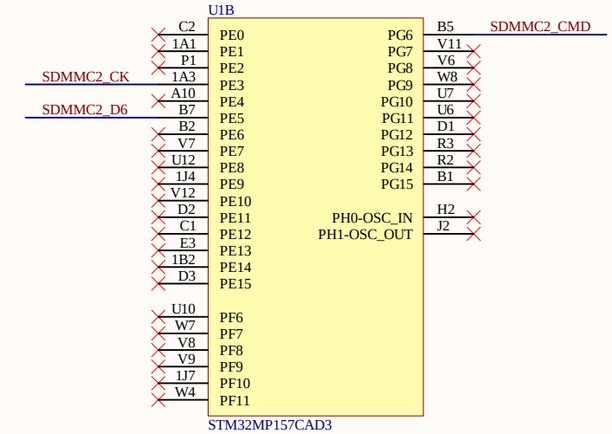
life.augmented

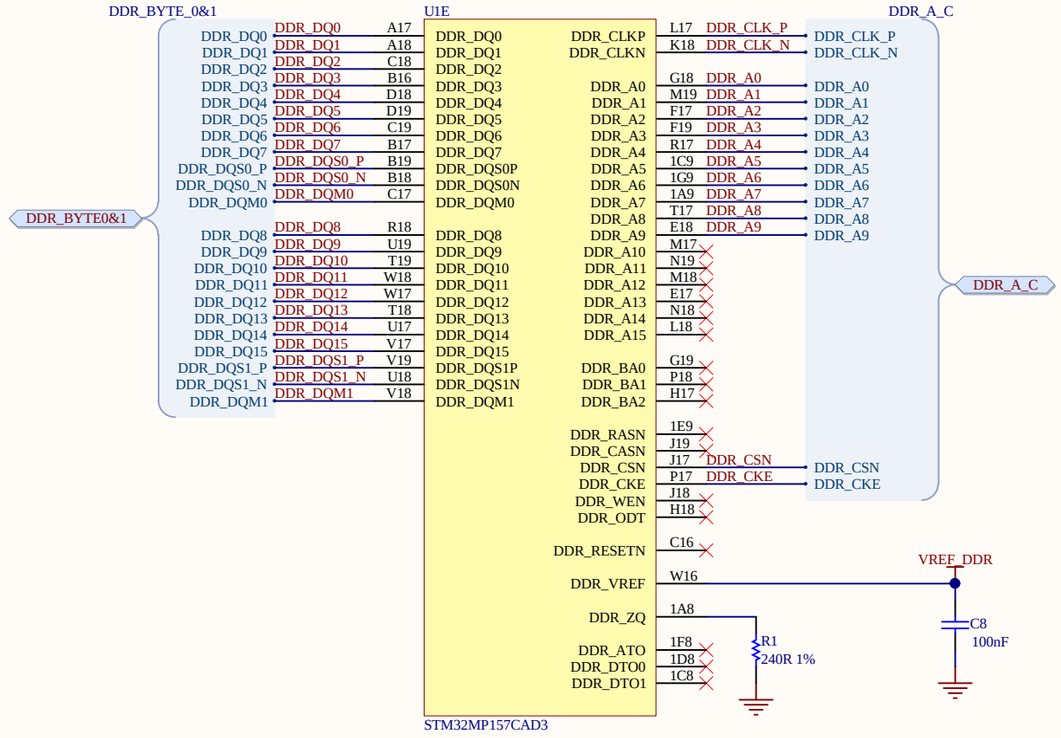


Title: **STM32MP15XXAD_LPDDR2x16-TOP**		
Project: **STM32MP15XXAD_LPDDR2x16**		
Variant: Example		
Revision: A-01		Reference:
Size: A4	Date: 14-MAR-19	Sheet: 2 of 7

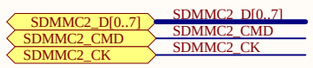
life.augmented



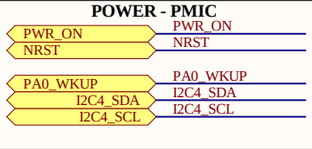




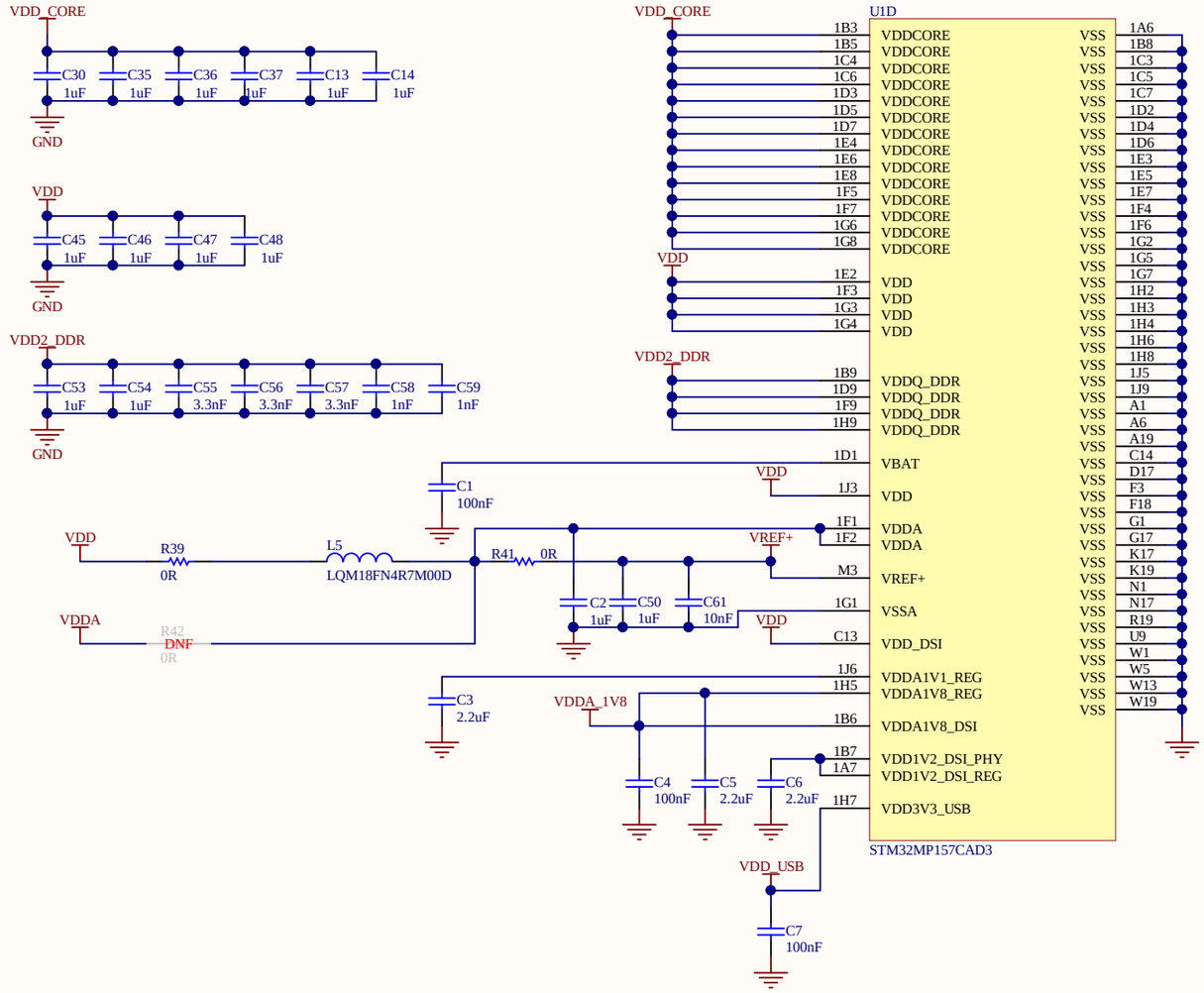
eMMC



POWER - PMIC

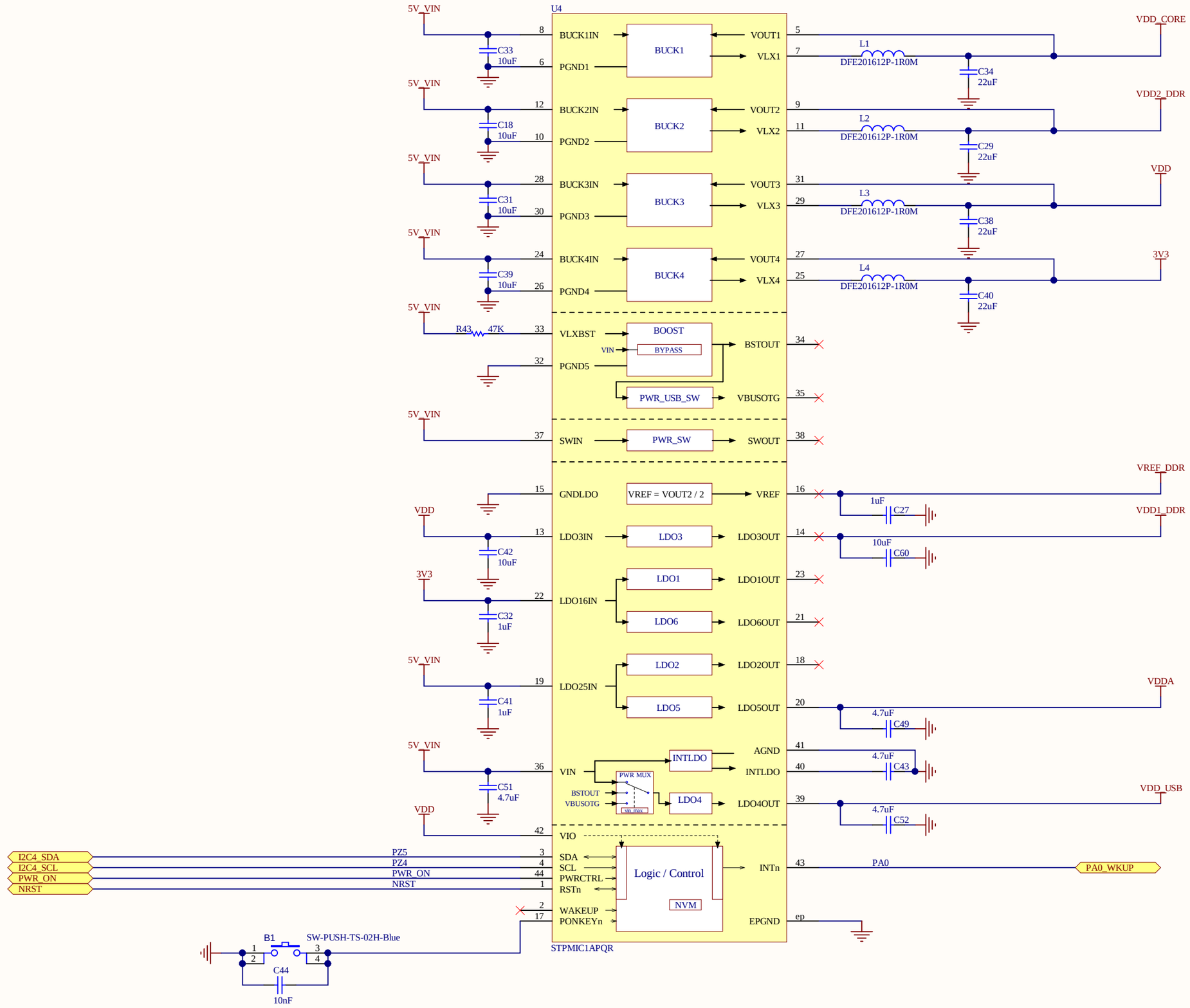


Title: **MPU**		
Project: **STM32MP15XXAD_LPDDR2x16**		
Variant: Example		
Revision: A-01		Reference:
Size: A3	Date: 14-MAR-19	Sheet: 3 of 7



Title: **Power_MPU**			
Project: **STM32MP15XXAD_LPDDR2x16**			
Variant: Example			
Revision: A-01		Reference:	
Size: A3	Date: 14-MAR-19	Sheet: 4 of 7	

life.augmented



Title: **Power_PMIC**		
Project: **STM32MP15XXAD_LPDDR2x16**		
Variant: Example		
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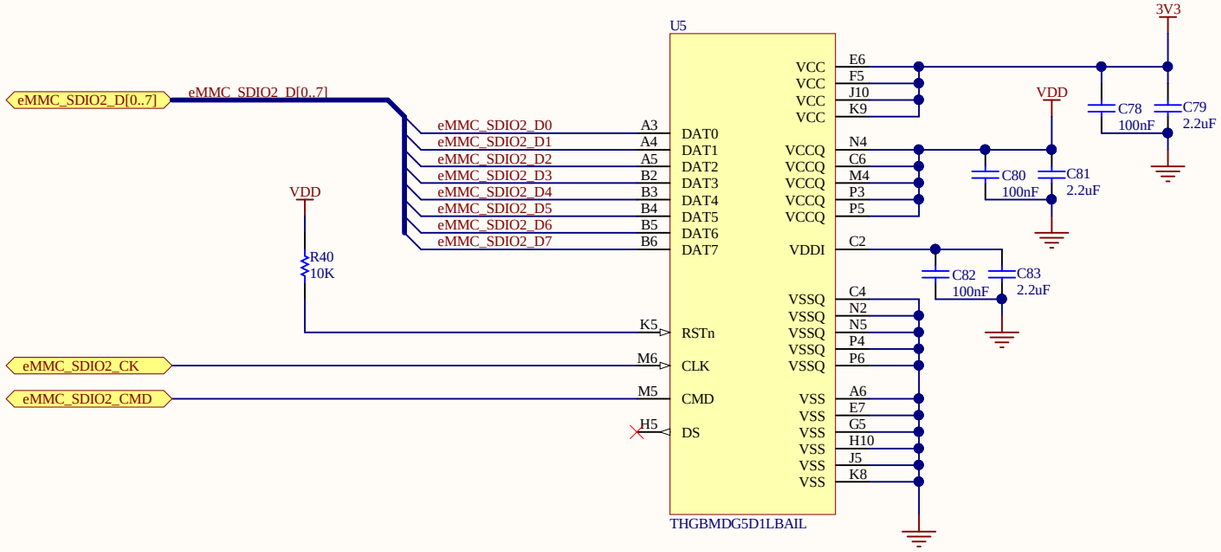

Title: **eMMC**		
Project: **STM32MP15XXAD_LPDDR2x16**		
Variant: Example		
Revision: A-01		Reference:
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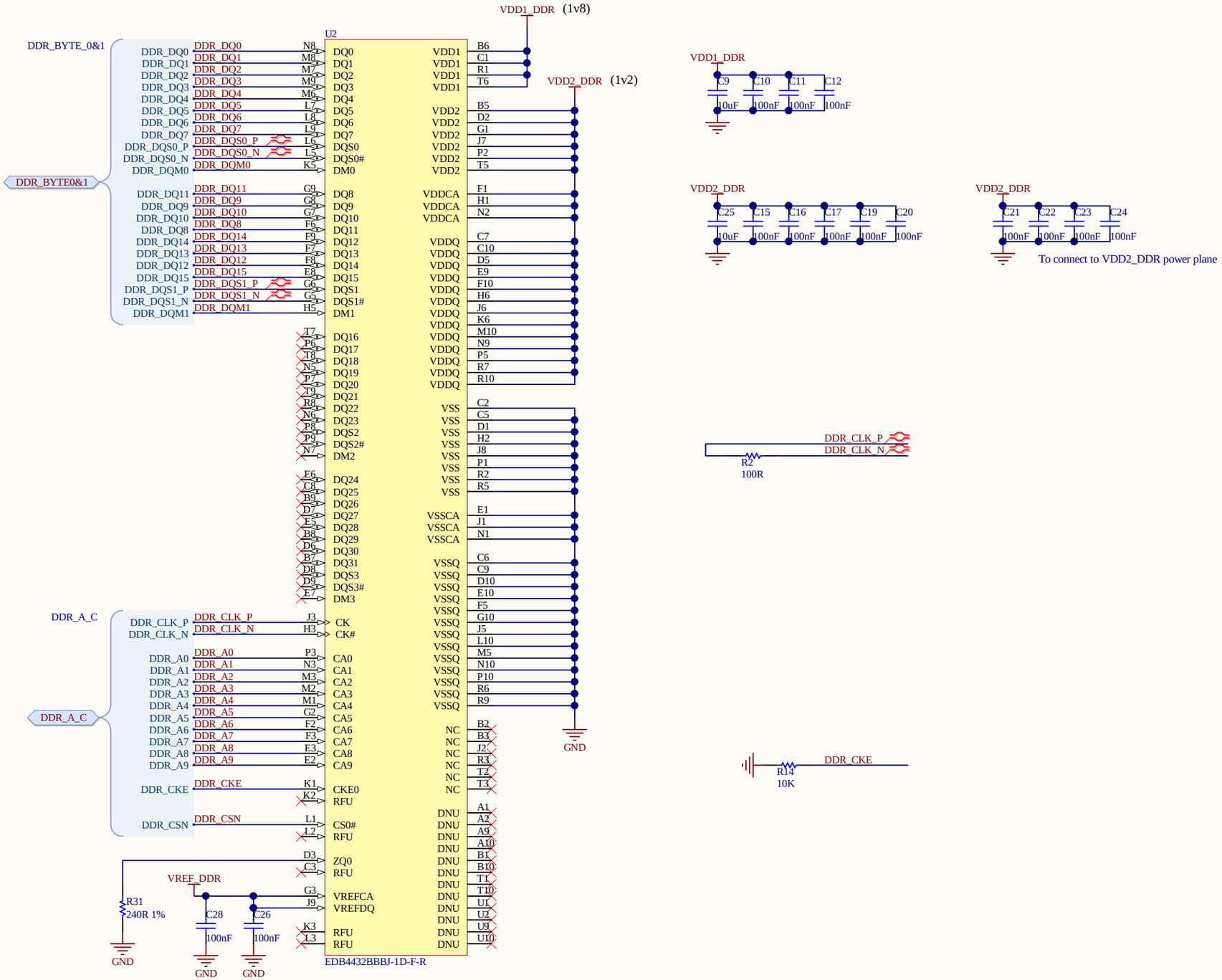

Title: **LPDDR2_16bits**		
Project: **STM32MP15XXAD_LPDDR2x16**		
Variant: Example		
Revision: A-01		Reference:
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life.augmented