# STM32MP15XXAC\_2DDR3Lx16

# Schematics not complete, only for layout example

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U\_STM32MP15XXAC\_2DDR3Lx16-Top STM32MP15XXAC\_2DDR3Lx16-Top.SchDoc

### Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen

Warning text.

Notes to generate the board layout.

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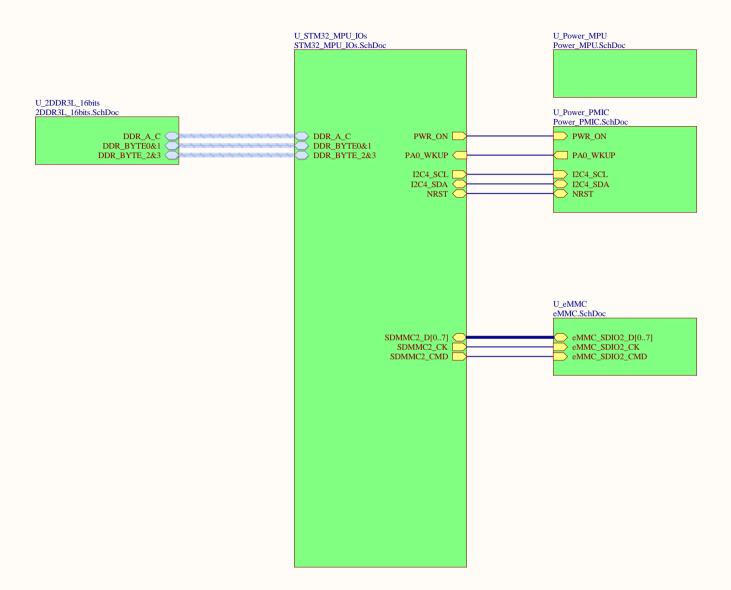
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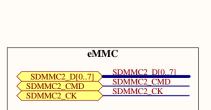
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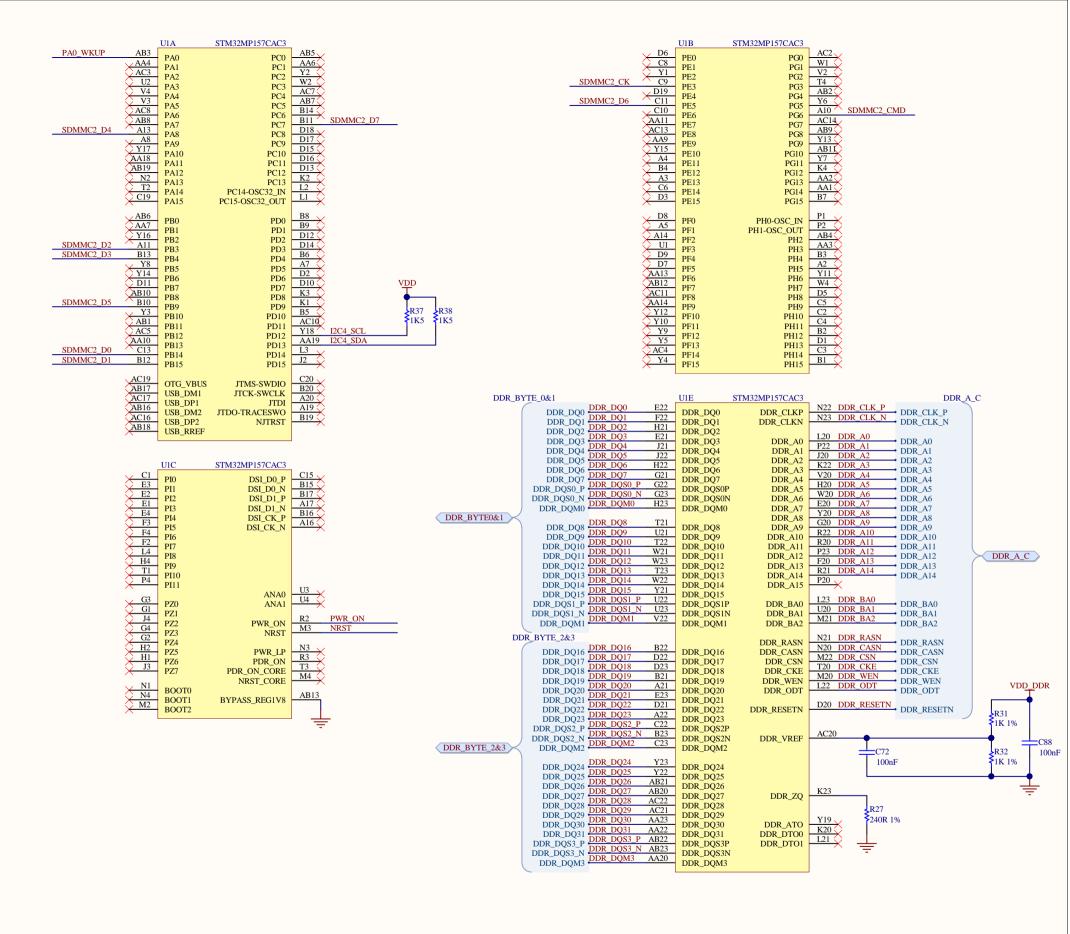
	32MP15XXAC_2DDR3I		
Project: STM32MP15XXAC_2DDR3Lx16			
Variant: Ex	ımple		
Revision: A-01		Reference:	life.augmented
Size: A4	Date: 06-JUN-19	Sheet: 1 of 7	me.augmentea



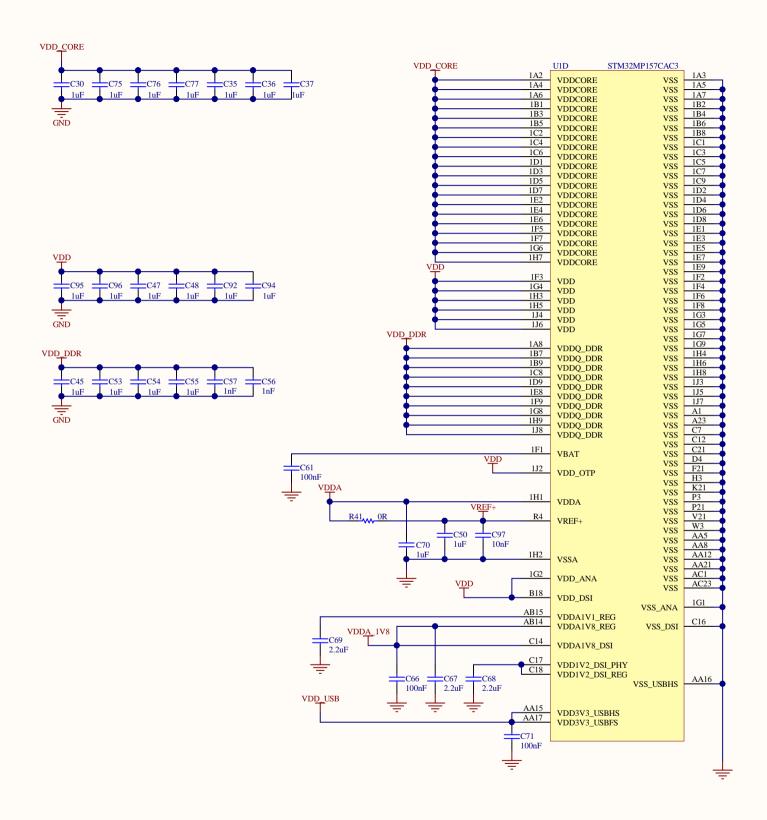
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Project: STN	432MP15XXAC_2DDR	3Lx16	
Variant: Exa	mple		
Revision: A	01	Reference:	116-
Size: A4	Date: 06-IUN-19	Sheet: 2 of 7	life.augmente



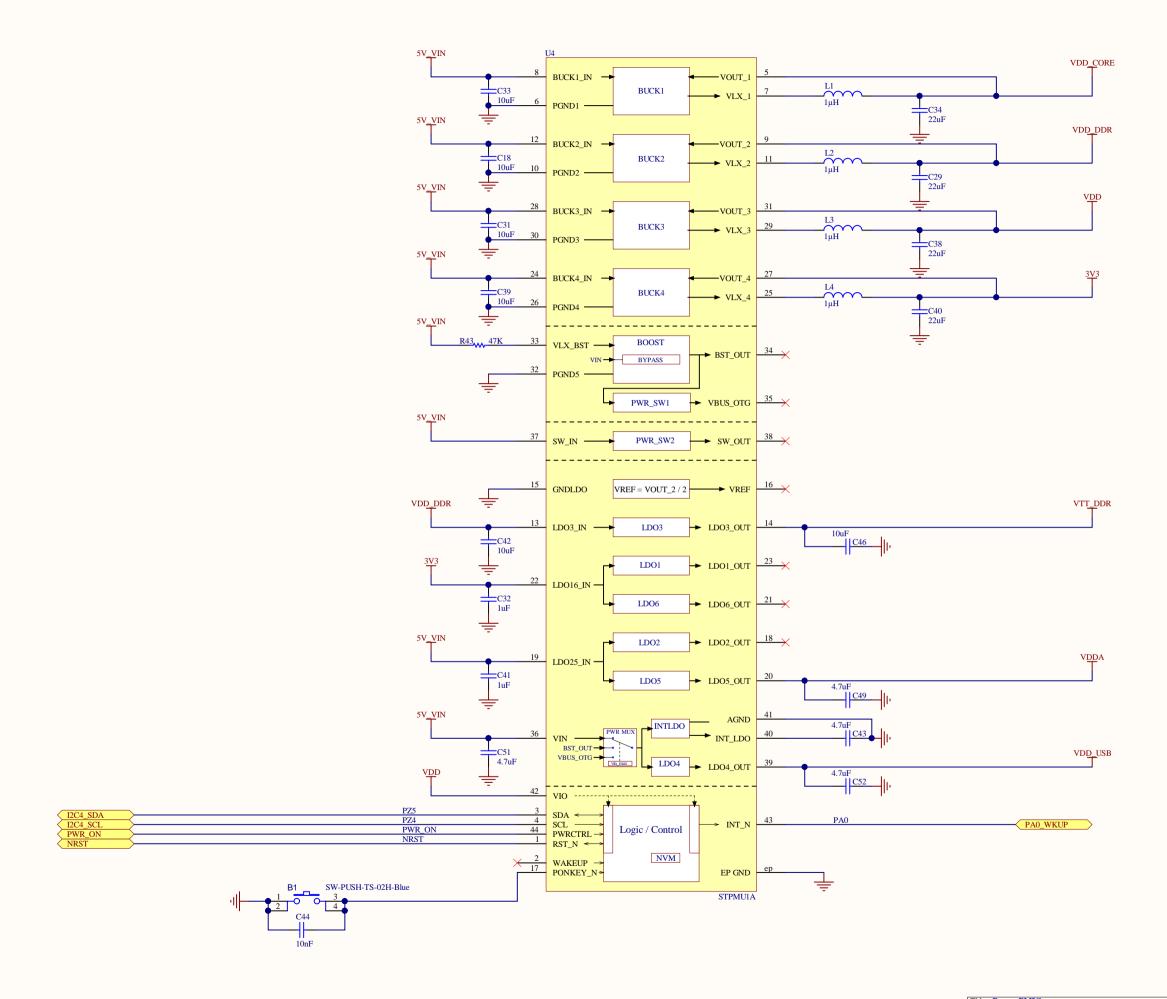
POWER - PMIC			
PWR ON	PWR_ON		
NRST	NRST		
	D. ( ) WWW. D.		
PA0_WKUP	PA0_WKUP		
I2C4_SDA	I2C4_SDA I2C4_SCL		
Li2C4_SCL	12C4_3CL		



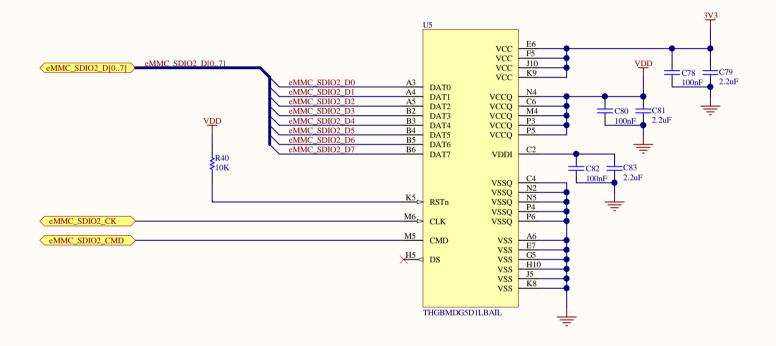
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Variant: Exa	mple		
Revision: A	-01	Reference:	
Size: A3	Date: 06-JUN-19	Sheet: 3 of 7	life.augment



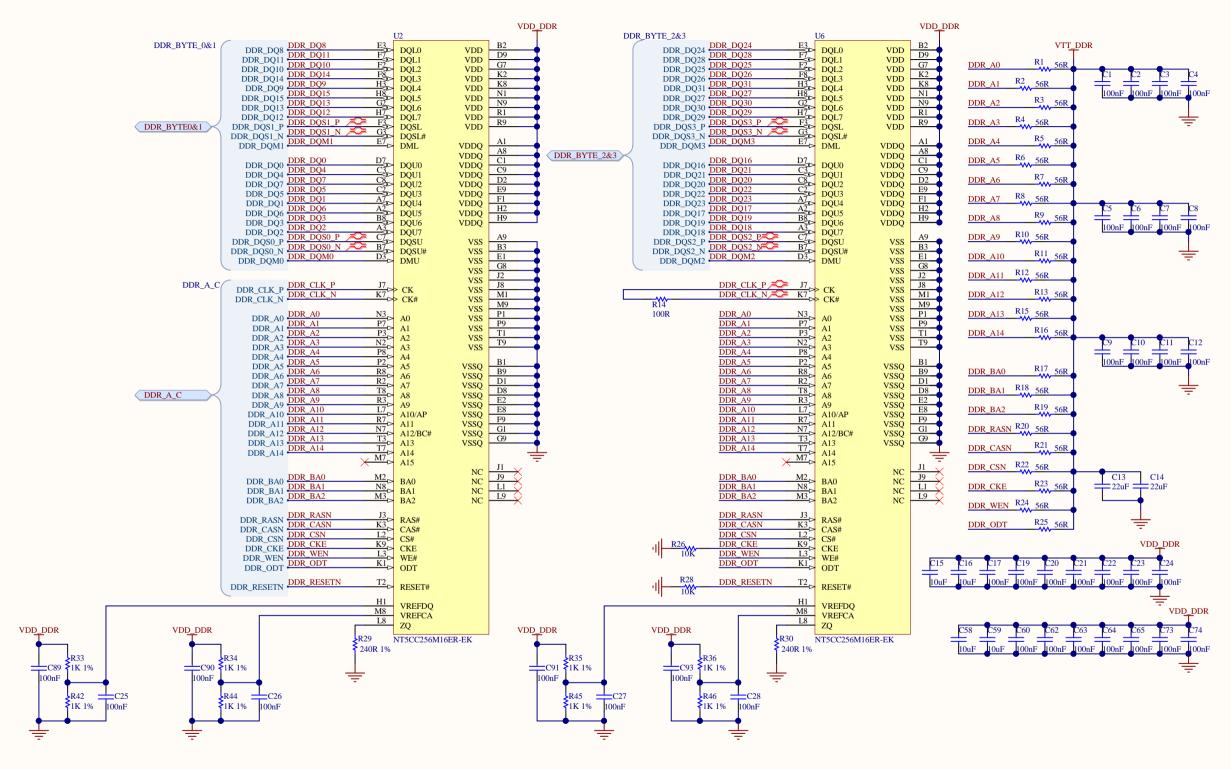
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Project: ST	M32MP15XXAC_2DD	R3Lx16	
Variant: Ex	ample		
Revision: A-01		Reference:	
Size: A3	Date: 06-JUN-19	Sheet: 4 of 7	life.augmente

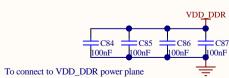


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Project: STN	I32MP15XXAC_2DDR3	BLx16	
Variant: Exa	mple		
Revision: A	-01	Reference:	175
Size: A3	Date: 06-JUN-19	Sheet: 5 of 7	life.augme



Title: eMM			
Project: STN	I32MP15XXAC_2DDR	3Lx16	
Variant: Exa	mple		
Revision: A	-01	Reference:	With a second second
Size: A3	Date: 06-JUN-19	Sheet: 6 of 7	life.augmented





Title: 2DDR	3L_16bits		
Project: STM	I32MP15XXAC_2DDF	R3Lx16	
Variant: Exa	mple		
Revision: A	-01	Reference:	177
Size: A3	Date: 06-JUN-19	Sheet: 7 of 7	life.augmen