STM32MP15XXAB_1DDR3Lx16

Schematics not complete, only for layout example

Table of contents

Sheet 1: Project (this page)

Sheet 2: Top

Sheet 3: STM32 MPU I/Os

Sheet 4: Power MPU

Sheet 5: Power PMIC

Sheet 6: eMMC

Sheet 7: 1DDR3L_16bits

U_STM32MP15XXAB_1DDR3Lx16-Top STM32MP15XXAB_1DDR3Lx16-Top.SchDoc

Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

Notes to generate the board layout.

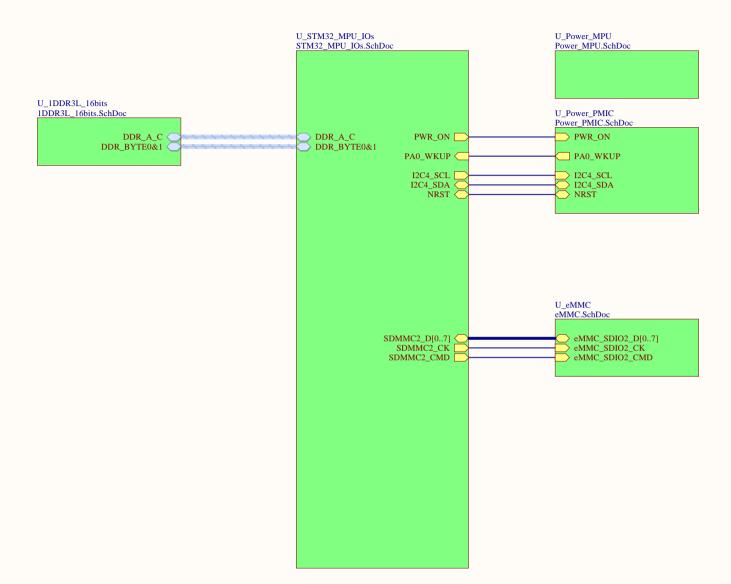
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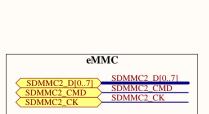
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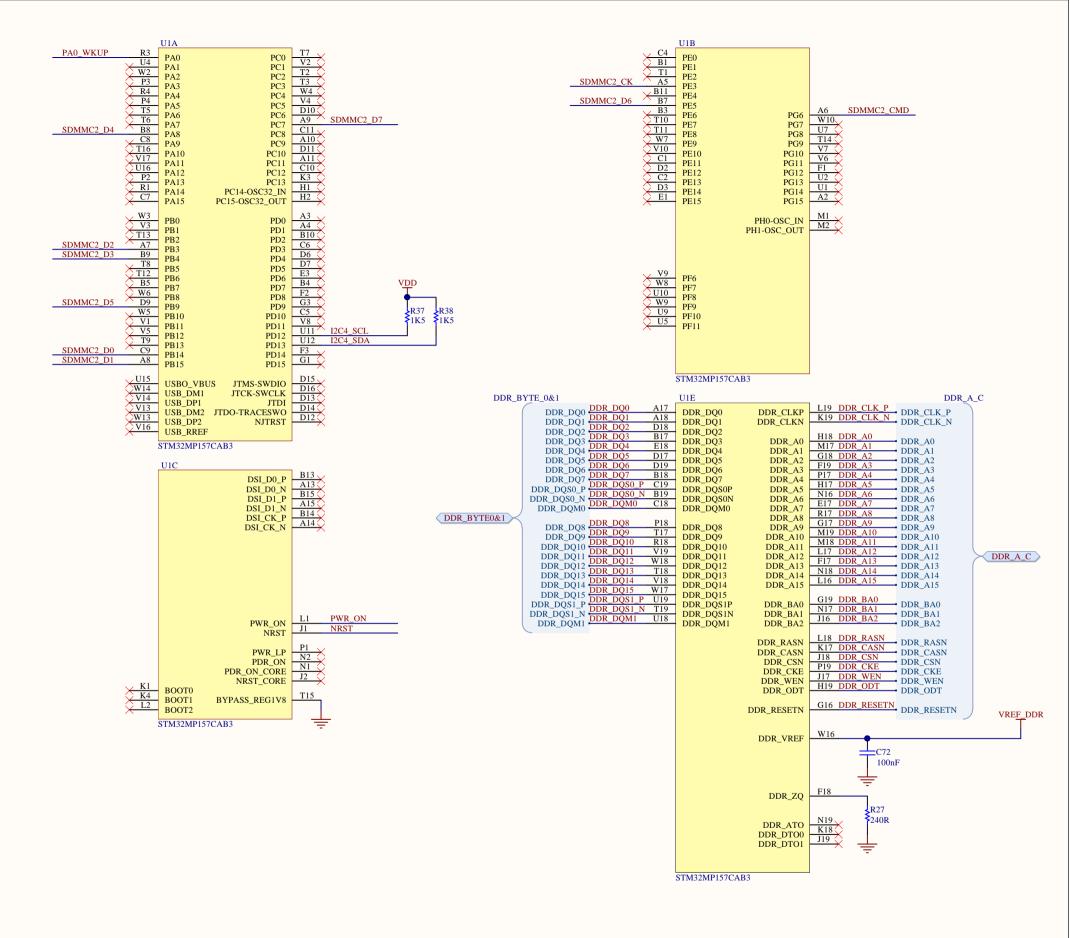
itle: STM3			
roject: STN			
ariant: Exa			
evision: A-01		Reference:	life.augmented
ize: A4	Date: 06-FEB-19	Sheet: 1 of 7	me-dugmentec



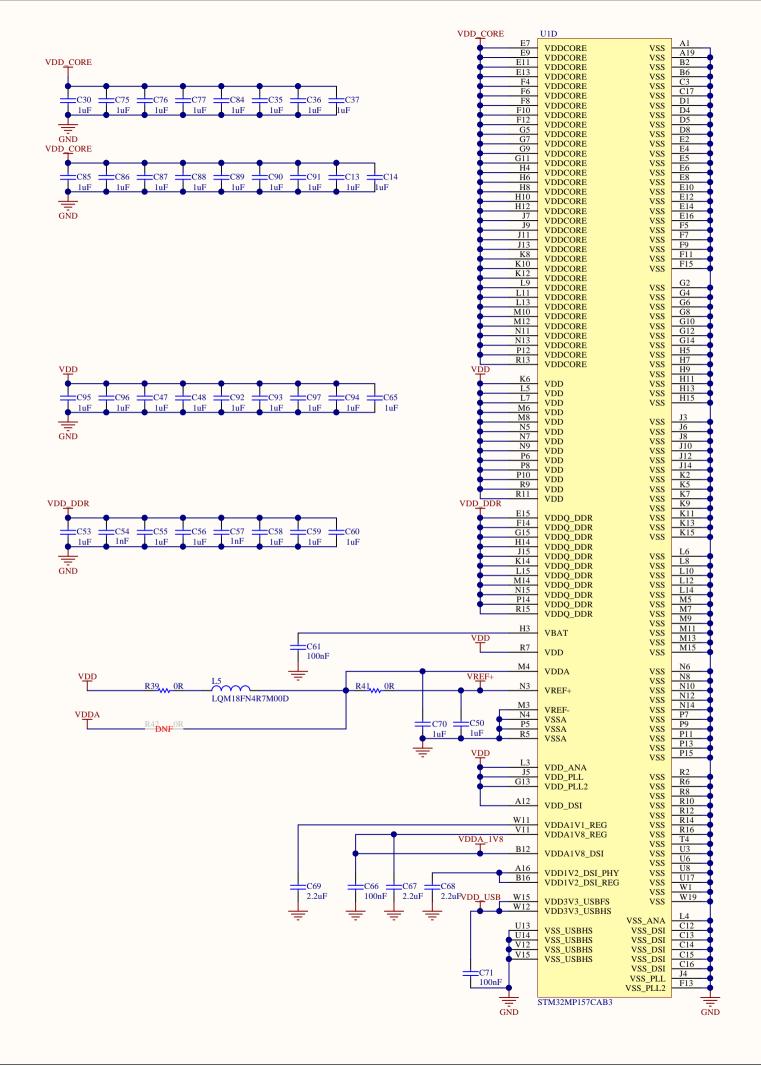
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Variant: Example			
Revision: A-01		Reference:	life_auamente
Size: A4	Date: 06-FEB-19	Sheet: 2 of 7	me-augmente



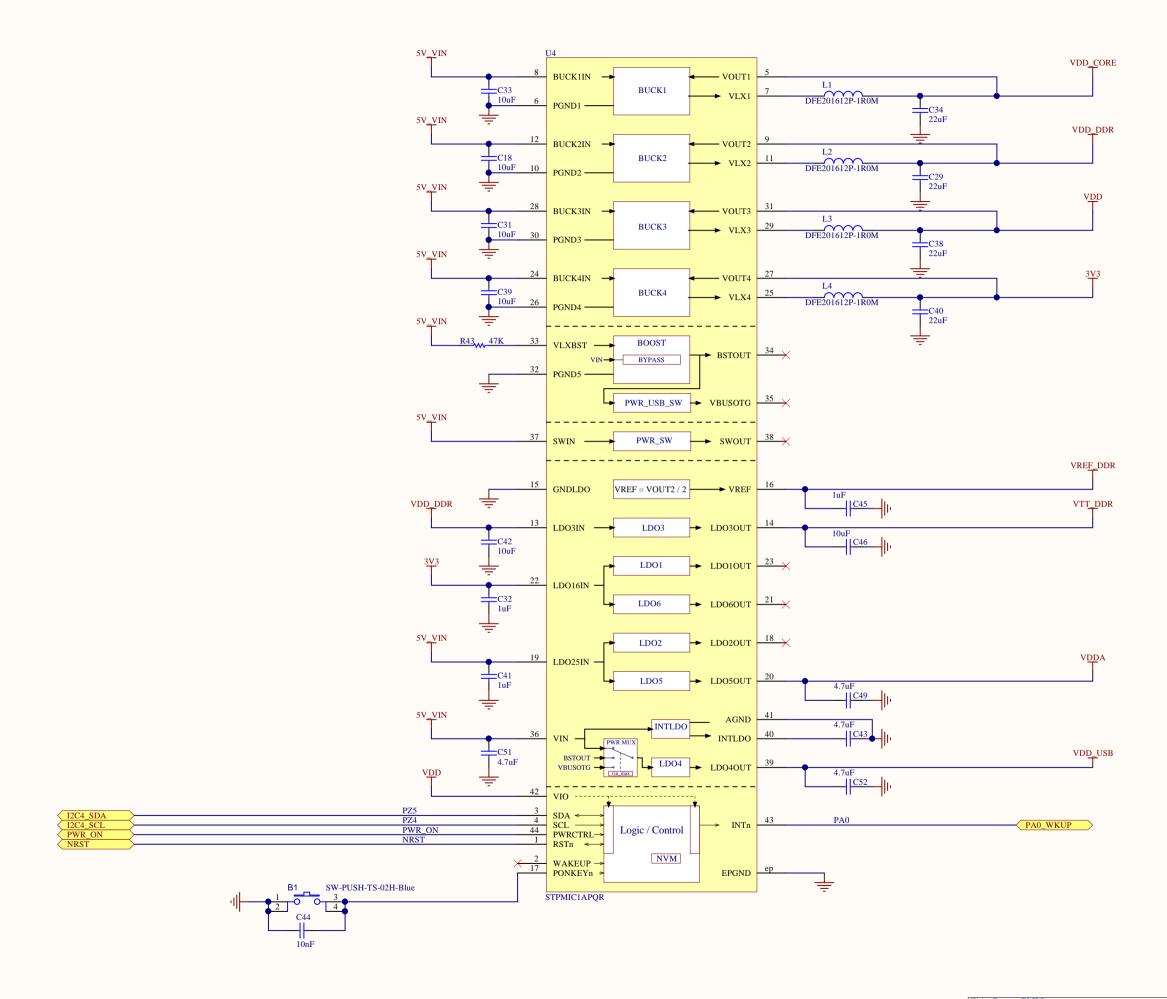
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PWR_ON	PWR_ON NRST			
NRST	NKS1			
DAG WWWD	PA0 WKUP			
PA0_WKUP I2C4 SDA	I2C4_SDA			
12C4_SDA 12C4_SCL	I2C4_SCL			
12C4_SCL				



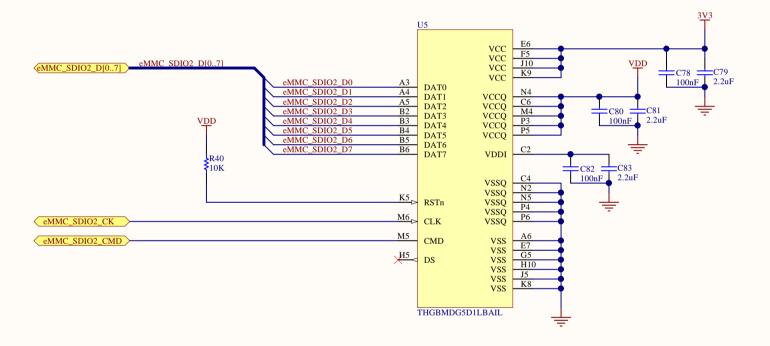
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1	Project: STN			
1	Variant: Example			
	Revision: A-01		Reference:	life guamanta
1	Size: A3	Date: 06-FEB-19	Sheet: 3 of 7	life_auamente



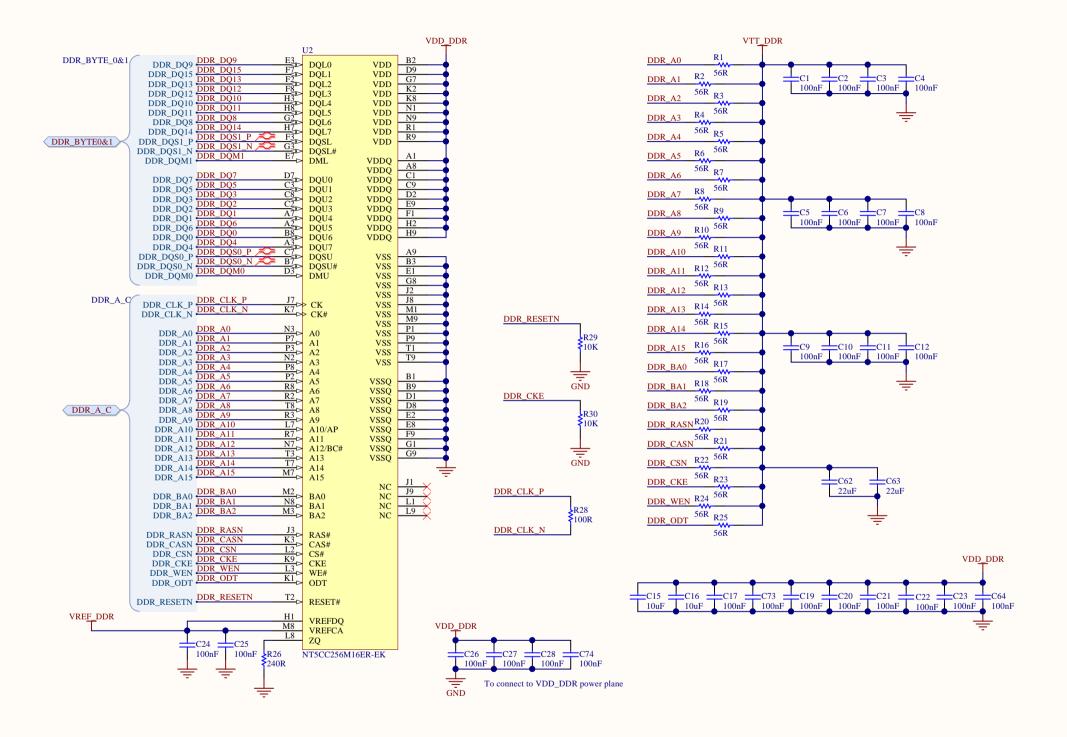
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Project: STM32MP15XXAB_1DDR3Lx16			
Variant: Example			
Revision: A-01		Reference:	life guama
Size: A3	Date: 06-FEB-19	Sheet: 4 of 7	life_auamer



Title: Power_PMIC			
Project: STM32MP15XXAB_1DDR3Lx16			
Variant: Exa	mple		
Revision: A	-01	Reference:	life aver
Size: A3	Date: 06-FEB-19	Sheet: 5 of 7	life_auam



Title: eMM			
Project: STN			
Variant: Exa			
Revision: A-01		Reference:	life augmented
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Title: 1DDR3L_16bits				
Project: STM32MP15XXAB_1DDR3Lx16				
Variant: Example				
Revision: A-01		Reference:		life guarante
Size: A3 Date: 06-	FFR-19	Sheet: 7 of 7		life.auamente