STM32MP15XXAD_1DDR3Lx16

Schematics not complete, only for layout example

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U_STM32MP15XXAD_1DDR3Lx16-Top STM32MP15XXAD_1DDR3Lx16-Top.SchDoc

Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

Notes to generate the board layout.

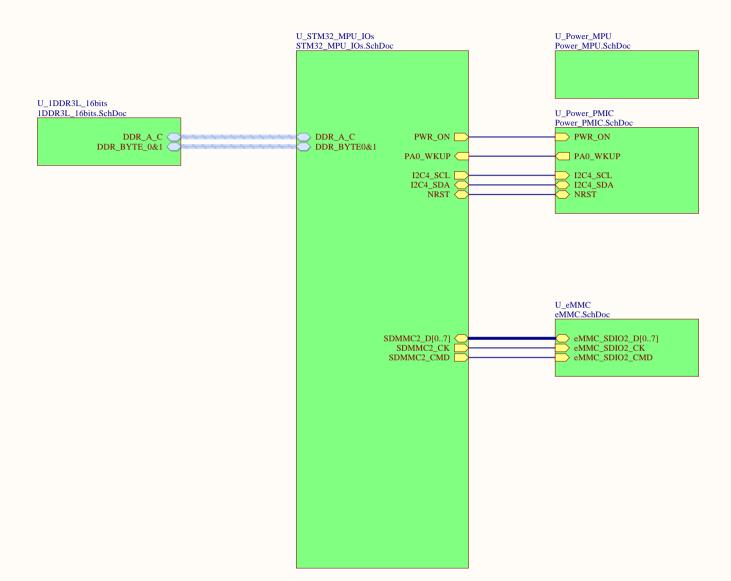
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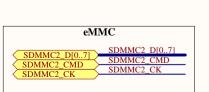
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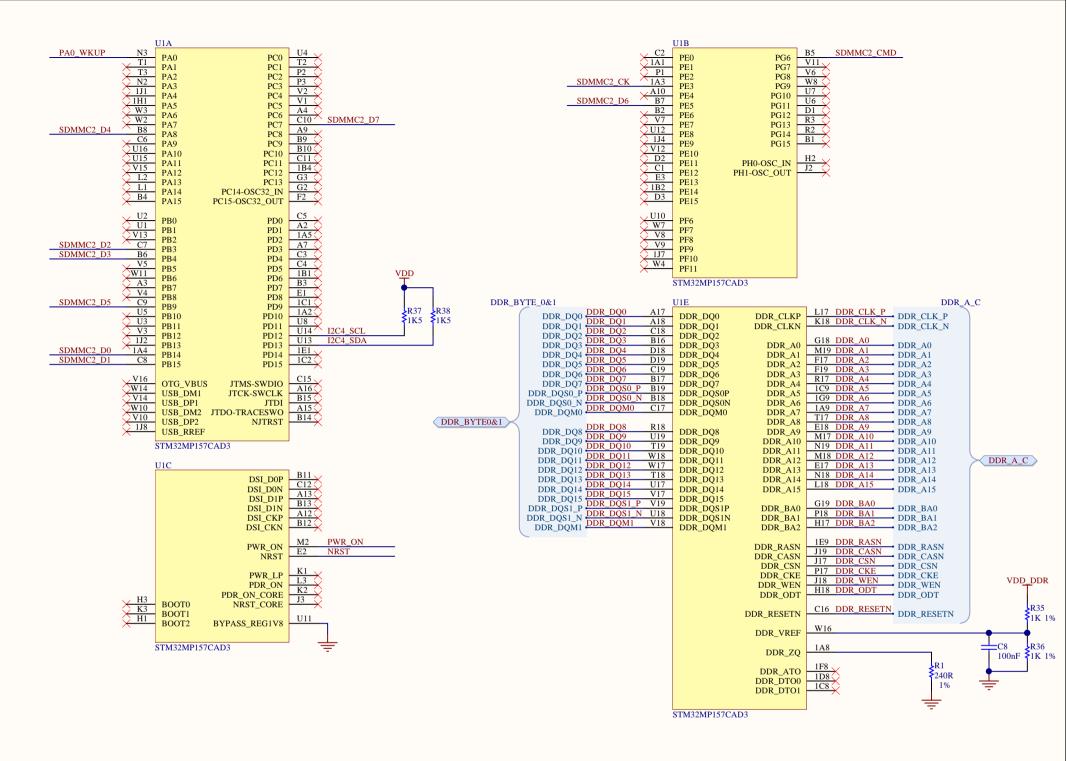
	32MP15XXAD_1DDR3		
roject: STI			
'ariant: Exa	imple		
levision: B	-01	Reference:	life_auamented
ize: A4	Date: 31-JAN-19	Sheet: 1 of 7	me.dugmented



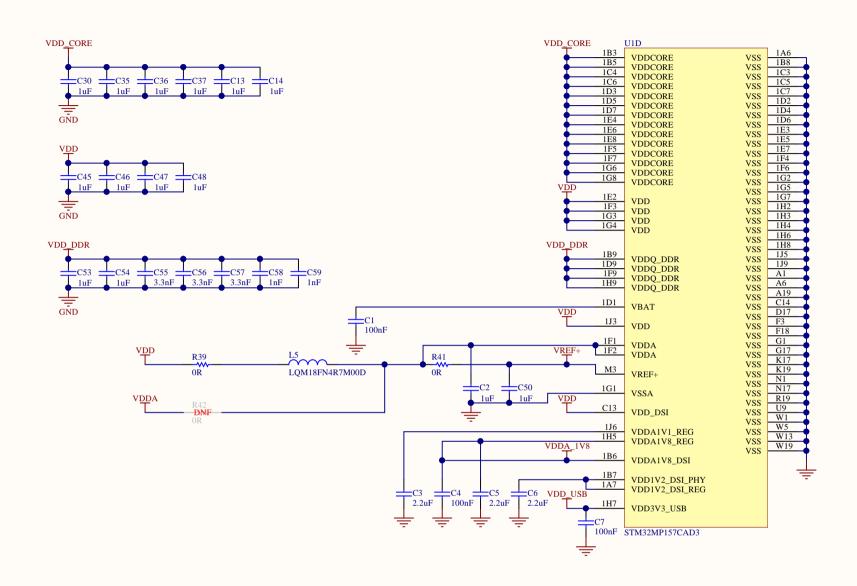
Title: STM3	2MP15XXAD 1DDR3	Ly16-TOP	
Project: STN			
Variant: Exa			
Revision: B	-01	Reference:	lijfe_auamente
Sizo: A4	Data: 21 IAN 10	Shoot: 2 of 7	Tie-dugmente



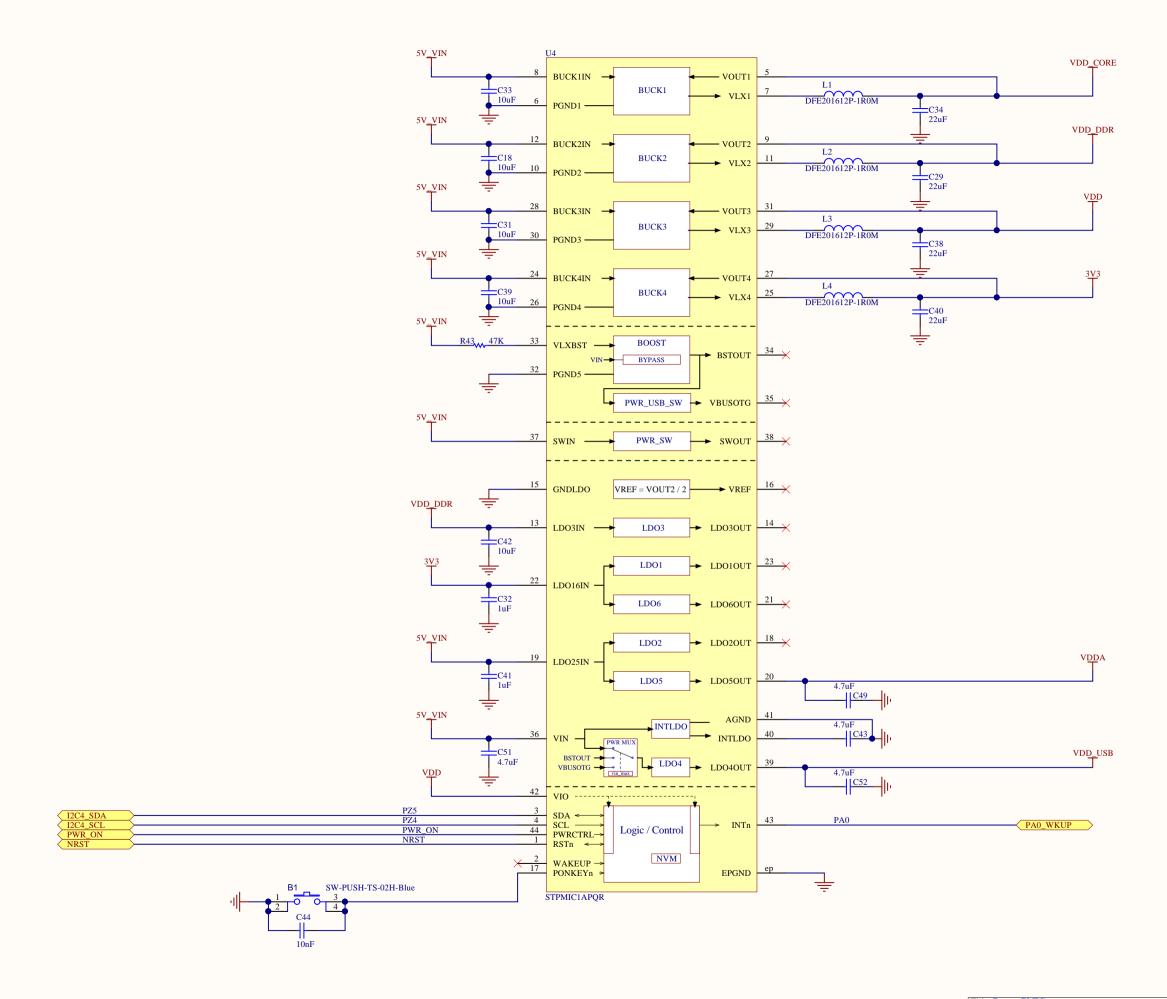
POWER	
PWR ON	PWR_ON
NRST	NRST
	PA0 WKUP
PA0_WKUP	I2C4 SDA
I2C4_SDA	I2C4_SCL
I2C4_SCL	



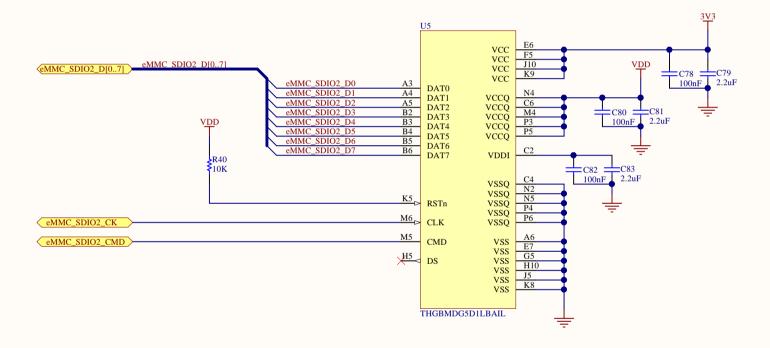
Title: MPU			
Project: STN	I32MP15XXAD_1DD	PR3Lx16	
Variant: Exa	mple		
Revision: B-01		Reference:	11/4
Size: A3	Date: 31-JAN-19	Sheet: 3 of 7	life_auamente



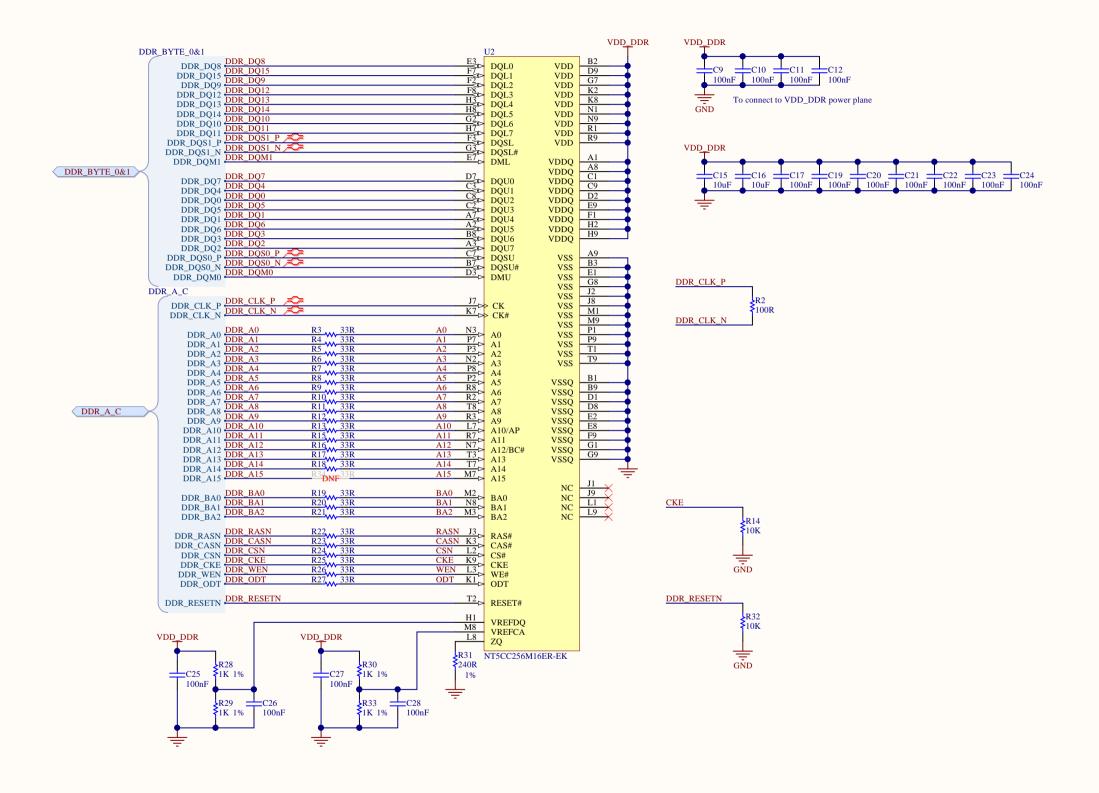
Title: Power	r_MPU		
Project: STN	/I32MP15XXAD_1DI	R3Lx16	
Variant: Exa	mple		
Revision: B	-01	Reference:	life guamer
Size: A3	Date: 31-JAN-19	Sheet: 4 of 7	life_auamer



Title: Power	PMIC		
Project: STN	432MP15XXAD_1DD	R3Lx16	
Variant: Exa	mple		
Revision: B	-01	Reference:	life auges
Size: A3	Date: 31-JAN-19	Sheet: 5 of 7	life.auame



Title: eMM0			
	I32MP15XXAD_1DD1	R3Lx16	
Variant: Exa	mple		
Revision: B	-01	Reference:	life augmented
Size: A3	Date: 31-JAN-19	Sheet: 6 of 7	life.auamented



Title: 1DDR	3L_16bits		
Project: STM32MP15XXAD_1DDR3Lx16			
Variant: Exa	mple		
Revision: B-01 Reference:		life_auamente	
Size: A3	Date: 31-JAN-19	Sheet: 7 of 7	mie-dudmente