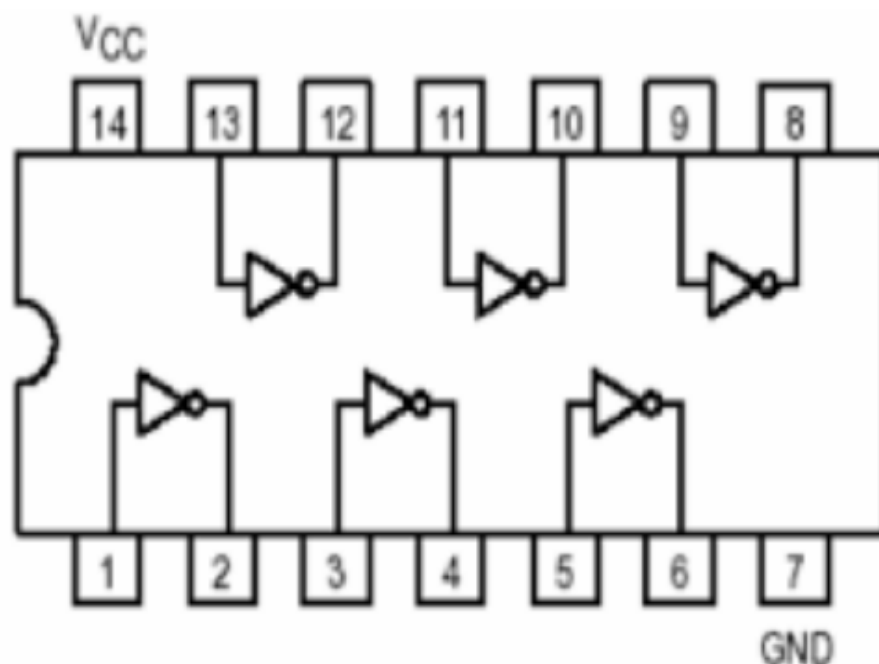


# Experiment 10 :

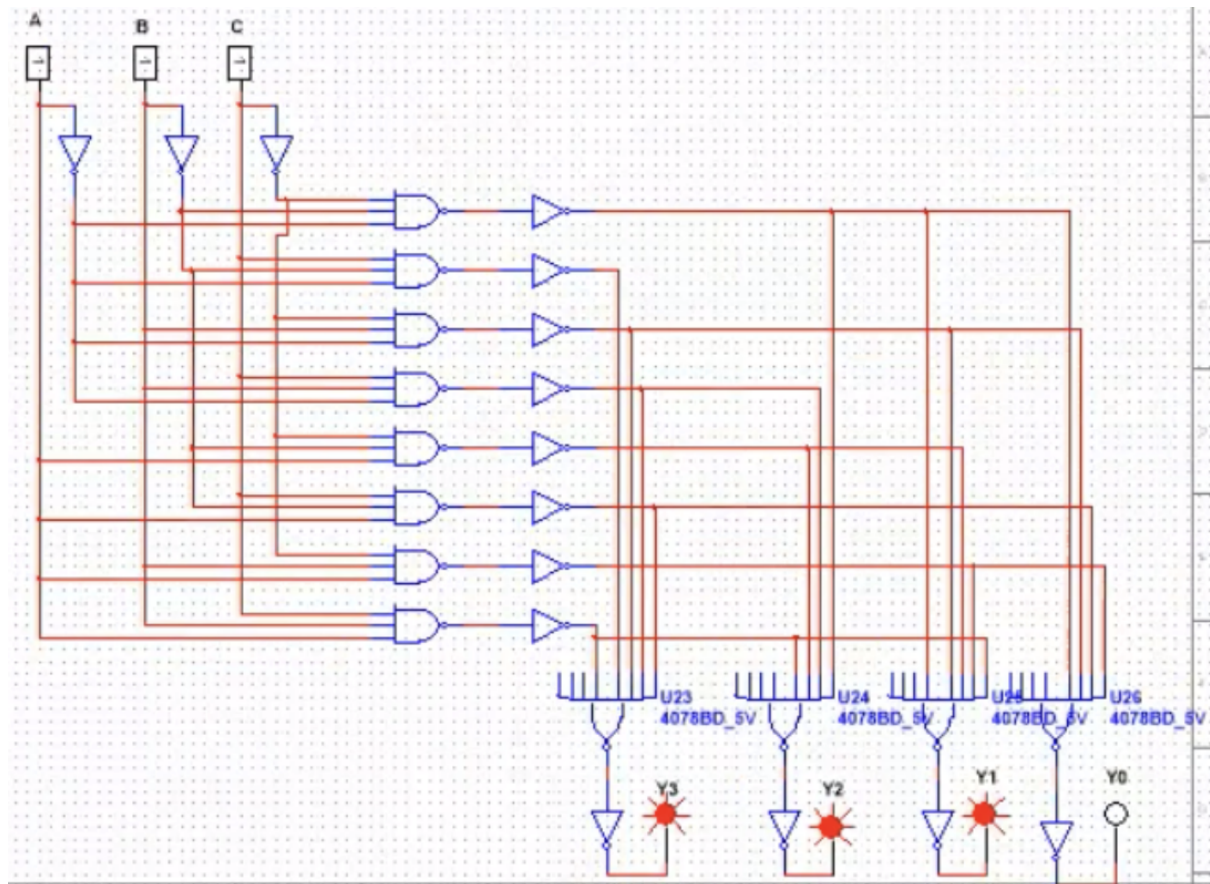
- **Aim** : Design a ROM(8x4) using a decoder, gates and diodes.
- **Apparatus Required** : 1. Trainer Kit (Micro LABORATORY Kit -II) or Wish board, Power supply
- **Software Used** : NI Multism 14.2 software.

## PIN Diagram :

### Pin Diagram of IC 7404 (HEX NOT GATES)



## Circuit Diagram :



## Theory :

### ***ROM (Read Only Memory) :***

ROM is used for storing programs that are permanently resident in the computer and for tables of constants that do not change in value once the production of the computer is completed. In this experiment we will design a 8X4 ROM for storing 4 bit data/code at 8 different memory locations using diodes and then read data/code from any of the 8 locations by sending three bit address via an address decoder circuit.

## Result :

| Address bits   |                |                | Location        | Output bits    |                |                |                | O/P<br>In<br>Hex |
|----------------|----------------|----------------|-----------------|----------------|----------------|----------------|----------------|------------------|
| A <sub>2</sub> | A <sub>1</sub> | A <sub>0</sub> |                 | Y <sub>3</sub> | Y <sub>2</sub> | Y <sub>1</sub> | Y <sub>0</sub> |                  |
| 0              | 0              | 0              | 1 <sup>st</sup> | 0              | 1              | 1              | 1              | 7 <sub>H</sub>   |
| 0              | 0              | 1              | 2 <sup>nd</sup> | 1              | 0              | 0              | 0              | 8 <sub>H</sub>   |
| 0              | 1              | 0              | 3 <sup>rd</sup> | 1              | 0              | 1              | 1              | B <sub>H</sub>   |
| 0              | 1              | 1              | 4 <sup>th</sup> | 1              | 1              | 0              | 0              | C <sub>H</sub>   |
| 1              | 0              | 0              | 5 <sup>th</sup> | 0              | 1              | 1              | 0              | 6 <sub>H</sub>   |
| 1              | 0              | 1              | 6 <sup>th</sup> | 1              | 0              | 0              | 1              | 9 <sub>H</sub>   |
| 1              | 1              | 0              | 7 <sup>th</sup> | 0              | 0              | 1              | 1              | 3 <sub>H</sub>   |
| 1              | 1              | 1              | 8 <sup>th</sup> | 1              | 1              | 1              | 0              | E <sub>H</sub>   |

## Conclusion :

ROM(8x4) has been designed successfully.