

# Weekly Report: Apr. 19 – Apr. 25

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# Weekly Report

- ▶ Bug identified and removed in XNOR based SIMD
- ▶ Bug identified and removed in dumping of input and output data
- ▶ Successful post-synthesis simulation for MVAU Batch Unit
- ▶ Complete tool flow established for regression test and comparison of HLS and RTL output
- ▶ Tool flow consists of the following:
  - ▶ Generate configuration files for HLS and RTL
  - ▶ Run HLS simulation and synthesis
  - ▶ Run RTL function simulation, synthesis and post-synthesis timing simulation
  - ▶ Extracting of performance data
  - ▶ Dumping of performance data in an output worksheet