

Weekly Report: Mar. 29 – Apr. 04

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Weekly Report

- ▶ Regression test for matrix vector activation batch and stream unit set up for all cases
- ▶ Integration of HLS simulation with RTL simulation and analysis in progress
- ▶ Key issue → Marix vector activation unit test failing for HLS when word length for weights > 1 bit. Non-generic, fixed solution implemented. Need for a generic solution.
- ▶ Key issue → Multiple hold violations in the RTL design after synthesis
- ▶ Key issue → No hold violations after implementing design (map/P&R)
- ▶ Next
 - ▶ Identifying hold violation problem, could it be a tool bug?