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Compiler/MSP430FR5969: FRAM2



[sadasivam arumu...](#) *Intellectual 940 points*
Community Member

Part Number: [MSP430FR5969](#)

Tool/software: TI C/C++ Compiler

Hi Sir,

Regarding FRAM2: Does 100% usage of FRAM2, will cause any inconsistency in MSP430FR5969 MCU. And to know about how the code is split to FRAM and FRAM2.(Case Study: I have deleted some functions in FRAM2. At that time, the remaining function in FRAM2 gets allocated to FRAM) If I want to put the function in FRAM from FRAM2, what are all the steps to be done?

[over 6 years ago](#)



[JD Crutchfield](#) *over 6 years ago*

[TI_Mastermind](#) 28745 points

Hello,

There should be no issues with using 100% of the FRAM on the MSP430FRxx devices.

The linker chooses where to put functions and all compiled code. If you want to specify where the linker places the code, you can modify the Linker command file. If you want to put a table or function at a specific memory address, you can force this with pragma's but it's not typically advisable. I would recommend letting the linker choose where to place things.

Is there a specific reason why you want it in FRAM2 vs FRAM?

If you can provide more background on exactly what you are doing, then maybe I can point you to an example or maybe there are other E2E threads where users have done something similar.



Thanks,
JD

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