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mcp430fr5969: spi_data_outlet_issue

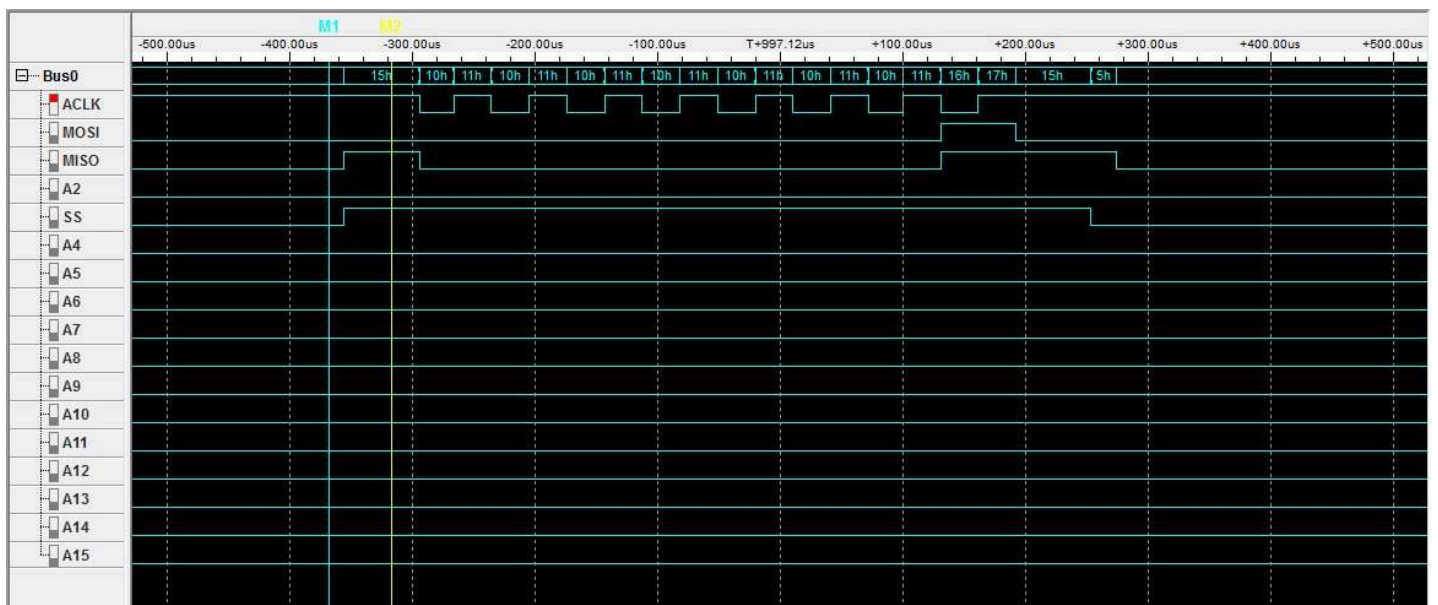


sadasivam arumu... **Intellectual** 940 points
Community Member

Part Number: [MSP430FR5969](#)

Hi Sir,

I am developing a code based on SPI module. Now I am working on example codes related to spi(4 pin mode). My doubt is regarding, I am sending data(0x01) to slave and Echoed back to master with the same data. But in Logical Analyzer I have noticed the following data. When i Debugged, I got 0x01 in master and 0x80 in slave. Is there a problem being in my code or whether there is some other issue.(Note: A2 pin is not connected). If it is worked then i would like to interface the same uC with BLE.





[over 8 years ago](#)



[Ryan Brown1](#) *over 8 years ago*

[TI_Guru****](#) 209677 points

Hi sadasivam,

What are your UC[A/B]xCTLW0 register settings and what slave device are you communicating with? The fact that you are getting a response means that the connections and timing are correct but some control bit is most likely misunderstood between devices. It could be the UCMSB bit since 0x80 is the reverse direction of 0x01. You could confirm this with further test data points.

Regards,
Ryan

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