

Microprocessor Assignment II

Vth Semester, CMP Department- D12/B

Date of Assignment- 08/10/2014

Date of Submission- 21/10/2014

1. Write short note on
 - (a) Register organization in Pentium Processors
 - (b) Integer and floating point unit pipelining in Pentium
2. Discuss memory protection mechanism and paging structure in 80386.
3. What are the different modes of operation in 80386?
4. Explain the architecture of 8087 math co-processor.
5. Write short note on SPARC Architecture.