CURRICULUM VITAE Adrian Samociuk, M.Sc.

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Areas of expertise: Personal summary:

Electronic engineer by profession and passion. Altium Designer Strong technical background from the best technical Signal Integrity

university in Poland. Experience from projects on Erasmus C/C++

student exchange allows to work efficiently in the Eclipse-based IDE international team. "Can Do" attitude and eager to learn

Real-time systems new skills. Knowledge and ability to fulfill requirements

Matlab confirmed by successfully finished projects. For more

details please visit my portfolio webpage.

Version Control Systems (SVN, GIT)

Education:

Oct. 2013 - Mar. 2015 Master's degree studies

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University: Warsaw University of Technology

Faculty: **Electronics and Information Technology** Specialization: Micro-systems and Electronic Systems

MSc Thesis:

"Real-time measurement system with network interface utilizing asymmetric, 2-core processors of LPC4300 series" (device PCB and software) with Krzysztof Poźniak, Ph.D., DSc. and Grzegorz Kasprowicz, Ph.D. as supervisors.

Jan. 2013 - Jun. 2013 International exchange Studies – LPP-ERASMUS

University: Chalmers University of Technology Embedded Electronic System Design Specialization:

Courses: Real-time systems (EDA222)

Embedded control systems (SSY190)

Remote sensing (RRY055)

Oct. 2006 – Mar. 2012 Bachelor's degree studies

University: Warsaw University of Technology

Faculty: Electronics and Information Technology Specialization: Electronics and Computer Engineering

BSc Thesis:

"Intelligent BLDC servo motor controller with high-level commands" (device PCB and software) with Krzysztof Poźniak, Ph.D., DSc. and Grzegorz Kasprowicz, Ph.D. as supervisors.

Work experience:

May 2012 – Dec. 2012 Resp

Impact Clean Power Technology S.A. Electronic Engineer Assistant in Embedded Software Department

Responsibilities:

- test and compare new Integrated Circuits for future use in the Battery Management System devices,
- diagnosis and repair of the damaged prototype boards and laboratory equipment
- prepare software for the electric and hybrid car display (PLC),
- test and calibrate finished product with NI PXI verification system

Mar. 2011 – May 2011

JM-Tronik Sp. z o.o. Trainee in R&D department

Responsibilities:

- design and construction of a test stands and equipment for the electrical energy meters,
- diagnosis and repair of damaged prototype boards,
- test and calibration.
- changes in schematics and implementation of a new features in cooperation with other engineers

Skills:

- Knowledge of Altium Designer, LabView, MATLAB, VisiLogic software
- Knowledge of EMC and Signal Integrity issues
- Electrical license E1 to operate an equipment, systems and networks with a voltage of less than 1kV (by Polish Electricians Association)
- Programming languages: C, C++, VHDL, Python, ladder
- Experience with TI, STM32, NXP microcontrollers (Cortex M4, M3 and M0) and their interfaces.
- Market Solutions knowledge by Freescale seminars
- STMicroelectronics certificate of programming ARM Cortex-M3 microcontrollers

- Knowledge of version control systems (e.g. SVN, GIT) and project tracking/management software (JIRA)
- Proficiency in operating systems (Linux, Windows)
- Experience with office and documentation programs (e.g. LaTeX, MS Office, OpenOffice)
- Ability to think analytically and make decisions
- Cat. B drivers license, clean driving record
- Languages:

Polish - native
English - advanced
Russian - basic
German - basic

Swedish - beginner (in progress)

Interests:

- Repairing electronic devices as a hobby (soldering, parts replacement)
- Construction of remotely operated vehicles
- Cooking for family and friends
- Sports (gym, formerly judo, capoeira)
- Fantasy literature

References:

Grzegorz Kasprowicz, Ph.D.
 CEO / R&D manager, Creotech Instruments S.A.

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Paweł Irzmański

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