

Taller Proyectos 1. Datalogger

USB-Power  
PCB0-Power-USB

G4: Andrés, Iván, Lucía, Víctor R, Alonso

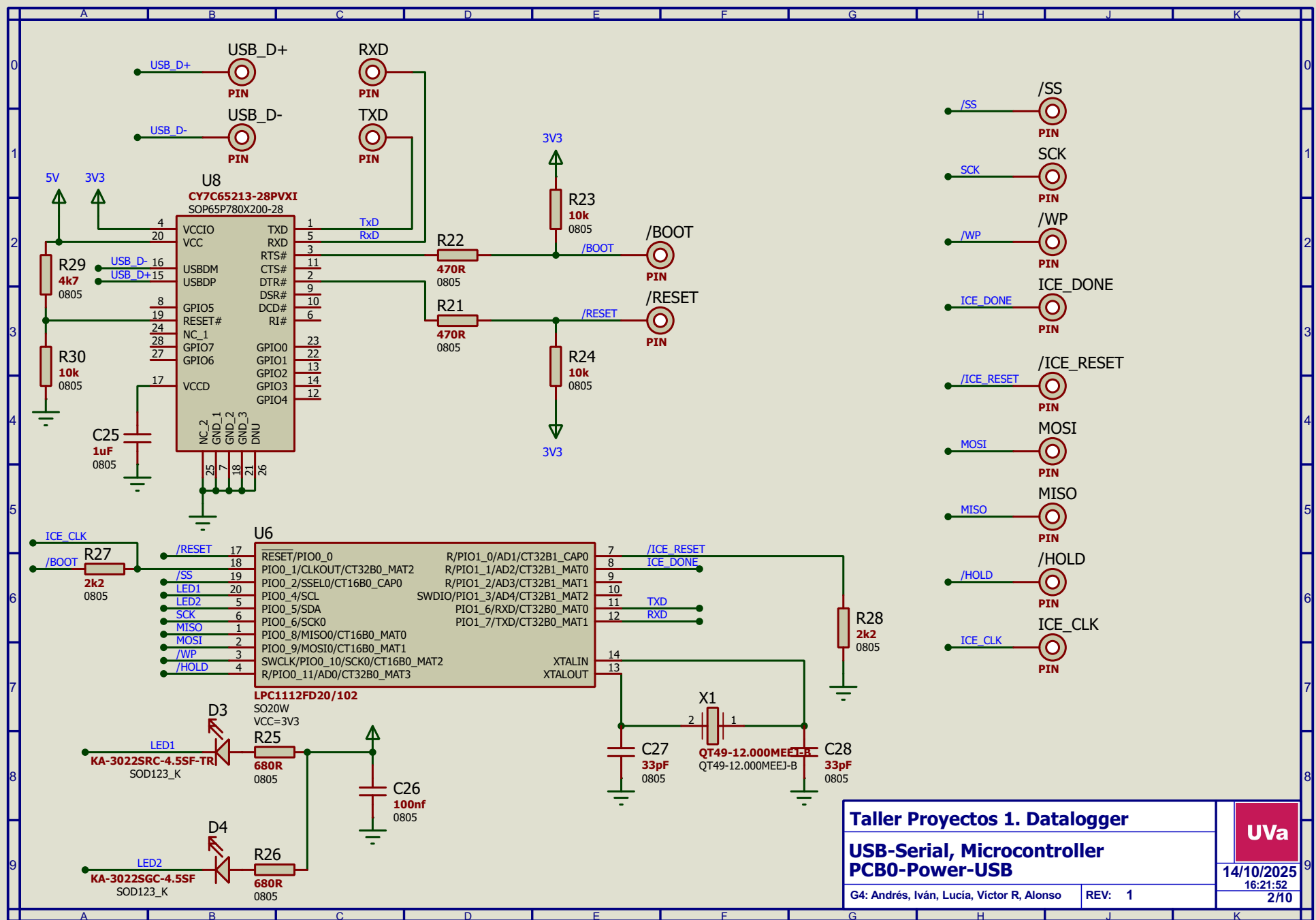
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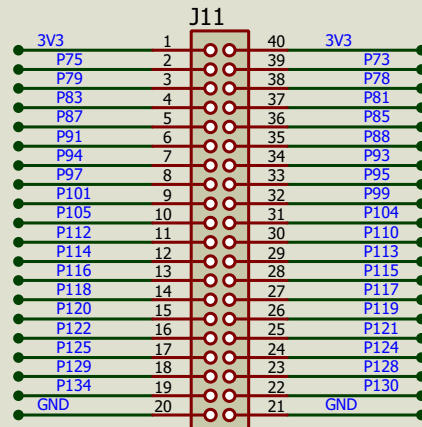
UVa

14/10/2025

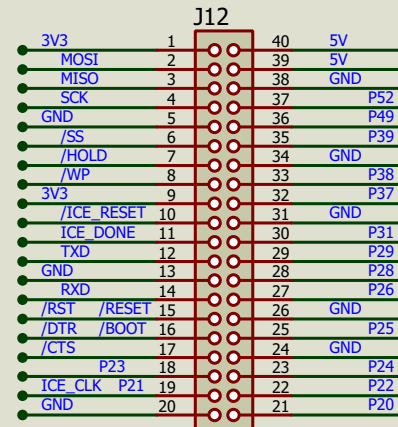
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1/10

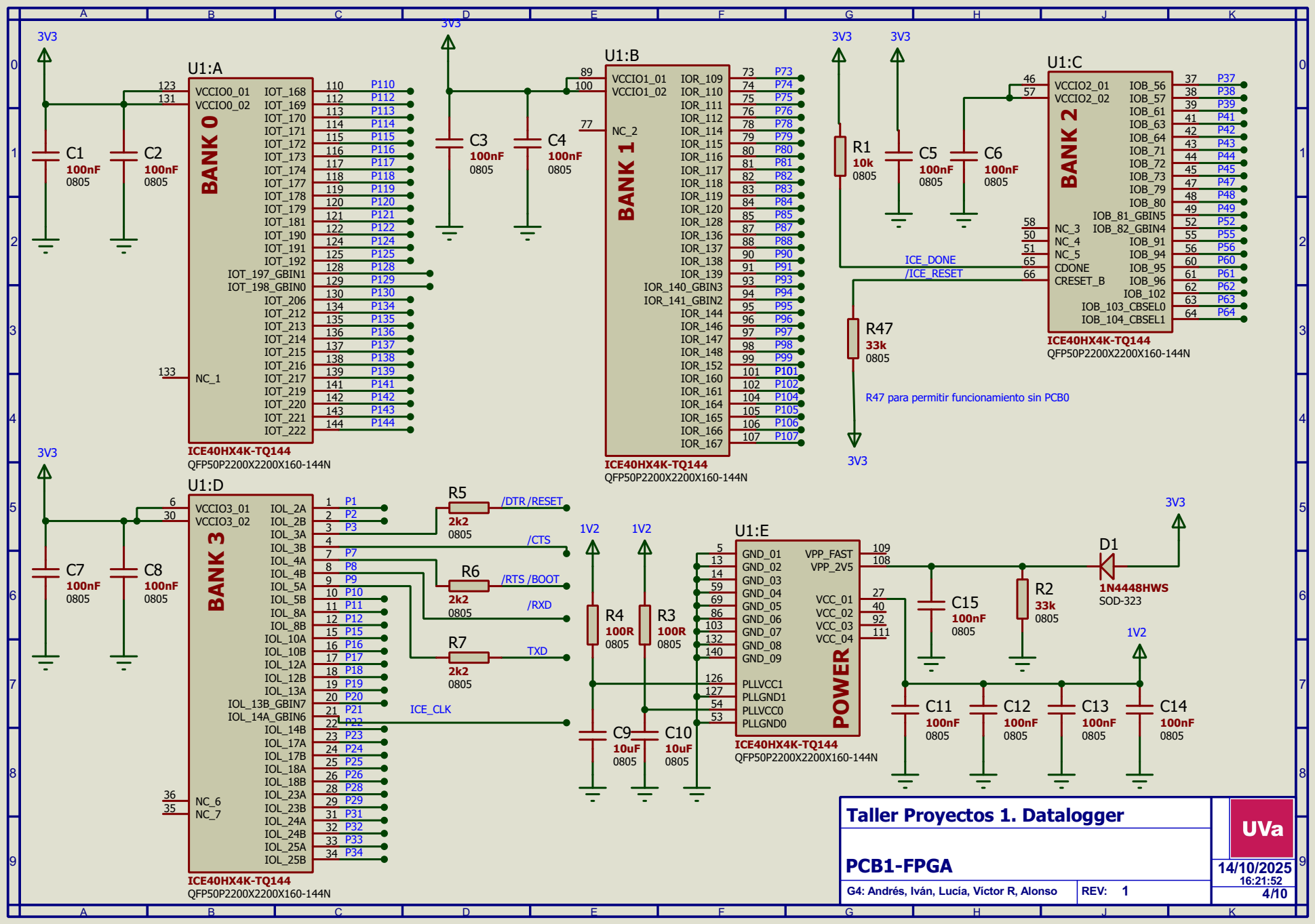




**2214S-40SG-85**  
CONN-DIL40-ROUND



**2214S-40GS-85**  
CONN-DIL40-ROUND



Taller Proyectos 1. Datalogger

PCB1-FPGA

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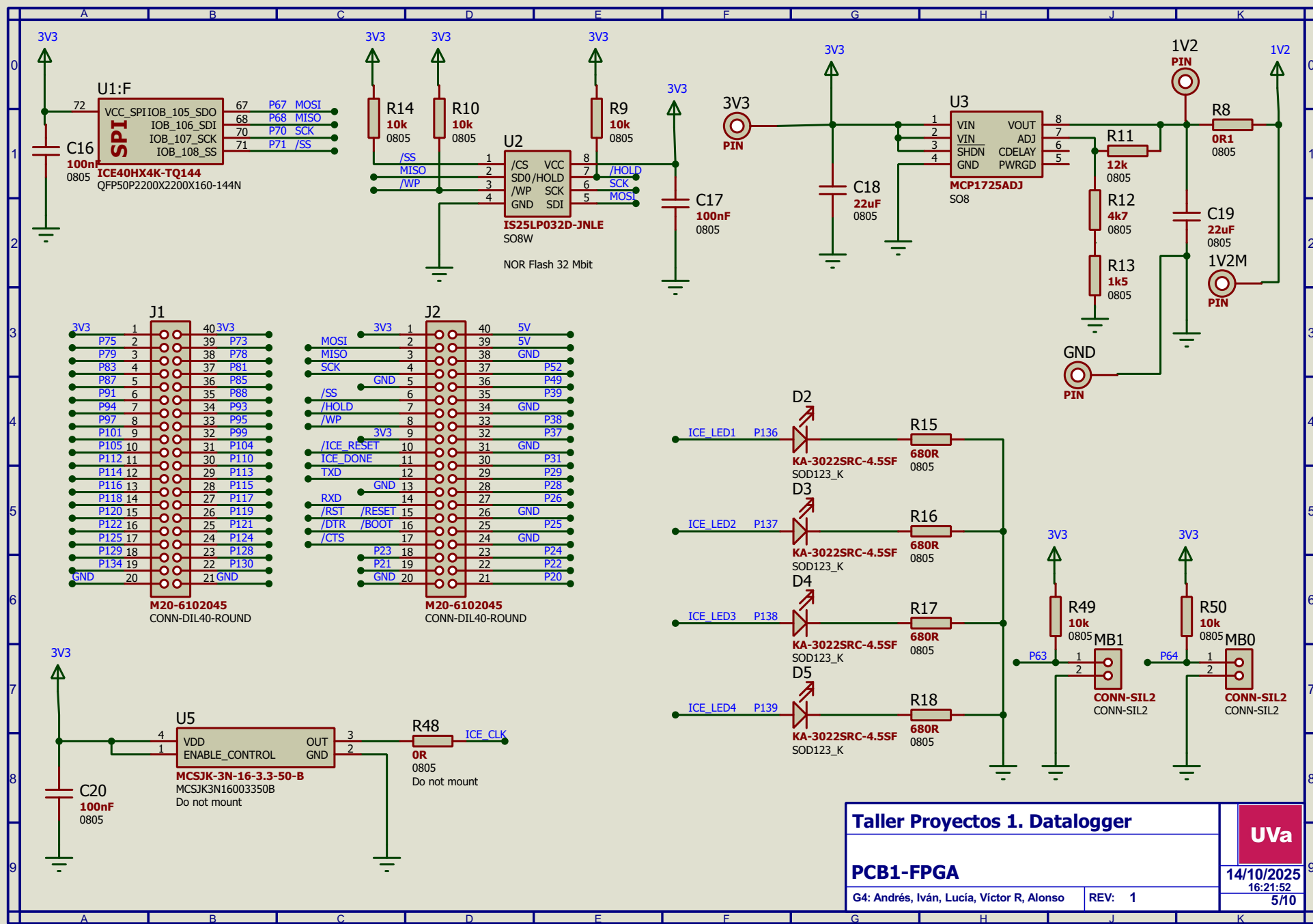
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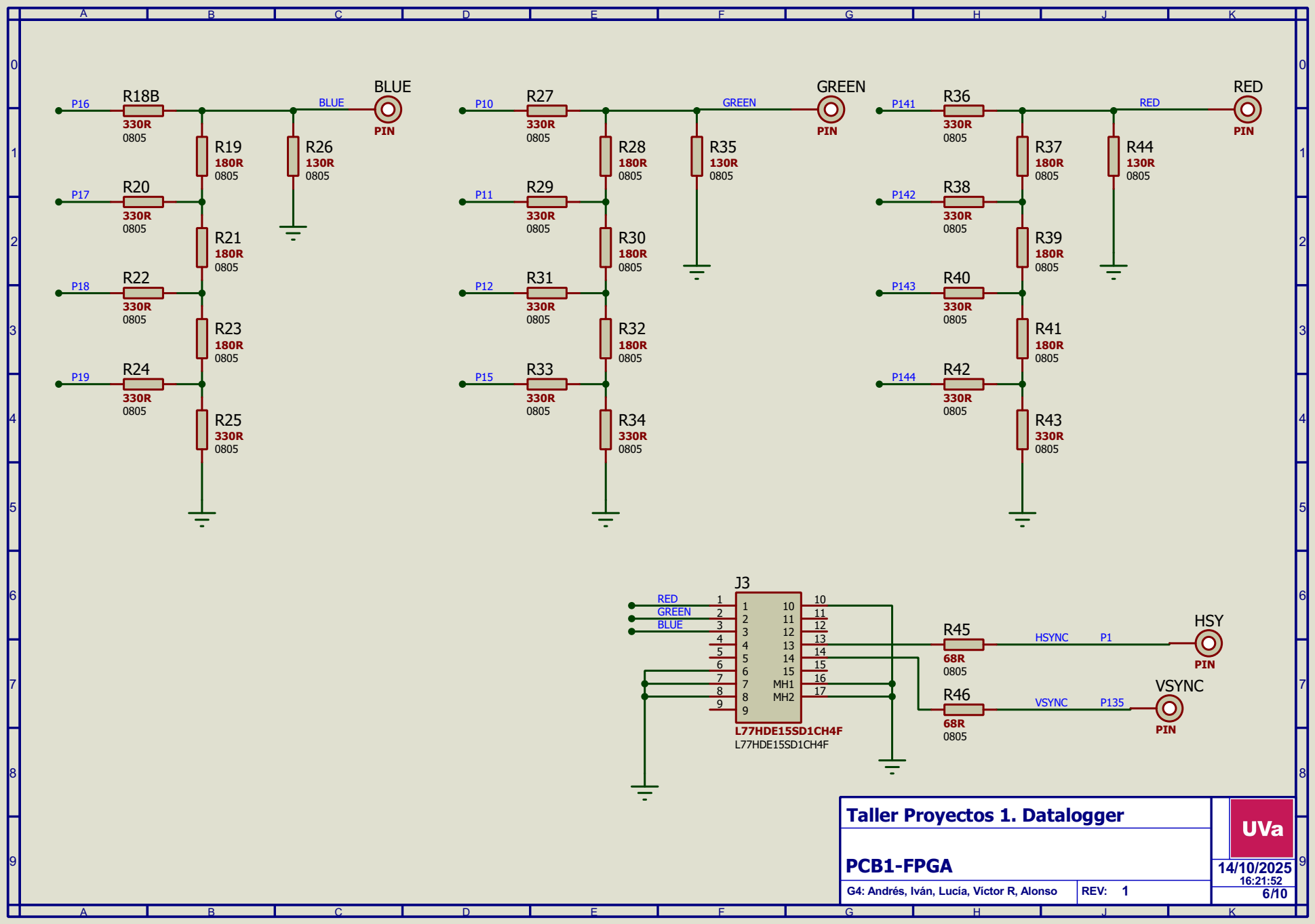
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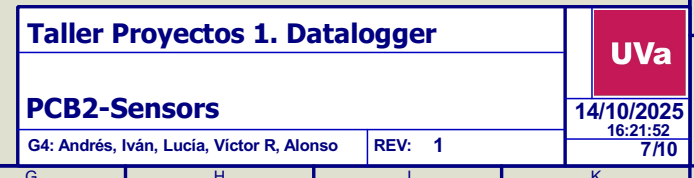
14/10/2025

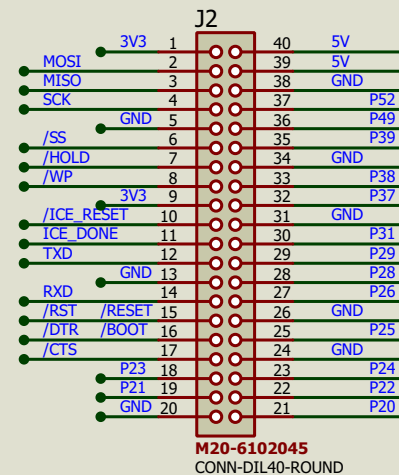
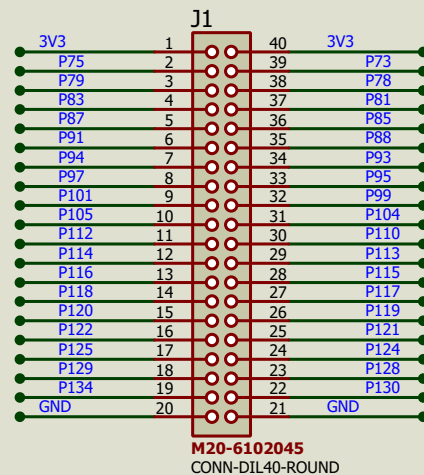
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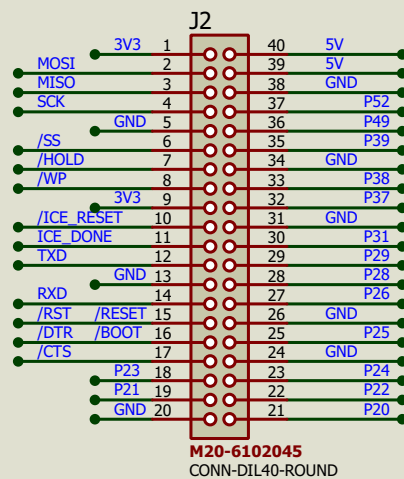












A	B	C	D	E	F	G	H	J	K
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1									
2									
3									
4									
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8									
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A	B	C	D	E	F	G	H	J	K

Taller Proyectos 1. Datalogger		<div>UVa</div>	
PCB1-FPGA			14/10/2025 16:21:52
G4: Andrés, Iván, Lucía, Víctor R, Alonso	REV: 1		1010