

ASANKA SOVIS

PERSONAL INFORMATION

Born	Sri Lanka, 12 th September 1998
Email & Social	akashsovis@gmail.com · LinkedIn · Github
Goal	Telecommunication engineering pending graduate with experience in embedded engineering, currently working at SenzMate IoT Intelligence as an embedded engineer, seeking further opportunities.

WORK EXPERIENCE

<i>SenzMate IoT Intelligence, Colombo</i>	<i>Dec 2023 - Present</i>	Associate Embedded Engineer, SENZMATE <ul style="list-style-type: none">· Research, design, implement, manage & test embedded systems & programs.· Identify areas of modification & write efficient code bases.· Brainstorm ideas with team members & guide junior team members.
	<i>May 2023 - Nov 2023</i>	Embedded Engineer - Intern, ATLAS POPULI <ul style="list-style-type: none">· Trainee embedded engineer fulfilling requirements for the degree program.· Part of the team that completed the Krakatoa Ko.5 project.· Developed test firmware, soldered & validated PCBs, & manufactured, bench-tested & debugged sub-assemblies including the machines themselves.
<i>MobForce Agency, New York (Remote)</i>	<i>Dec 2020 - Jun 2021</i>	Project Manager, MOBFORCE AGENCY <ul style="list-style-type: none">· Managed internal projects, prepared reports, coordinated the design team & marketing team & handled online meetings.· Additionally did market research, built project playbooks, social media handling, & SEO. <i>Reference: George WEST · Founder & Current owner · georgewestjr88@gmail.com</i>

EDUCATIONAL QUALIFICATIONS

<i>BEng. (Hons) in Electrical, Electronics & Communications Engineering</i>	<i>Apr 2019 - Present</i>	SLTC Research University, Padukka <ul style="list-style-type: none"><i>School: School of Engineering</i><i>Description: Focused on electronics engineering while specializing in telecommunication engineering. In addition to theoretical knowledge; special focus on self-learning & project-based experience.</i><i>(Pending Graduation: April 2024)</i>
<i>School Education</i>	<i>Jan 2004 - Sep 2018</i>	Maris Stella College, Negombo

ADDITIONAL QUALIFICATIONS

<i>Diploma in Software Engineering</i>	<i>Sep 2018 - Jun 2019</i>	ESoft Metro Campus, Negombo <ul style="list-style-type: none"><i>Description: Software development life cycle, basic programming languages such as Java, Python & C++ were included in the training. In addition, database development & software testing techniques were included.</i>
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CERTIFICATIONS

<i>Yonsei University on Coursera Online</i>	<i>Apr 2022</i> · IoT Wireless & Cloud Computing Emerging Technologies Certification Link
	<i>Apr 2022</i> · Smart Device & Mobile Emerging Technologies Certification Link

BerkeleyX on edX
Online

Dec 2021 · Professional Certificate for Blockchain Fundamentals
[Certification Link](#)

MITX on edX
Online

Feb 2020 · Verified Certificate for Introduction to Computer Science & Programming Using Python
[Certification Link](#)

SKILLS & EXPERTISE

Technical Skills	PCB design, C/C++ programming, Verilog & HDL development, Version control, Linux, Zephyr & other RTOS, OOP concepts, Systems development
Platforms	AMD XILINX, INTEL QUARTUS, AVR, ARDUINO, STM32, NORDIC NRF
Languages	ASSEMBLY, PYTHON, JAVA, JAVASCRIPT, C/C++, VISUAL C#, VISUAL BASIC, PHP
Extra Skills	Blockchain technology, Cybersecurity, Agile development, Networking, Project management, Documentation, L ^A T _E X

PROFESSIONAL PROFILE

Portfolio	<ul style="list-style-type: none">· Final Year Project on Developing an Optimized Timetable Scheduling Algorithm using Machine Learning, Cluster Computing & High-Performance Computing: Project Almanac.· Other noteworthy projects including Indoor Positioning System using FPGA, Prometheus: Smart fire alarm system, Metroparking: Smart parking system, Muragala password manager & Akaradiya: Opensource sinhala dictionary.· Maintain blogs related to programming & electronics on Blogger & Hashnode.
Publications	<ul style="list-style-type: none">· Enhanced Timetable Scheduling: A High-Performance Computational Approach — Publisher: IEEE
Achievements	<ul style="list-style-type: none">· Completed the Aquinas Diploma in English Language & Literature with Distinction Pass in 2019.· Represented the school in the Sri Lanka Robotics Championship (SLRC) organized by the E-Club of the University of Moratuwa in 2016.
Organizations	<ul style="list-style-type: none">· Former member of the Students for the Exploration & Development of Space (SEDS) SLTC club of Sri Lanka Technological Campus since 2020.· Former member of the University Student Enterprise (USE) club of Sri Lanka Technological Campus since 2020.
Languages	SINHALA · Mothertongue ENGLISH · Intermediate (conversationally & literary fluent)
Interests	Guitar · Workout · Swimming · Blogging · Tinkering with Electronics

REFERENCE

Yohan Pandigama	Founder - C2L3 Lecturer (CS) - Sri Lanka Technological Campus, Padukka. Former HoD - IT - CINEC Campus, Malabe. MIEEE, MIEEE - CS, MACM, MBCS, MCS (SL), MCS, BIT yohanp@sltc.ac.lk · +94 71 300 8883
Lasith Premaratne	Senior Embedded Engineer - Atlas Labs, Sri Lanka. B.Eng Swinburne University of Technology, Australia. lasithishan@gmail.com · +94 77 68 53 610