

ASANKA SOVIS

PERSONAL INFORMATION

Born	Sri Lanka, 12 th September 1998
Email & Social	akashsovis@gmail.com · LinkedIn · Github
Goal	Electronics engineering graduate with experience in Embedded Engineering, currently working at Zebra Technologies as a Software Engineer, seeking further opportunities.

WORK EXPERIENCE

Zebra Technologies, Colombo	Oct 2024 - Present	Software Engineer, I (Grade 10), ZEBRA
		<ul style="list-style-type: none">· Design, develop & test a variety of software assignments that constitute a portion of a larger project.· Develop & test documentation which describes the software functionality.· Run & review software tests designed by more experienced engineers.
	Jun 2024 - Oct 2024	Embedded Engineer, SENZMATE
SenzMate IoT Intelligence, Colombo	Dec 2023 - May 2024	Associate Embedded Engineer, SENZMATE
		<ul style="list-style-type: none">· Researched, designed, implemented, managed & tested embedded systems including firmware & automation scripts.· Identified areas of modification & wrote efficient code bases.· Brainstormed ideas with team members & guided junior team members.
	May 2023 - Nov 2023	Embedded Engineer - Intern, ATLAS LABS
Atlas Labs, Colombo		<ul style="list-style-type: none">· Fulfilled the requirements of the degree program as a trainee engineer.· Part of the team that completed the Krakatoa Ko.5 project.· Developed test firmware, soldered & validated PCBs, & manufactured, bench-tested & debugged sub-assemblies including the prototypes themselves.

EDUCATIONAL QUALIFICATIONS

BSc. (Hons) in Engineering in Electronics & Telecommunica- tions	Apr 2019 - Nov 2023	SLTC Research University, Padukka
		School: School of Engineering Description: Focused on electronics engineering while specializing in telecommunication engineering. In addition to theoretical knowledge; special focus on self-learning & project-based experience. Research Project: Project Almanac
	Jan 2004 - Sep 2018	Maris Stella College, Negombo

CERTIFICATIONS

BerkeleyX on edX Online	Dec 2021 · Professional Certificate for Blockchain Fundamentals Certification Link
MITX on edX Online	Feb 2020 · Verified Certificate for Introduction to Computer Science & Programming Using Python Certification Link

SKILLS & EXPERTISE

<i>Technical Skills</i>	PCB Design, Firmware Development, Hardware Design & FPGA, Embedded Linux, Zephyr & other RTOS, Systems Integration, QNX Development
<i>Platforms</i>	AMD XILINX, INTEL QUARTUS, ATMEL, ARDUINO, STM32, ESP32, NORDIC NRF
<i>Languages</i>	C/C++, ASSEMBLY, PYTHON, BASH, JAVA, JAVASCRIPT, VISUAL C#, RUST, PHP
<i>Extra Skills</i>	Blockchain Technology, Reverse Engineering, Cybersecurity, Agile Development, Networking, Project Management, Documentation, \LaTeX

PROFESSIONAL PROFILE

<i>Portfolio</i>	<ul style="list-style-type: none">· Final year project on developing an optimized timetable scheduling system using machine learning, cluster computing & high-performance computing: Project Almanac.· Other noteworthy projects including Indoor Positioning System using FPGA, Prometheus: Smart Fire Alarm System, Metroparking: Smart Parking System, Muragala Password Manager & Akaradiya: Opensource Sinhala Dictionary.· Maintain blogs related to programming & electronics on Blogger & Hashnode.
<i>Publications</i>	<ul style="list-style-type: none">· Enhanced Timetable Scheduling: A High-Performance Computational Approach — Publisher: IEEE
<i>Achievements</i>	<ul style="list-style-type: none">· One of the top 100 submissions of the Make it Matter, 2023 competition held by Nordic Semiconductor on hackster.io, received by the Prometheus Fire Alarm System v2.0.· Completed the Aquinas Diploma in English Language & Literature with Distinction Pass in 2019.· Represented the school in the Sri Lanka Robotics Championship (SLRC) organized by the E-Club of the University of Moratuwa in 2016.
<i>Organizations</i>	<ul style="list-style-type: none">· Former member of the Students for the Exploration & Development of Space (SEDS) club of SLTC Research University since 2020.· Former member of the University Student Enterprise (USE) club of SLTC Research University since 2020.
<i>Languages</i>	SINHALA · Mother tongue ENGLISH · Intermediate (conversationally & literary fluent)
<i>Interests</i>	Guitar · Workout · Swimming · Blogging · Tinkering with Electronics

REFERENCE

<i>Yohan Pandigama</i>	Founder - C2L3 Former Lecturer (CS) - SLTC Research University, Padukka. Former HoD - IT - CINEC Campus, Malabe. MIEEE, MIEEE - CS, MACM, MBCS, MCS (SL), MCS, BIT yohanp@sltc.ac.lk · +94 71 300 8883
<i>Lasith Premaratne</i>	Senior Embedded Engineer - Atlas Labs, Sri Lanka. B.Eng Swinburne University of Technology, Australia. lasithishan@gmail.com · +94 77 68 53 610