ASANKA SOVIS

PERSONAL INFORMATION

Born in Sri Lanka, 12 September 1998

email & social   akashsovis@gmail.com   ·   LinkedIn   ·   Github

Goal   Telecommunication Engineering Pending Graduate with experience in hardware, electronics, programming & computer science, seeking to improve the skillset by working with a prominent organization as an Embedded Engineer.

WORK EXPERIENCE

May 2023 - Nov 2023   Embedded Engineer - Intern, ATLAS POPULI

Atlas Populi, Colombo
· Trainee embedded engineer fulfilling requirements for the degree program.
· Part of the team that completed the Krakatoa Ko.5 project.
· Developed test firmware, soldered & validated PCBs, & manufactured, bench-tested & debugged sub-assemblies including the machines themselves.

Dec 2020 - Jun 2021   Project Manager, MOBFORCE AGENCY

MobForce Agency, New York (Remote)
· Managed internal projects, prepared reports, coordinated the design team & marketing team & handled online meetings.
· Additionally did market research, built project playbooks, social media handling, & SEO.
Reference: George WEST · Founder & Current owner · georgewestjr88@gmail.com

EDUCATIONAL QUAIFICATIONS

Apr 2019 - Mar 2024   Sri Lanka Technological Campus, Meepe

BSc (Hons) in Electronics & Telecommunication Eng.
School: School of Engineering
Description: Focused on electronics engineering while specializing in telecommunication engineering. In addition to theoretical knowledge; special focus on self-learning & project-based experience.

(Pending Graduation: March 2024)

School Education   *Jan 2004 - Sep 2018*   Maris Stella College, Negombo

ADDITIONAL QUALIFICATIONS

Sep 2018 - Jun 2019   ESoft Metro Campus, Negombo

Diploma in Software Engineering
Description: Software development life cycle, basic programming languages such as Java, Python & C++ were included in the training. In addition, database development & software testing techniques were included.

CERTIFICATIONS

Yonsei University on Coursera Online
Apr 2022 · IoT Wireless & Cloud Computing Emerging Technologies
Certification Link
April 2022 · Smart Device & Mobile Emerging Technologies
Certification Link

BerkeleyX on edX Online
Dec 2021 · Professional Certificate for Blockchain Fundamentals
Certification Link

MITX on edX	*Feb 2020* · Verified Certificate for Introduction to Computer Science & Programming Using Python Certification Link

PROFESSIONAL PROFILE

Portfolio	· Final Year Project on Developing an Optimized Timetable Scheduling Algorithm using Machine Learning, Cluster Computing & High-Performance Computing: Project Almanac. · Other noteworthy projects including Indoor Positioning System using FPGA, Prometheus: Smart fire alarm system, Metroparking: Smart parking system, Muragala password manager & Akaradiya: Opensource sinhala dictionary. · Maintain active blogs related to programming & electronics on both Blogger & Hashnode since 2021.
Publications	· Enhanced Timetable Scheduling: A High-Performance Computational Approach — Publisher: IEEE
Achievements	· Completed the Aquinas Diploma in English Language & Literature with Distinction Pass in 2019. · Represented the school in the Sri Lanka Robotics Championship (SLRC) organized by the E-Club of the University of Moratuwa in 2016.
Organizations	· An active member of the Students for the Exploration & Development of Space (SEDS) SLTC club of Sri Lanka Technological Campus since 2020. · An active member of the University Student Enterprise (USE) club of Sri Lanka Technological Campus since 2020.
Languages	SINHALA · Mothertongue ENGLISH · Intermediate (conversationally & literary fluent)
Interests	Guitar · Workout · Swimming · Blogging · Tinkering with Electronics

ADDITIONAL SKILLS

Hard Skills	Electronics, Verilog & HDL, Hardware, Assembly Programming, OOP Programming, Procedural Programming, LaTeX
Soft Skills	Attention to Detail, Time Management, Multitasking, Analytical Skills, Teamwork, Creativity, Problem Solving, Working Under Pressure
Programming Skills	ASSEMBLY, PYTHON, JAVA, JAVASCRIPT, C, C++, VISUAL C#, VISUAL BASIC, PROCESSING, PHP, VERILOG
Extra Skills	Blockchain Technology, Cybersecurity, Agile Development, Git & Github, Linux OS, Networking, Graphic Designing, Documentation Writing

REFERENCE

Yohan Pandigama	Founder - C2L3 Lecturer (CS) - Sri Lanka Technological Campus, Padukka. Former HoD - IT - CINEC Campus, Malabe. MIEEE, MIEEE - CS, MACM, MBCS, MCS (SL), MCS, BIT yohanp@sltc.ac.lk · +94 71 300 8883
Lasith Premaratne	Senior Embedded Engineer - Atlas Labs, Sri Lanka. B.Eng Swinburne University of Technology, Australia. lasithishan@gmail.com · +94 77 68 53 610