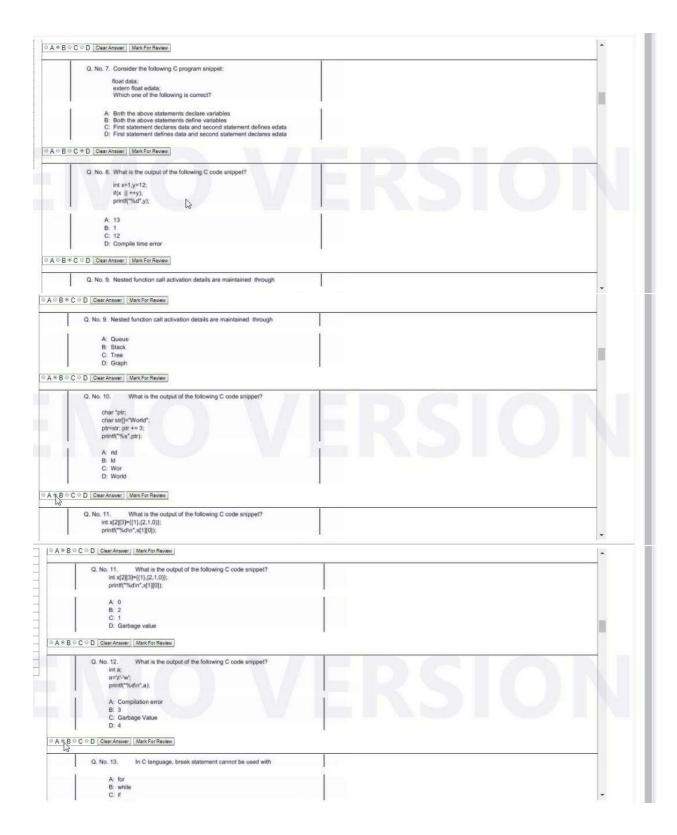
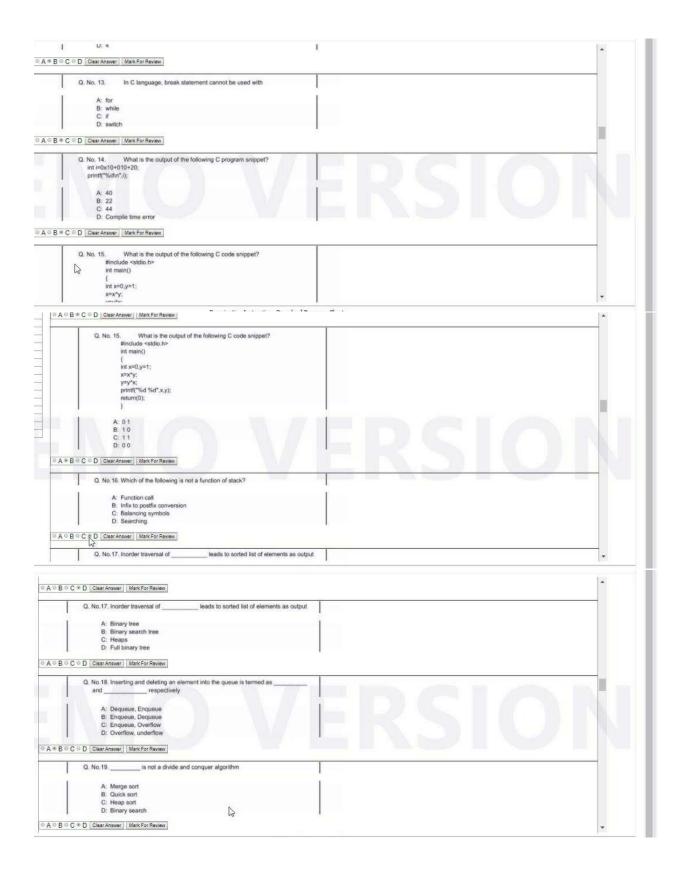
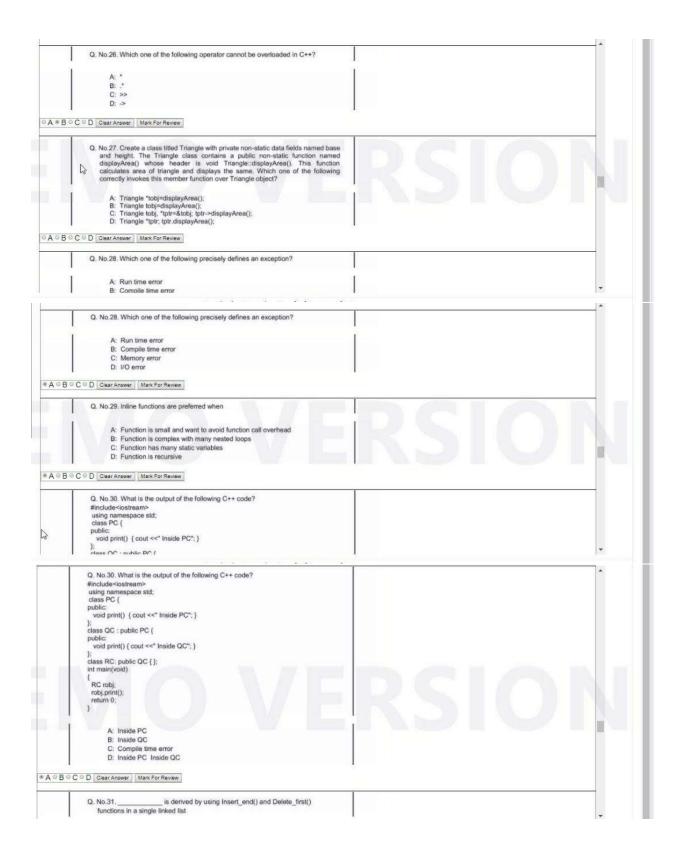
```
Q. No. 1. What is the output of the following C program?
#include<stdio.h>
                                   struct XYZ
                                      int a;
                                      struct XYZ *next;
                                  int main()
                                      struct XYZ temp;
                                      temp.a = 10;
temp.next = NULL;
printf("%d", temp.a);
                                       return 0;
                                        A: 10
B: Garbage value
C: Compile time error
                                        D: Runtime error
    ○ A ○ B ○ C ○ D Clear Answer Mark For Review
                              Q. No. 2. What is the problem with the following C program code?
           1
○ A ○ B ○ C ○ D Clear Answer Mark For Review
                    Q. No. 2. What is the problem with the following C program code?
                        #include<stdio.h>
#include <stdlib.h>
int main()

    Results in dangling pointer
    Compile time error
    Results in memory leak
    Runtime error
□ A □ B □ C □ D | Clear Answer | Mark For Review
                    Q. No. 3. What is the output of the following C program?
                        #include<stdio.h>
void g(int *x, int *y)
                         y=x;
'y = 3;
                       }
int a = 1, b = 2;
int main()
{
   ○ A ○ B ○ C ○ D | Clear Answer | Mark For Review
                       Q. No. 3. What is the output of the following C program? #include<stdio.h> void g(int *x, int *y)
                             y=x;
*y = 3;
                            int a = 1, b = 2;
int main()
                           (
g(&a, &b);
printf("%d %d in", a, b);
return 0;
● A ○ B ○ C ○ D Clear Answer Mark For Review
                       Q. No. 4. What is the output of the following program? 
#include <stdio.h>
                           int main()
                           {
int x;
if(x=1)
printf(" Good ");
else
printf(" Bed");
```

į.	G. No. 4. What is the output of the following program? #include saldio.h> int main() { int x; if(x=1) printf("Good"); else printf("Bad"); return(0); }	
0 A 9 B 0	A: Unpredictable result as x is not initiated B: Always prints Good C: Compile time error D: Always prints Bad	ONI
645	Q. No. 5. What is the output of the following C program? #include <stdio.h> #define a 10 int main() { printf("%d",a+=2); }</stdio.h>	
	A: 10 B: 12 C: Compile time error D: Runtime error	
○ A * B	Examination Instruction Download Response Sheet 8 © C © D Clear Answer Mark For Review]	
	Q. No. 5. What is the output of the following C program? #include <sidio.h> #define a 10 int main() { printf(*%d*,a**2); }</sidio.h>	
0 A 0 B 8	A: 10 B: 12 C: Compile time error D: Runtime error D: Runtime error Mark For Review	SNI
- G	Q. No. 6. What is the output of the following C program? #include <stdio.h> #define x 2+3 #define x 2+2 int main() { printf("%d",x"y); }</stdio.h>	
40 2	A: 15 B: 7 C: 8	•
	Q. No. 6. What is the output of the following C program? #include <atrio h=""> #idefine x 2+3 #idefine y 1+2 int main() { printf(*%d*,x*y);</atrio>	
	A: 15 B: 7 C: 8 D: Complie time error	
	Leg B O C D [Clear Answer] [Mark For Review]	
	Q. No. 7. Consider the following C program snippet: float data: extern float edata: Which one of the following is correct?	
	A: Both the above statements declare variables B: Both the above statements define variables C: First statement declares data and second statement defines edata D: First statement defines data and second statement declares edata	
0 A 0	, O B O C O D Clear Answer Mark For Review	*







⊕ A ○ B ○ (C D Clear Answer Mark For Review	
	Q. No.31 is derived by using Insert_end() and Delete_first() functions in a single linked list	
1	A: Stack	
	B: Queue C: Dqueue	
	D: Tree	
○ A ○ B ⊕ (© □ Clear Answer Mark For Review	
	Q. No.32 protocol finds the MAC address of a host from its known IP address.	
- 1	A: ARP	
	B: RARP C: ICMP	
	D: IGMP	
⊕ A ⊕ B ⊕ C	C □ D Clear Answer Mark For Review	
	Q. No.33. The multiple access method used in GSM cellular technology	*
P	A: FDMA & CDMA R: CDMA & TDMA	*
1	D: IGMP	*
*A OB O	C © D Clear Answer Mark For Review	
1	Q. No.33. The multiple access method used in GSM cellular technology	
1	A: FDMA & CDMA	
	B: CDMA & TDMA C: FDMA & TDMA	
	D: CDMA & CSMA	
⊕ A ⊕ B ⊕ i	C D Clear Answer Mark For Review	
	Q. No.34. In a data communications system, the information to be communicated is	
	A: Medium	
	B: Protocol	
	C: Message D: Transmission	
OA OB ®	© C © D [Clear Answer] [Mark For Review]	
1	Q. No.35. If the least significant bit of the first byte is 1, the Ethernet address is	
1		56
. 1	A: multimet	l.v.
0 A 0 B 8	C D Clear Answer Mark For Review.	
	Q. No.34. In a data communications system, the information to be communicated is	
	the	
l î	A: Medium	
	B: Protocol C: Message	
Į.	D: Transmission	
□ A ⊝ B ®	® C □ D Clear Answer Mark For Review	
	Q. No.35. If the least significant bit of the first byte is 1, the Ethernet address is	
	A: multicast	
	B: broadcast	
	C: unicast D: geocast	
○ A * B ○	© C © D Clear Answer Mark For Review	
	Q. No.36 is the combination of an IP address and a port number in	
	networking.	
1	A: transport address	
	N. Called Address	jota .

	Q. No.37. The error detection method which uses one's complement arithmetic is	
	A: Checksum B: CRC	
	C. Simple parity check	
	D: Two-dimensional parity check	
O A O B	C D Clear Answer UnMark	
	Q. No.38. The inter frame space, contention window, and acknowledgments are	
	used in which access method to avoid collisions	
	A: CSMA/CD	
	B: FDMA	
	C: CSMA/CA D: TDMA	
o A ⊕ B	® C □ D Clear Answer Mark For Review	
	Q. No.39. How many bits is the physical address used by Ethernet?	
	W. NV.39. From many one is the physical aduless used by contened?	
	A: 32-bit	
	B: 48-bit C: R4-bit	•
	2 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	12
	Q. No.39. How many bits is the physical address used by Ethernet?	
	A: 32-bit	
	B: 48-bit	
	C: 64-bit	
	D: 128-bit	
A⊕B	© C © D Clear Answer Mark For Review	
	Q. No.40. The headers are, when the data packet is forwarded from the	
	upper to the lower layers.	
	A: Rearranged	
	B: Removed	
	C: Added	. A
	D: Modified	
A⊕B	® C U D Clear Answer Mark For Review	
	Q. No.41. A central controller or hub is required in which type of topology?	10
	A: Mesh	
	B: Bus	
	C: Star D: Ring	
		•
new:2		
	1 se mounte	•
A · B	# C D D Clear Answer: Mark For Review	
	Q. No.41. A central controller or hub is required in which type of topology?	
	A: Mesh	
	B: Bus	
	C: Star D: Ring	
		1000
AURI	® C □ D Clear Answer Mark For Review	
A - 0	Q. No.42. Process is	
M - 0	Q. No.42, Process is A: program in High level language kept on disk	
N - D	A: program in High level language kept on disk B: contents of main memory	
n - 5	A: program in High level language kept on disk B: contents of main memory C: a program in execution D: a ich in expendence memory	
	A: program in High level language kept on disk B: contents of main memory C: a program in execution D: a job in secondary memory	
	A: program in High level language kept on disk B: contents of main memory C: a program in execution D: a job in secondary memory	
	A: program in High level language kept on disk B: contents of main memory C: a program in execution D: a job in secondary memory	
	A: program in High level language kept on disk B: contents of main memory C: a program in execution D: a job in secondary memory © C © D Clear Answer Mark For Review O. No.43. Which of the following describes the ability of an OS to support multiple,	

der tenvia		
1	U: a job in secondary memory	
A © B ® C	D Clear Answer Mark For Review	
- 1	0.44.49.99/4.478.478.478.478.478.478.478.478.478.47	
el	Q. No.43. Which of the following describes the ability of an OS to support multiple, concurrent paths of execution within a single process?	
1	A: Multithreading	
	B: Multiprocessing	
	C: Multitasking	
	D: Multiprogramming	
A ⊕ B ⊕ C	D Clear Answer Mark For Review	
	Q. No.44. What is not shared by threads?	
	A: Code	
	8: Data	
	C: Files	
d	D: Registers	
A @ B @ C	D Clear Answer Mark For Review	
	Q. No.45. High page faults leads to –	
	148	
	A: Swapping	₩.
OAOB (C 0 D Clear Answer Mark For Review	1
	Q. No.44. What is not shared by threads?	
794	The state of the s	
	A: Code	
	B: Data	
	C: Files D: Registers	
- 1	D. Registers	
A B B O	C D Clear Answer Mark For Review	
	Q. No.45. High page faults leads to	
- 1		
- 1	A: Swapping	
	B: Compaction G	
	C: Thrashing	
	D: External Fragmentation	
0 A 0 B * (C @ D Clear Answer Mark For Review	
T	Q. No.46. What is compaction?	
1	A: A technique for overcoming internal fragmentation	
	B: A paging technique	
	C: A technique for overcoming external fragmentation	
	C. A learninger for overconning external magnification	★

B: Compaction C: Thrashing D: External Fragmentation	
○ B ◎ C ○ D Clear Answer Mark For Review	
Q. No.46. What is compaction?	
A: A technique for overcoming internal fragmentation B: A paging technique C: A technique for overcoming external fragmentation D: A technique for overcoming fatal error	
◎ B ® C ◎ D Clear Answer Mark For Review	
Q. No.47. short term scheduler is also known as	J7 T 1
A: cpu scheduler B: job scheduler C: middle term scheduler D: none of these	
◎ B ◎ C ◎ D [Clear Answer] Mark For Review	
Q. No.48. Find the wrong statement about multilevel queue scheduling	
D: Scheduling must be done between the gueries	
D: Scheduling must be done between the queues A B C D Clear Answer Mark For Review	•
A © B © C © D Clear Answer Mark For Review Q. No.49. Accessing speed is higher for	*
A B C D Clear Answer Mark For Review	•
A © B © C © D Clear Answer Mark For Review Q. No.49. Accessing speed is higher for A: Solid-state disks B: Main memory	
A © B © C © D Clear Answer Mark For Review Q. No.49. Accessing speed is higher for A: Solid-state disks	
A © B © C © D Clear Answer Mark For Review Q. No.49. Accessing speed is higher for A: Solid-state disks B: Main memory C: Cache	
A © B © C © D Clear Answer Mark For Review Q. No.49. Accessing speed is higher for A: Solid-state disks B: Main memory C: Cache D: Registers	
A B C D Clear Answer Mark For Review Q. No.49. Accessing speed is higher for A: Solid-state disks B: Main memory C: Cache D: Registers A B C D Clear Answer Mark For Review	
A B C D Clear Answer Mark For Review Q. No.49. Accessing speed is higher for A: Solid-state disks B: Main memory C: Cache D: Registers A B C D Clear Answer Mark For Review Q. No.50. Virtual memory is A: extremely large main memory B: extremely large secondary memory C: Illusion of extremely large memory	