Befehl	op	funct3	funct7	LT	Zero	PCSrc	ResultSrc	MemWrite	ALUControl	ALUSrc	ImmSrc	RegWrite	SLInput	ShiftDirection	PCTargetSrc
srli	0010011	101	0000000	x	x	x	11	0	xxx	x	000	1	1	1	0
lui	0110111	-	-	x	x	x	11	0	xxx	x	110	1	0	0	0
blt	1100011	100	-	0 oder 1	x	LT	xx	0	001	0	xxx	0	x	x	0
jalr	1100111	000	-	x	x	1	10	0	xxx	x	000	1	x	x	1
											ShiftDirection (0->left, 1->right)				
Immediates															
ImmSrc	ImmExt	Instr. Type													
000	{20[instr[31]]}	I													
001	{{20{instr[31]]}	S													
010	{{19{instr[31]]}	B													
011	{{12{instr[31]]}	J													
110	{{12{instr[31:1	U													